Specifications for
Vector Signal Generator R&S SMIQ

Valid from 7/2002

ROHDE&SCHWARZ

Specifications are guaranteed under the following conditions: 30 minutes warmup time at ambient temperature, specified environmental conditions met, calibration cycle adhered to, and total calibration performed. Data designed „overrange" are not guaranteed. Data without tolerances: typical values. – In compliance with the 3GPP standard, chip rates are specified in Mcps (million chips per second), whereas bit rates, symbol rates and sample rates are specified in kbps (thousand bits per second) or ksps (thousand symbols/samples per second). Mcps, kbps and ksps are not SI units.

RF features

Frequency

Range	
R&S SMIQ02B	300 kHz to 2.2 GHz
R&S SMIQ03B	300 kHz to 3.3 GHz
R&S SMIQ04B	300 kHz to 4.4 GHz
R&S SMIQ06B	300 kHz to 6.4 GHz
Resolution (CW, analog modulation, attenuator mode AUTO)	0.1 Hz
Setting time to within <1×10^{-7} for f>450 MHz and <240 Hz for f<450 MHz after IEC/IEEE-bus delimiter with ALC MODE set to ON or ALC OFF MODE set to TABLE	
Normal operation	
f ≤ 3.3 GHz	<3 ms
f > 3.3 GHz	<3.3 ms
After trigger pulse in list mode	
f ≤ 3.3 GHz	<500 µs
f > 3.3 GHz	<700 µs
Fast restore mode	
f ≤ 3.3 GHz	<800 µs
f > 3.3 GHz	<1 ms
Phase offset	adjustable in steps of 0.1°

Reference frequency

	Standard	**Option R&S SM-B1**
Aging (after 30 days of operation)	1×10^{-6}/year	<1×10^{-9}/day
Temperature effect (0°C to 50°C)	2×10^{-6}	<5×10^{-8}
Warm-up time		≤15 min
Output for internal reference		
Frequency	10 MHz	
Level	8 dBm	
Source impedance	50 Ω	
Input for external reference		
Frequency	1 MHz to 16 MHz in 1 MHz steps	
Permissible frequency drift	3×10^{-6}	
Input level	0.1 V to 2 V rms	
Input impedance	200 Ω	
Electronic tuning (EXT. TUNE)	1×10^{-7}/V	values to standard,
Input voltage range	0 V to ±10 V	but with Adjustment
Input impedance	10 kΩ	State On

Level

Range	
R&S SMIQ02B/03B	–144 dBm to +13 dBm (PEP) [1]
R&S SMIQ04B/06B	–144 dBm to +10 dBm (PEP) [1]
Overranging without guarantee of specs	up to 16 dBm
Resolution (CW, FM, φM, attenuator mode AUTO)	0.1 dB or 0.01 dB

Total level uncertainty >–127 dBm [2) 12)], CW	
f ≤2.5 GHz	<0.5 dB
f >2.5 GHz to 4 GHz	<0.9 dB
f >4 GHz to 6.4 GHz	<1.2 dB
Output impedance	50 Ω

VSWR	f ≤2.2 GHz	2.2 GHz < f ≤ 6.4 GHz
max. level	<1.8	<2.0
≤–3 dBm	<1.5	<1.8

Setting time to within 0.1 dB from settled level after IEC/IEEE bus delimiter in CW, FM, φM	<25 ms with mechanical attenuator <2.5 ms without mechanical attenuator
Non-interrupting level setting	
FIXED mode	setting range >20 dB
ELECTRONIC mode	setting range >80 dB
Overload protection	protects the unit from externally applied RF power (from 50 Ω source) and DC voltage
Max. permissible RF power	50 W (R&S SMIQ02B/R&S SMIQ03B) 1 W (R&S SMIQ04B/R&S SMIQ06B)
Max. permissible DC voltage	35 V (R&S SMIQ02B/R&S SMIQ03B) 0 V (R&S SMIQ04B/R&S SMIQ06B)

Spectral purity [2)]

Spurious		
Harmonics	at levels ≤10 dBm (R&S SMIQ02B/03B) at levels ≤7 dBm (R&S SMIQ04B/06B)	<–30 dBc
Nonharmonics		
	CW, carrier offset >10 kHz	
	0.3 MHz to 450 MHz	<–74 dBc
	>450 MHz to 1500 MHz	<–80 dBc
	>1500 MHz to 3000 MHz	<–74 dBc
	>3000 MHz to 3300 MHz	<–60 dBc
	>3300 MHz	as with vector modulation
	Vector modulation,	
	carrier offset 10 kHz to < 300 MHz	
	0.3 MHz to 3300 MHz	<–70 dBc
	carrier offset ≥300 MHz	
	0.3 MHz to 3300 MHz	<–60 dBc
	carrier offset 10 kHz to < 900 MHz	
	>3300 MHz to 6000 MHz	<–64 dBc
	>6000 MHz	<–58 dBc
	carrier offset ≥900 MHz	
	>3300 MHz, ≥5 dBm	<–50 dBc
Broadband noise, CW, carrier offset >5 MHz, measurement bandwidth 1 Hz		
f >20 MHz to 450 MHz		<–136 dBc (–142 dBc typ.)
f >450 MHz to 3040 MHz		<–138 dBc (–144 dBc typ.)
f >3040 MHz to 3300 MHz		<–136 dBc (–142 dBc typ.)
f >3300 MHz to 6400 MHz		<–132 dBc (–138 dBc typ.)
Broadband noise, vector modulation,		
f >20 MHz, carrier offset >5 MHz to 3300 MHz		<–136 dBc (–140 dBc typ.)
f >20 MHz, carrier offset >3300 MHz to 6400 MHz		<–133 dBc (–137 dBc typ.)

SSB phase noise, carrier offset 20 kHz, measurement bandwidth 1 Hz	CW	Vector/dig. mod.
f = 20 MHz to 450 MHz	<–116 dBc	<–119 dBc
f = 1 GHz	<–126 dBc	<–123 dBc
f = 2 GHz	<–120 dBc	<–120 dBc
f = 3 GHz	<–116 dBc	<–116 dBc
f = 6 GHz	<–110 dBc	<–110 dBc

Residual FM, rms (f = 1 GHz) 0.3 kHz to 3 kHz (ITU-T) 0.02 kHz to 23 kHz	 <1 Hz <4 Hz
Residual AM, rms (0.02 kHz to 23 kHz)	<0.02%

Sweep

RF sweep, AF sweep Modes Sweep range Step width (lin) Step width (log)	digital sweep in discrete steps automatic, single shot, manual or external trigger, linear or logarithmic user-selectable user-selectable 0.01% to 100%
Level sweep Modes Sweep range Step width (log)	not available with vector or digital modulation automatic, single shot, manual or external trigger, logarithmic 0.1 dB to 20 dB 0.1 dB to 20 dB
Step time Resolution	3 ms to 5 s 0.1 ms
Markers	3, user-selectable
MARKER output signal	TTL level (HCT), selectable polarity
X output	0 V to 10 V
BLANK output signal	TTL level (HCT), selectable polarity

Internal modulation generator

Frequency range Resolution	0.1 Hz to 1 MHz 0.1 Hz
Frequency error	$<(1\times10^{-4}$ of setting + 0.012 Hz)
Frequency response up to 100 kHz	<0.4 dB
Frequency response up to 1 MHz	<2 dB
Distortion up to 100 kHz (R_L >200 Ω, peak level 1 V)	 <0.2%
Open-circuit voltage at LF socket Resolution Setting error at 1 kHz	1 mV to 4 V peak 1 mV 1% + 1 mV
Output impedance	approx. 10 Ω
Frequency setting time (after receiving last IEC/IEEE-bus character)	 <3 ms

Analog modulation

Vector modulation

Level accuracy with vector modulation, additional error with ALC OFF, relative to CW	<0.3 dB
Mode	external DC
I and Q modulation inputs	
Input impedance	50 Ω
VSWR (DC to 30 MHz)	<1.2
If R&S SMIQ is fitted with **two** fading simulators (R&S SMIQB14 and R&S SMIQB15) **and** a noise generator and distortion simulator (R&S SMIQB17), the VSWR of the I and Q inputs for vector modulation (DC to 30 MHz) degrades to	<1.25
Input voltage for full-scale input	$\sqrt{I^2 + Q^2}$ = 0.5 V (1 V EMF with 50 Ω source)
Static error vector [3]	
level ≤8 dBm PEP (R&S SMIQ02B/03B)	
level ≤5 dBm PEP (R&S SMIQ04B/06B)	
up to 3.3 GHz	
rms value	<0.5%
peak value	<1%
>3.3 GHz	
rms value	<1%
peak value	<2%
Modulation frequency response	
up to 5 MHz	<0.4 dB
up to 30 MHz	<3 dB
Carrier leakage at 0 V input voltage, referred to full-scale input [3]	<–45 dBc
I/Q impairments	
Carrier leakage	
Setting range	0% to 50%
Resolution	0.5%
I ≠ Q	
Setting range	–12% to +12%
Resolution	0.1%
Quadrature offset	
Setting range	–10° to +10°
Resolution	0.1°
Envelope control	
RF level can be controlled with an analog voltage of 0 V to 1 V via the POWER RAMP input	1 V: set level 0 V: maximum level attenuation
Input impedance	10 kΩ
Linear range	0 dB to –30 dB (–35 dB typ.)
Attenuation error at –20 dB	<0.5 dB
On/off ratio	>80 dB
Delay time	1 µs typ.
Rise/fall time (10% to 90%)	<1 µs

Amplitude modulation [2)]

Modes	internal, external AC/DC
Modulation depth	0% to 100%
Resolution	0.1%
Setting error at 1 kHz (m <80%)	<4% of reading +1%
AM distortion at 1 kHz	
m = 30%	<1%
m = 80%	<2%
Modulation frequency range, RF ≥5 MHz	DC to 50 kHz
for RF <5 MHz	DC to 3 kHz
Modulation frequency response	
20 Hz to 20 kHz	<3 dB
for RF <5 MHz, 20 Hz to 3 kHz	<3 dB
Incidental φM at 30% AM,	
AF = 1 kHz, peak value	<0.1 rad
Modulation input EXT1	
Input impedance	>100 kΩ
Input voltage for selected modulation depth	1 V peak
High/low indication (10 Hz to 50 kHz)	for inaccuracy >3%

Broadband amplitude modulation

Mode	external DC
Modulation frequency response	
up to 10 MHz	<1 dB
up to 30 MHz	<3 dB
Modulation input (broadband AM)	
Input impedance	50 Ω
Input voltage for 100% AM	0.25 V peak

Pulse modulation

Modes	external
On/off ratio	>80 dB
Rise/fall time (10%/90%)	30 ns typ.
Pulse repetition frequency	0 kHz to 1 MHz
Pulse delay	200 ns typ.
Modulation input PULSE	
Input signal	TTL level (HCT)
Input impedance	>10 kΩ

Frequency modulation with option R&S SM-B5

Modes	internal, external AC/DC, two-tone with two modulation channels FM1 and FM2
Max. deviation without I/Q modulation	
0.3 MHz to 450 MHz	2 MHz
>450 MHz to 750 MHz	0.5 MHz
>750 MHz to 1500 MHz	1 MHz
>1500 MHz to 3300 MHz	2 MHz
>3300 MHz to 6400 MHz	4 MHz

Max. deviation with I/Q modulation	
0.3 MHz to 750 MHz	2 MHz
>750 MHz to 1200 MHz	1 MHz
>1200 MHz to 3300 MHz	2 MHz
>3300 MHz to 6400 MHz	4 MHz
Resolution	<1%, min. 10 Hz
Setting error at AF = 1 kHz	<(3% of setting + 20 Hz)
FM distortion at AF = 1 kHz and half maximum deviation	<0.5%
Modulation frequency range	
with maximum deviation	DC to 500 kHz
at <25% of maximum deviation	DC to 2 MHz
Modulation frequency response	
10 Hz to 100 kHz	<0.5 dB
10 Hz to 2 MHz	<3 dB
Incidental AM at 40 kHz deviation, AF = 1 kHz, carrier frequency >5 MHz	<0.1%
Carrier frequency offset with FM	<0.01% of maximum deviation +1% of selected deviation
EXT1, EXT2 modulation inputs	
Input impedance	>100 kΩ
Input voltage for selected modulation depth	1 V peak
High/low indication (10 Hz to 100 kHz)	for inaccuracy >3%

Phase modulation with option R&S SM-B5[13)]

Modes	internal, external AC/DC, two-tone with two modulation channels PM1 and PM2
Max. deviation without I/Q modulation	
0.3 MHz to 450 MHz	20 rad
>450 MHz to 750 MHz	5 rad
>750 MHz to 1500 MHz	10 rad
>1500 MHz to 3300 MHz	20 rad
>3300 MHz to 6400 MHz	40 rad
Max. deviation with I/Q modulation	
0.3 MHz to 750 MHz	20 rad
>750 MHz to 1200 MHz	10 rad
>1200 MHz to 3300 MHz	20 rad
>3300 MHz to 6400 MHz	40 rad
Resolution	<1%, min. 0.001 rad
Setting error at AF = 1 kHz	<3% of reading + 0.01 rad
Distortion at AF = 1 kHz and half maximum deviation	<1%
Modulation frequency range	DC to 100 kHz
Modulation frequency response	
10 Hz to 100 kHz	<0.8 dB
EXT1, EXT2 modulation inputs	
Input impedance	>100 kΩ
Input voltage for selected modulation depth	1 V peak
High/low indication (10 Hz to 100 kHz)	for inaccuracy >3%

Digital modulation

Digital modulation with optional Modulation Coder R&S SMIQB20

Modes	internal, external serial, external parallel
Predefined modulation settings	APCO C4FM, APCO CQPSK, CDPD, CT2, DECT, GSM, IRIDIUM, NADC, PDC, PHS, TETRA, TFTS, PWT, ICO BPSK, ICO GMSK, ICO QPSK, GSM EDGE, CDMA IS-95, WCDMA, QPSK
Internal PRBS	selectable lengths: 2^9-1, $2^{15}-1$, $2^{16}-1$, $2^{20}-1$, $2^{21}-1$ and $2^{23}-1$
I/Q bandwidth	12 MHz
Modulation specifications apply at levels ≤8 dBm (PEP) with R&S SMIQ02B/03B and at levels ≤5 dBm (PEP) with R&S SMIQ04B/06B	
Total level uncertainty at levels >–127 dBm with digital modulation, crest factor <20 dB [2) 3)]	
f ≤2,5 GHz	<0.7 dB
f >2,5 GHz to 4 GHz	<1.2 dB
f >4 GHz	<1.5 dB
For best short time repeatability use ALL OFF mode table	
Clock generation	
Clock mode	internal or external
Resolution	0.001 Hz
Error	$<2^{-42}$, related to reference frequency
Inputs	DATA, BIT CLOCK, SYMBOL CLOCK, PAR DATA
Serial data are taken from BNC connectors, parallel data (symbols) from rear PAR DATA connector. Parallel symbols may contain 1 to 8 bits and read in using an internal or external clock signal	
Trigger threshold	–2.5 V to +2.5 V, selectable, resolution 0.01 V
Input impedance	1 kΩ to ground, 50 Ω to ground
Max. data rate, serial	30 MHz, 50 MHz typ.
Max. symbol rate, parallel	18 MHz
Outputs	DATA, BIT CLOCK, SYMBOL CLOCK, PAR DATA, (all TTL levels)
I and Q baseband signals, output voltage, EMF, peak value	$\sqrt{I^2+Q^2}=1\,V$
Power ramp	
Output voltage	0 V to 1 V
Output impedance	10 Ω
Level attenuation	
via LEV ATT input	
Range	0 dB to 70 dB
Additional level error caused by attenuation [3)]	<1 dB (up to 35 dB), <1.5 dB (up to 70 dB)
Envelope control	
Modes	
Analog	External via POWER RAMP input (for data see vector modulation above). With an internal power ramp, the connector serves as an output.
Digital	Internal or external via BURST GATE input/output (PAR DATA connector). The BURST GATE input triggers a power ramp (TTL levels). The low/high transition starts the ramp function from blanking level to maximum level, the high/low transition from maximum level to blanking level. With an internal power ramp, the connector serves as an output.
Operating range	1 kHz to 2.5 MHz
Rise/fall time	
Setting range	0.25 symbols to 32 symbols
Resolution	1/4 symbol
Minimum time	1 µs
Modulation modes	ASK, FSK, GMSK, PSK, QAM

Baseband filter Each filter can be used with any type of modulation. Exception: GMSK/GFSK only with Gaussian baseband filter	√cos (α = 0.1 to 0.99), cos (α = 0.1 to 0.99), resolution 0.01 Gauss, B x T = 0.15 to 2.5, resolution 0.01 GaussLin, B x T = 0.3 Bessel, B x T = 1.25 and 2.5/ IS-95 with or without equalizer / special filter for WCDMA, APCO C4FM / special filter for TETRA / split phase, B x T = 0.15 to 2.5 / rectangular
Filter modes	low EVM: for minimum error vector low ACP: for minimum adjacent-channel power
ASK	
Symbol rate	100 Hz to 18 MHz [11], max. 5 MHz
Modulation depth	0% to 100%
FSK	
Modulation modes	2FSK, 4FSK, 4FSK APCO, GFSK
Symbol rate	100 Hz to 7.5 MHz [11]
Shift	(0.1 to 100) x f_{symb} , max. 5 MHz
Resolution	<0.5%
Deviation error, rms value for shift 200 Hz to 650 kHz, symbol rate <1.3 Msymbol/s, filter √cos or cos with α = 0.25 to 0.7 or Gauss with B x T = 0.2 to 0.7	<1.3%
GMSK	
Bit rate	100 Hz to 7.5 MHz [11]
Modulation phase error with PRBS data,	
up to 1 Mbit/s, rms value	<1°
Peak value	<3°
PSK	
Modulation modes	BPSK, QPSK, OQPSK, QPSK (IS-95), OQPSK (IS-95), QPSK (ICO), QPSK (INMARSAT), π/4DQPSK, π/4QPSK, 8PSK, 8PSK EDGE
Symbol rate	100 Hz to 18 MHz [11]
Error vector magnitude, rms for filter √cos, cos, IS-95, WCDMA	
up to 200 ksymbol/s, α ≥0.25	<1.2%
up to 1.5 Msymbol/s, α ≥0.25	<2%
up to 3 Msymbol/s, α ≥0.25	<3%
>3 Msymbol/s, α ≥0.25	typ. <3%
TETRA ACP	
f = (380 to 470) MHz, (915 to 988) MHz, level ≤8 dBm PEP, low distortion output mode	
Offset 25 kHz	≤–71 dB, –74 dB typ.
Offset 50 kHz	≤–76 dB, –80 dB typ.
QAM	
Modulation modes	16QAM, 32QAM, 64QAM, 256QAM
Symbol rate	100 Hz to 18 MHz [11]
Error vector magnitude, rms with 16QAM for filter √cos, cos, IS-95, WCDMA	<2%
up to 1 Msymbol/s, α ≥0.25	<3%
up to 3 Msymbol/s, α ≥0.25	<3% typ.
>3 Msymbol/s, α ≥0.25	

User mapping	via IEEE bus with software User Mod
Modulation modes	PSK, QAM, FSK
Modulation symbols	1 to 8 bit per symbol
Coding	differential, phase offset (PSK)
Range of symbol rate	like PSK, QAM and FSK
User baseband filter	via IEEE bus with software User Mod
Impulse length	8 or 16 symbols long
Oversampling	3 to 32
Modulation coding	differential coding, differential and Gray, GSM, NADC, TETRA, TFTS, PDC, PHS, differential phase coding, APCO25, PWT, INMARSAT, VDL

Modulation with coding

The table below shows the possible combinations of modulation with coding (X = combination possible).

	Coding off	Differential	Differential phase	Differential + Gray	GSM	NADC, PDC,PHS, TETRA,APCO25, PWT	TFTS/ TETRA	INMARSAT	VDL
ASK	X	X		X					
BPSK	X	X		X					
QPSK	X	X		X				X	
QPSK (IS-95)	X	X		X				X	
INMARSAT	X	X		X				X	
QPSK ICO	X	X		X				X	
OQPSK	X	X		X				X	
OQPSK (IS-95)	X	X		X				X	
π/4 QPSK	X								
π/4 DQPSK	X					X	X		
8PSK	X	X		X					X
8PSK_EDGE	X	X		X					X
2FSK	X	X		X	X				
4FSK, 4FSK APCO	X	X		X					
GFSK	X	X		X	X				
GMSK	X	X		X	X				
16QAM	X	X	X	X					
32QAM	X	X	X	X					
64QAM	X	X	X	X					
256QAM	X		X						

Data generator (option R&S SMIQB11)

Programmable data memory for modulation data, envelope-control and trigger signals. The data generator can be operated only in conjunction with the optional modulation coder (R&S SMIQB20).

Memory capacity	15 Mbit
Max. symbol rate	8.5 MHz
Modes	automatically repeating, single shot, manually or externally triggered

Inputs	
Trigger input TRIGIN	for starting the data sequences in the data memory
Trigger frequency	<10 kHz
Selectable trigger delay	0 to 2^{16}–1 symbols
Selectable trigger suppression	0 to 2^{26}–1 symbols after trigger
Switching threshold	–2.5 V to 2.5 V, selectable, resolution 0.1 V
Input impedance	1 kΩ to ground, 50 Ω to ground
Required pulse width	>50 ns
Outputs	
DATA modulation data	see data under "Digital Modulation"
BURST GATE, LEV ATT	control signals for envelope control and level attenuation
CW	control signal for switching off digital modulation
TRIGOUT 1, TRIGOUT 2	user-programmable trigger signals
TRIGOUT 3	trigger signal on event
HOP	control signal for frequency hopping in LIST MODE

Memory extension (option R&S SMIQB12)

The data generator memory can be extended to max. 79 Mbit by fitting up to two options R&S SMIQB12.

Memory capacity	32 Mbit

Digital standards with options R&S SMIQB20 and R&S SMIQB11

Modulation and ACP specifications apply at the respective standard frequency ranges and at levels
≤8 dBm PEP with R&S SMIQ02B/03B
≤5 dBm PEP with R&S SMIQ04B/06B

Data sources	
with TDMA	internal, external via RS232 interface (SERDATA)
with CDMA	internal
Digital standard GSM / EDGE	**to GSM standard**
Frequency	
Standard	880 MHz to 960 MHz/1710 MHz to 2000 MHz
Range	same as R&S SMIQ
Modulation	GMSK or 8PSK EDGE (8PSK with 3π/8 rotation)
Symbol rate	
Standard	270 833 Hz
Range	100 Hz to 300 kHz
Baseband filter (GMSK)	
Standard	Gauss, BxT = 0.3
Range	Gauss, BxT = 0.2 to 0.7
Baseband filter (8PSK EDGE)	Gauss Linear
Frame structure	1 to 8 slots user-definable any burst types can be combined, incl. GSM and EDGE mixed
Burst types	NORMAL, DUMMY, ALL DATA, NORMAL EDGE
Burst on/off ratio	>80 dB
Burst rise/fall time	corresponding to 3 symbols, <18 µs
Slot attenuation, selectable	0 dB to 70 dB
Internal modulation data	PRBS, 2^9–1, 2^{11}–1, 2^{15}–1, 2^{16}–1, 2^{20}–1, 2^{21}–1, 2^{23}–1, programmable data memory
External serial modulation data	asynchronous via RS232 interface (SERDATA)

GMSK	
Phase error, rms value	<1°, 0.3° typ.
Phase error, peak value	<3°, 1° typ.
8PSK EDGE	
Error vector magnitude, rms	<1.2%, 0.6% typ.
Power density spectrum, typ. values (standard, resolution bandwidth 30 kHz, without power ramping)	
Offset 200 kHz	–34 dB
Offset 400 kHz	–70 dB
Offset 600 kHz	–78 dB
Digital standard DECT	**to ETS300175-2 and ETS300176-1**
Frequency	
Standard	1880 MHz to 1900 MHz
Range	same as R&S SMIQ
Modulation	GFSK (standard), π/4 DQPSK
Deviation with GFSK	
Standard	288 kHz
Range	(0.1 to 1) x f_{symb}
Symbol rate	
Standard	1152000 Hz
Range with GFSK	100 Hz to 1200 kHz
Range with π/4 DQPSK	100 Hz to 1200 kHz
Baseband filter	
Standard	Gauss, B x T = 0.5
Range	Gauss, B x T = 0.2 to 0.7 √cos (α = 0.2 to 0.7), cos (α = 0.2 to 0.7)
Frame structure	1 to 12 slots of 24 slots user-definable
Slot types	FULL (basic), DOUBLE (high capacity), ALL DATA
Burst on/off ratio	>80 dB
Burst rise/fall time	corresponding to 2 symbols, <10 µs
Slot attenuation, selectable	0 dB to 70 dB
Internal modulation data	PRBS, 2^9–1, 2^{15}–1, 2^{16}–1, 2^{20}–1, 2^{21}–1, 2^{23}–1, programmable data memory
Deviation error, rms	<1.3%
Special functions	
Timing adjustment simulation	lengthening (+) or shortening (–) of every 35th frame
Range	–4 bit to +4 bit (resolution 1 bit)
Jitter simulation	time lead for even-numbered frames
Range	0 bit to +4 bit (resolution 1 bit)
Slot timing shift	time shifting of a single slot
Range	–9 bit to +9 bit (resolution 1 bit)
Selectable preamble	normal or prolonged
Response during ramp-up	modulated or CW
Digital standard NADC	**to IS-54 and IS-136**
Frequency	
Standard	824 MHz to 894 MHz/1850 MHz to 2000 MHz
Range	same as R&S SMIQ
Modulation	π/4 DQPSK
Symbol rate	
Standard	24.300 kHz
Range	100 Hz to 200 kHz

Baseband filter	
Standard	√cos (α=0.35), cos (α=0.35)
Range	√cos (α=0.2 to 0.7), cos (α=0.2 to 0.7)
Frame structure	1 to 6 slots user-definable for uplink and downlink
Burst types	Up/Down TCH, ALL DATA, Up SHORT
Burst on/off ratio	>80 dB
Burst rise/fall time	corresponding to 3 symbols, <123.4 µs
Slot attenuation, selectable	0 dB to 70 dB
Internal modulation data	PRBS, 2^9–1, 2^{11}–1, 2^{15}–1, 2^{16}–1, 2^{20}–1, 2^{21}–1, 2^{23}–1, programmable data memory
External serial modulation data	asynchronous via RS232 interface (SERDATA)
Error vector magnitude, rms	<1.2%, 0.4% typ.
Adjacent-channel power, typ. values, without power ramping	
Offset 30 kHz	–35 dBc
Offset 60 kHz	–75 dBc
Offset 90 kHz	–78 dBc
Digital standard PDC	**to RCR STD-27**
Frequency	810 MHz to 826 MHz/940 MHz to 956MHz /1429 MHz to 1453 MHz/1477 MHz to 1501 MHz
Standard	same as R&S SMIQ
Range	
Modulation	π/4 DQPSK
Symbol rate	
Standard	21 kHz
Range	100 Hz to 200 kHz
Baseband filter	
Standard	√cos (α=0.5), cos (α=0.5)
Range	√cos (α=0.2 to 0.7), cos (α=0.2 to 0.7)
Frame structure	1 to 6 slots user-definable for uplink and downlink
Burst types	TCH, SYNC, VOX, ALL DATA
Burst on/off ratio	>80 dB
Burst rise/fall time	corresponding to 2 symbols, <95.2 µs
Slot attenuation, selectable	0 dB to 70 dB
Internal modulation data	PRBS, 2^9–1, 2^{11}–1, 2^{15}–1, 2^{16}–1, 2^{20}–1, 2^{21}–1, 2^{23}–1, programmable data memory
External serial modulation data	asynchronous via RS232 interface (SERDATA)
Error vector magnitude, rms	<1.2%, 0.4% typ.
Adjacent-channel power, typ. values, without power ramping	
Offset 50 kHz	–74 dBc
Offset 100 kHz	–78 dBc
Digital standard PHS	to RCR STD-28
Frequency	
Standard	1895.0 MHz to 1918.1 MHz
Range	same as R&S SMIQ
Modulation	π/4 DQPSK
Symbol rate	
Standard	192 kHz
Range	100 Hz to 200 kHz
Baseband filter	
Standard	√cos (α=0.5), cos (α=0.5)
Range	√cos (α = 0.2 to 0.7), cos (α = 0.2 to 0.7)

Frame structure	1 to 8 slots user-definable
Burst types	TCH (32 kbit and 16 kbit channel), SYNC, VOX, ALL DATA
Burst on/off ratio	>80 dB
Burst rise/fall time	corresponding to 2 symbols, <13 µs
Slot attenuation, selectable	0 dB to 70 dB
Internal modulation data	PRBS, 2^9–1, 2^{11}–1, 2^{15}–1, 2^{16}–1, 2^{20}–1, 2^{21}–1, 2^{23}–1, programmable data memory
External serial modulation data	asynchronous via RS232 interface (SERDATA)
Error vector magnitude, rms	<1.2%, 0.4% typ.
Adjacent-channel power, typ. values, without power ramping)	
Offset 600 kHz	–74 dBc
Offset 900 kHz	–76 dBc

Digital standard IS-95 CDMA with option R&S SMIQB42

Modulation and ACP specifications apply at the respective standard frequency ranges and at levels
≤8 dBm PEP with R&S SMIQ02B/03B
≤5 dBm PEP with R&S SMIQ04B/06B

To TIA standard IS-95A and J-STD-008	
Frequency	
Standard	824 MHz to 894 MHz, 1850 MHz to 2000 MHz
Range	same as R&S SMIQ
Modulation	QPSK, OQPSK
Chip rate	
Standard	1.2288 MHz
Range	0.1 Mcps to 7 Mcps
Reverse link coded	0.1 Mcps to 1.3 Mcps
Sequence length	
Forward link	1 superframe (80 ms)
Reverse link	1 superframe (80 ms) if user-definable data lists are used: calculation in real time, ie unlimited sequence length
Reverse link coded	calculation in real time, ie unlimited sequence length
Baseband filter	
Standard	IS-95 with or without equalizer
Other filters	$\sqrt{}$cos (α=0.2 to 0.7), cos (α=0.2 to 0.7)
Forward link, mode 18	
Number of code channels	1 to 18
Walsh code selectable	0 to 63
Code channel power	0.0 dB to –30 dB, 4 user-definable levels
Forward link, mode 64	
Number of code channels	1 to 64
Code channel power	0.0 dB to –30 dB, 2 user-definable levels
Reverse link	full-rate mode, half-rate mode with random power gating
Reverse link coded	incl. frame quality indicator, convolutional encoder, block interleaver traffic channel, 9600/4800/2400/1200 bps traffic channel, 14400/7200/3600/1800 bps access channel, 4800 bps

Internal modulation data	
Forward link, 19200 bit/s	PRBS, 0 sequence, 1 sequence, 01 alternating, different for each code channel
Reverse link, 28800 bit/s	PRBS, 0 sequence, 1 sequence, 01 alternating, programmable data memory
Reverse link coded	PRBS, 2^9-1, $2^{15}-1$, $2^{16}-1$, $2^{20}-1$, $2^{21}-1$, $2^{23}-1$, programmable data memory
Synchronization signals (chip rate 1.2288 Mcps)	chip clock, input and output, 2 outputs for 80 ms, 80/3 ms, 20 ms, 2 s clock, trigger input
Modulation accuracy ρ	>0.9996
Adjacent-channel power ratio at 30 kHz bandwidth	
Reverse link	
Offset 885 kHz	–77 dBc typ.
Offset 1.25 MHz	–83 dBc typ.
Offset 1.98 MHz	–84 dBc typ.
With option R&S SMIQB47, IQ filter 850 kHz	
Offset 885 kHz	<–78 dBc, –82 dBc typ.
Offset 1.25 MHz	<–83 dBc, –87 dBc typ.
Offset 1.98 MHz	<–85 dBc, –89 dBc typ.
9 channels forward link	
Offset 885 kHz	–77 dBc typ.
Offset 1.25 MHz	–79 dBc typ.
Offset 1.98 MHz	–80 dBc typ.
With option R&S SMIQB47, IQ filter 850 kHz	
Offset 885 kHz	<–74 dBc, –78 dBc typ.
Offset 1.25 MHz	<–80 dBc, –84 dBc typ.
Offset 1.98 MHz	<–83 dBc, –86 dBc typ.

Digital standard WCDMA with option R&S SMIQB43

To NTT DoCoMo 1.0 and ARIB standard 0.0

Modulation and ACP specifications apply at the respective standard frequency ranges and at levels
≤8 dBm PEP with R&S SMIQ02B/03B
≤5 dBm PEP with R&S SMIQ04B/06B

Frequency	
Standard	1800 MHz to 2200 MHz
Range	same as R&S SMIQ
Modulation	QPSK, OQPSK
General settings	
Chip rate	
Standard	4.096 Mcps
Range	0.1 Mcps to 7 Mcps
Link direction	uplink and downlink
Sequence length	45 frames without option R&S SMIQB12 150 frames with 1 option R&S SMIQB12 240 frames with 2 options R&S SMIQB12
Baseband filter	
Standard	WCDMA 0.22
Other filters	$\sqrt{\cos}$ (α = 0.1 to 0.7), cos (α = 0.1 to 0.7)

Code channels and spreading	
Number	mode 4: 4 channels with different power mode 8: 8 channels, 1 channel with different power and 7 channels with equal power mode 15: 15 channels with equal power
Multicode operation	yes
Code channel power	0.0 dB to –30 dB
Short code	selectable for each code channel
Range	0 to 127
LMS	1 to FF hex
Long code	selectable for each code channel
Initial value uplink	0 to 1FFFFFFFFF hex
Initial value downlink	0 to 3FFFF hex
Time offset	0 to 40959 chips (1 radio frame)
Physical channel with frame structure	
Link direction	downlink, uplink, uplink IQ-multiplexed to ARIB 0.0
Downlink channels	perch 1, common control 64 ksps sample rate, dedicated channel with 32, 64, 128, 256, 512, 1024 ksps sample rate
Uplink channels	common control 64 ksps sample rate, dedicated channel with 32, 64, 128, 256, 512, 1024 ksps sample rate
Uplink channels (ARIB)	dedicated control channel with 16 ksps sample rate, dedicated data channel with 16, 32, 64, 128, 256, 512, 1024 ksps sample rate
Data offset	time offset, separately adjustable for each code channel
Range offset	0 to 1 radio frame
Resolution offset	1 symbol
Internal modulation data	
DATA field	PRBS, 2^9–1, 2^{11}–1, 2^{15}–1, 2^{16}–1 programmable data memory
TPC field	00, 11, alternating, programmable data memory
Synchronization signals	chip clock, input and output outputs for slot, frame clock or marker for repetition of chip sequence trigger input
Error vector magnitude, rms	<2%, 1.5% typ.
with option R&S SMIQB47, IQ filter 2.5 MHz	<3%, 1.8% typ.
Adjacent-channel power, 1 DTCH	
Offset 5 MHz, low distortion output mode	–67 dBc typ. [8]
Offset 10 MHz, low noise output mode	–70 dBc typ. [8]
With option R&S SMIQB47, IQ filter 2.5 MHz	
Offset 5 MHz, low distortion output mode	<–65 dBc, –70 dBc typ. [8]
Offset 10 MHz, low noise output mode	<–71 dBc, –74 dBc typ. [8]

Digital standard WCDMA 3GPP (FDD) with option R&S SMIQB45

To 3GPP standard 4.1.0 (FDD)

3GPP (FDD) version	4.1.0 to 3GPP technical specifications TS25.211, TS25.213, TS25.141, TS25.101 and TS25.104
Frequency	
Standard	1800 MHz to 2200 MHz
Range	same as R&S SMIQ
General settings	
Chip rate	
Standard	3.840 Mcps,
Range	1 Mcps to 5 Mcps

Link direction	uplink (reverse link) and downlink (forward link)
Sequence length	1 to 13 frames
Baseband filter	
Standard	√cos, α = 0.22
Other filters	√cos, (α = 0.1 to 0.99), cos (α = 0.1 to 0.99), user filter
Clipping level	Setting of clipping value relative to highest peak in percent. Clipping takes place prior to baseband filtering and reduces the crest factor. The range is 1 to 100 %.
Code channels	
Downlink	up to 512 data channels (plus special channels) divided among up to four base stations (BS) with 128 code channels each
Uplink	up to four mobile stations (MS) each operating in one of modes PRACH only, PCPCH only, DPCCH + DPDCHs
Physical channels in downlink	
P-CPICH	Primary Common Pilot Channel
Symbol rate	15 ksps, fixed
Channelization code	0, fixed
Slot structure	predefined symbols
S-CPICH	Secondary Common Pilot Channel
Symbol rate	15 ksps, fixed
Channelization code	0 to 255
Slot structure	predefined symbols
P-SCH	Primary Sync Channel
Symbol rate	15 ksps, fixed
Slot structure	synchronization code (SC)
S-SCH	Secondary Sync Channel
Symbol rate	15 ksps, fixed
Slot structure	synchronization code (SC)
P-CCPCH	Primary Common Control Physical Channel
Symbol rate	15 ksps, fixed
Channelization code	1, fixed
Slot structure	data
S-CCPCH	Secondary Common Control Physical Channel
Symbol rate	15, 30, 60, 120, 240, 480, 960 ksps
Channelization code	depending on symbol rate, 0 to max. 255
Slot structure	data, TFCI, pilot
PICH	Page Indication Channel
Symbol rate	15 ksps, fixed
Channelization code	0 to 255
Number of PIs per frame	18, 36, 72, 144
Slot structure	page indicator bits, not used bits
AP-AICH	Access Preamble Acquisition Indication Channel
Symbol rate	15 ksps, fixed
Channelization code	0 to 255
Slot structure	acquisition indicators, empty symbols

AICH	Acquisition Indication Channel
Symbol rate	15 ksps, fixed
Channelization code	0 to 255
Slot structure	acquisition indicators, empty symbols
PDSCH	Physical Downlink Shared Channel
Symbol rate	15, 30, 60, 120, 240, 480, 960 ksps
Channelization code	depending on symbol rate, 0 to max. 255
Slot structure	data
DL-DPCCH	Dedicated Physical Control Channel
Symbol rate	7.5 ksps, fixed
Channelization code	0 to 511
Slot structure	TPC, pilot
DPCH	Dedicated Physical Channel
Symbol rate	7.5, 15, 30, 60, 120, 240, 480, 960 ksps
Channelization code	depending on symbol rate, 0 to max. 511
Slot structure	data 1, TPC, TFCI, data 2, pilot
Physical channels in uplink	
PRACH	Physical Random Access Channel
Symbol rate	15, 30, 60, 120 ksps
Frame structure	preamble(s), message part consisting of data and control section
Preamble part power	–60 dB to 0 dB
Data part power	–60 dB to 0 dB
Control part power	–60 dB to 0 dB
Preamble repetition	1 to 10
Signature	0 to 15
Access slot	0 to 14
Message part length	1 or 2 frames
TFCI	0 to 1023
User data	PRBS: PN9, PN11, PN15, PN16 all 0, all 1 and bit pattern (max. 24 bit long)
PCPCH	Physical Common Packet Channel
Symbol rate	15, 30, 60, 120, 240, 480, 960 ksps
Frame structure	access preamble(s), collision detection preamble, power control preamble, message part consisting of data and control section
Preamble part power	–60 dB to 0 dB
Data part power	–60 dB to 0 dB
Control part power	–60 dB to 0 dB
Preamble power step	0 dB to 10 dB
Preamble repetition	1 to 10
Signature	0 to 15
Access slot	0 to 14
Message part length	1 to 10 frames
Power control preamble length	0 or 8 slots
FBI state	OFF/1 bit/2 bit
FBI pattern	all 0, all 1 and bit pattern (max. 24 bit long)
User data	PRBS: PN9, PN11, PN15, PN16 all 0, all 1 and bit pattern (max. 24 bit long)
DPCCH	Dedicated Physical Control Channel
Symbol rate	15 ksps, fixed
Channelization code	0, fixed
DL–UL timing offset	1024 chips, fixed
Slot format	0 to 5
FBI state	OFF/1 bit/2 bit
FBI pattern	all 0, all 1 and bit pattern (max. 24 bit long)
TFCI state	OFF/ON
TFCI	0 to 1023
Use TPC for dynamic output	OFF/ON
Power control	if this function is active, the TPC pattern is used to vary the transmit power of the MS code channels versus time
Output power control step	–10 dB to +10 dB

DPDCH	Dedicated Physical Data Channel
Overall symbol rate	overall data rate of all uplink DPDCHs 15, 30, 60, 120, 240, 480, 960, 2 x 960, 3 x 960, 4 x 960, 5 x 960, 6 x 960 ksps
Active DPDCHs	1 to 6, depending on overall symbol rate
Symbol rate	fixed for active DPDCHs, depending on overall symbol rate
Channelization code	fixed for active DPDCHs, depending on overall symbol rate
Channel power	–60 dB to 0 dB for all DPDCHs
User data	PRBS: PN9, PN11, PN15, PN16 all 0, all 1 and bit pattern (max. 24 bit long)
Parameters for each base station (BS)	
State	OFF/ON
2nd search code group	0 to 63 (depending on scrambling code)
Scrambling code	0 to 5FFFF hex or off
TFCI state	OFF/ON
TFCI	0 to 1023
TPC pattern readout mode	use of TPC pattern: continuous, single + all 0, single + all 1, single + alternating 01, single + alternating 10
Use TPC for dynamic output power control	OFF/ON if this function is active, the TPC pattern is used to vary the transmit power of the code channels versus time
Output power control step	–10 dB to +10 dB
Transmit diversity	OFF/antenna 1/antenna 2 if this function is active, the output signal for antenna 1 or antenna 2 can be generated as defined in the standard
Parameter for each mobile station (MS)	
State	OFF/ON
Mode	PRACH only, PCPCH only, DPCCH + DPDCHs
Scrambling code	0 to FF FFFF hex
Scrambling code mode	long, short, off
TPC pattern	all 0, all 1 and bit pattern (max. 24 bit long)
TPC pattern readout mode	use of TPC pattern: continuous, single + all 0, single + all 1, single + alternating 01, single + alternating 10
Parameters for each downlink code channel, independently selectable	
State	OFF/ON
Symbol rate	between 7.5 ksps and 960 ksps, depending on type of physical channel
Channelization code	range 0 to max. 511, depending on symbol rate and type of physical channel
Power	–60 dB to 0 dB
User data	PRBS: PN9, PN11, PN15, PN16 all 0, all 1 and bit pattern (max. 24 bit long)
Timing offset	separately adjustable for each code channel 0 to 150 (in units of 256 chips)
Pilot length	2, 4, 8, 16 bit depending on symbol rate
TPC pattern	all 0, all 1 and bit pattern (max. 24 bit long)
Multicode state	OFF/ON

Assistant functions to facilitate operation	
Test models	
Downlink	test model 1 with 16/32/64 channels test model 2 test model 3 with 16/32 channels test model 4
Uplink (not standardized)	DPCCH + 1 DPDCH at 60 ksps sample rate DPCCH + 1 DPDCH at 960 ksps sample rate
Parameterizable predefined settings	generation of complex signal scenarios in downlink with parameterizable default settings selectable parameters: use and symbol rate of special channels (for synchronization of mobile), number and symbol rate of data channels, crest factor: minimal/average/worst
Multichannel edit	common configuration of data channels of BS channel table; selectable parameters, partly with start value and step size: range of data channels to be set, symbol rate, channelization code with step size, channel power with step size, data, TPC, timing offset with step size, multicode state, state
Copy BS/MS	adopting the configuration of a BS for another BS/MS for the definition of multi-BS/MS scenarios or BS signals with more than 128 channels parameters: source and destination of copying, channelization code offset for simple definition of BS signals with more than 128 channels and continuous channelization codes
Resolve domain conflicts	elimination of code channel overlapping in code domain (domain conflicts) occurring in a BS/MS
Graphic displays	
Domain conflicts	Display of domain conflicts (overlapping of code channels in code domain) in the lines concerned of the channel tables. The code domain occupied by the code channels involved in the conflict can also be displayed.
Code domain	Display of code domain occupied by current BS. Domain areas in which conflicts occur are highlighted. The distribution of code channels in the code domain as well as channel powers are shown qualitatively.
Channel graph	Display of all active channels of a BS versus the channel table index. The powers of the code channels are shown qualitatively.
CCDF	Display of complementary cumulative distribution function of current signal. This function gives the probability of the magnitudes of complex IQ samples exceeding a predefined threshold. Together with the current CCDF, the CCDFs of the two 3GPP signals last generated can be displayed to observe the effect of parameter changes. The crest factor of the signal can be seen in the CCDF.
Constellation diagram	Display of constellation diagram versus IQ samples of current 3GPP signal. This diagram allows qualitative assessment of channel configuration, channel power ratios, and the effect of parameters such as data and data offset.

Adjacent-channel power, 1 DPCH (crest factor=5,4 dB)	
Chip rate 3.84 MHz	
Without option R&S SMIQB47	
Offset 5 MHz, low distortion output mode	–67 dBc typ.[8]
Offset 10 MHz, low noise output mode	–70 dBc typ. [8]
With option R&S SMIQB47, IQ filter 2.5 MHz	
Offset 5 MHz, low distortion output mode	<–65 dBc, –70 dBc typ.[8]
Offset 10 MHz, low noise output mode	<–71 dBc, –74 dBc typ.[8]
Adjacent-channel power, test model 1, 64 DPCH (crest factor=10,6 dB)	
Without option R&S SMIQB47	
Offset 5 MHz, low distortion output mode	–64 dBc typ.[8]
Offset 10 MHz, low noise output mode	–67 dBc typ.[8]
With option R&S SMIQB47, IQ filter 2.5 MHz	
Offset 5 MHz, low distortion output mode	<–64 dBc, –68 dBc typ.[8]
Offset 10 MHz, low noise output mode	<–67 dBc, –70 dBc typ.[8]

Enhanced functions for digital standard WCDMA 3GPP (FDD) with option R&S SMIQB48

3GPP (FDD) version	4.1.0 to 3GPP technical specifications TS25.101, TS25.104, TS25.141, TS25.211 and TS25.213

Option R&S SMIQB45 WCDMA 3GPP is extended by the following functions:

Enhanced Channels

Channels of WCDMA system in R&S SMIQ that offer enhanced functionality compared with standard channels of option R&S SMIQB45.
Can be used in downlink for max. four DPCHs and in uplink for one DPCCH and max. six DPDCHs.
All DPCHs or DPDCHs have the same symbol rate.
Enhanced functions at a glance:

- Sequences of up to 1022 frames
- Realtime BCH with incrementing SFN
- Data lists for data fields and TPC field
- External power control
- Channel coding
- Bit error insertion
- Block error insertion
- Orthogonal channel noise simulation (OCNS)
- Additional mobile stations

Sequences of up to 1022 frames	generation of WCDMA signals with length of max. 256 frames with four active enhanced channels and max. 1022 frames with one active enhanced channel
Applications	– continuous measurement of physical bit error rate (without channel coding) on code channel with PN9 data without wrap-around problems – use of user data (data lists) with externally processed long data sequences for enhanced channels
Realtime BCH with incrementing SFN	Generation of a realtime downlink BCH (coded P-CCPCH) with incrementing system frame number (0 to 4094). BCH can be combined with all reference measurement channels (bit rate 12.2 kbps, 64 kbps, 144 kbps, 384 kbps) or AMR of 12.2 kbps bit rate. Max. sequence length: RMC 12.2 kbps 2044 frames RMC 64 kbps 512 frames RMC 144 kbps 512 frames RMC 384 kbps 512 frames AMR 12.2 kbps 2044 frames
Application	– receiver and performance tests to TS25.101 – test of mobile synchronization to BS signal combined with: continuous measurement of DTCH and DCCH bit and block error rate using PN9 data

Data lists for data fields and TPC field	For the enhanced channels, the data fields and the transmit power control (TPC) field of the slots can be filled from data lists. This allows the use of externally precoded data or the generation of long power control profiles for the DUT.
Applications	– measurement of UE power control steps – measurement of UE max. output power
External power control	Variation of output power of max. 4 enhanced channels in realtime via external control line. The power of all active enhanced channels can be increased or decreased jointly by means of a TTL signal.
Common parameters	
Power step	0.25 dB to 30 dB
Power up range	0 dB to 30 dB
Power down range	0 dB to 30 dB
Parameters for each enhanced channel	
Start power	–60 dB to 0 dB
Power control	OFF; UP; DOWN
Graphic display	current output power (differential power relative to start power) of channels with external power control shown by bargraph
Application	test of SIR based closed loop power control
Channel coding	Coding of up to four enhanced channels in accordance with definition of reference measurement channels given in TS25.101, TS25.104 and TS25.141. In addition, AMR speech 12.2 kbps to TS25.944 and RACH/CPCH (TB size 168 bit or 360 bit, data PN9 fixed) to TS25.141 are supported. Common coding scheme and symbol rate for all enhanced channels.
Implemented reference measurement channels	– uplink reference measurement channel for 12.2 kbps, 64 kbps, 144 kbps, 384 kbps – downlink reference measurement channel for 12.2 kbps, 64 kbps, 144 kbps, 384 kbps
Channel coding structure	– CRC attachment – tail bit attachment – convolutional coding or turbo coding, depending on symbol rate – 1st interleaving – radio frame segmentation – rate matching – 2nd interleaving
Sequence length of coded signal	
4 enhanced channels	up to 256 frames (10 ms each)
1 enhanced channel	up to 1022 frames (10 ms each)
Applications	bit error rate (BER) measurements to TS25.101/104 (radio transmission and reception), eg: – blocking characteristics – spurious response – intermodulation characteristics block error rate (BLER) measurements to TS25.101/104 (radio transmission and reception), eg: – demodulation of dedicated channel under static propagation conditions (in conjunction with R&S SMIQB17) – demodulation of dedicated channel under multipath fading propagation conditions (in conjunction with R&S SMIQB14 and -B17) – test of receiver decoder

Bit error insertion	generation of bit errors by impairment of data stream, either before coding in case of active channel coding, or otherwise at the physical layer
Parameter Bit error rate (nominal BER)	10^{-1} to 10^{-7} with display of resulting BER
Application	verification of internal BER calculation to TS25.141 (BS conformance testing)
Block error insertion	generation of block errors by impairment of CRC during coding of enhanced channels
Parameter Block error rate (nominal BLER)	10^{-1} to 10^{-4} with display of resulting BLER
Application	verification of internal BLER calculation to TS25.141 (BS conformance testing)
Orthogonal channel noise simulation (OCNS)	Simulation of orthogonal background or interference channels for enhanced channels of a base station. If this feature is activated, 16 DPCHs according to TS25.241, 4.1.0, table C.6 are added. The total power of the OCNS part is adjusted automatically in order to achieve a total power of 0 dB.
Applications	– test of mobile receiver under realistic conditions – measurement of maximum input level to TS25.101
Additional mobile stations	Simulation of up to 64 mobile stations in addition to the four user-configurable mobile stations of option R&S SMIQB45. The scrambling codes of the additional mobiles differ from one another.
Parameters	
Number of additional MS	1 to 64
Scrambling code step	1 to 1000 hex
Power offset	–20 to 20 dB
Applications	base station test under realistic receiving conditions
Requirements for installation of option R&S SMIQB48	R&S SMIQxxB with options R&S SMIQB20, R&S SMIQB45, R&S SMIQB11. Maximum sequence length of enhanced channels requires maximum memory extension of data generator, ie two options R&S SMIQB12.

Arbitrary waveform generator with option R&S SMIQB60

Waveform memory, interpolation	
Output memory	
Length of waveform	1 to 524216 in steps of one sample
Note: The specified waveform length cannot be directly compared with the relevant data of conventional ARB generators. In R&S SMIQB60, the oversampling needed for suppressing repetitive spectra by means of the analog filter is effected automatically and in realtime by way of hardware interpolation, ie the stored waveform is not extended by the oversampling factor. For W-CDMA signals, for example, oversampling of only 1.62 is needed. This compares with a conventional ARB with oversampling of 4, meaning that R&S SMIQB60 output memory capacity corresponds to 1.25 Msamples.	
Resolution	12 bit
Loading time for 512k I/Q samples	4 s
Nonvolatile memory	
Number of blocks	22 (one waveform occupies at least one block)
Block size	65527
Interpolation	
Interpolation bandwidth (–0.1dB)	0.375 x clock rate
Repetitive spectra suppression through analog filter	>70 dB

Clock generation	
Clock rate	1 kHz to 40 MHz
Resolution	0.001 Hz
Clock mode	internal or external
Error	$<2^{-42}$ related to reference frequency
Signal output	
Channels	2 (I and Q)
Output impedance	50 Ω
Output level (EMF, peak)	
Normal mode	$\sqrt{I^2 + Q^2} = 1\,V$
Manual mode	–6 dB to 0 dB referred to 1 V, setting range up to +3 dB
Level difference between channels	<0.2% at 1 kHz [3]
DC offset	<–54 dB in normal mode [3]
Frequency response	
Magnitude	
up to 12 MHz	<1 dB
up to 10 MHz	0.1 dB typ.
Group delay	
up to 10 MHz	1 ns typ.
I/Q imbalance	
Magnitude	
up to 10 MHz	0.05 dB typ.
Group delay	
up to 10 MHz	0.5 ns typ.
SFDR (sinewave 1 MHz, clock 4 MHz, measurement range up to 12 MHz)	>60 dB
Trigger	
Trigger modes	auto, retrig, armed auto, armed retrig
Trigger source	internal or external
External trigger input	threshold –2.5 V to 2.5 V, impedance 1 kΩ / 50 Ω
Externe trigger frequency	<10 MHz
Externer trigger delay range	0 to 2^{16} samples
Externer trigger inhibit range	0 to 2^{26} samples
Pulse width	>50 ns
Trigger outputs	
Number	2
Delay	0 to 524216 samples
On time	1 to 524215 samples
Off time	1 to 524215 samples
Level	TTL
Graphic displays	
CCDF	determination and graphic display of CCDF of waveform loaded into output memory; CCDF also serves for crest factor determination. The CCDF traces of the three waveforms last loaded can be displayed simultaneously.

Operation with WinIQSIM™

WinIQSIM™ is a Windows software that allows a wide variety of I and Q baseband signals to be calculated on a PC (see WinIQSIM™ data sheet PD 0757.6940). From version 3.30, the software supports downloading of waveforms into R&S SMIQ and operation of option R&S SMIQB60 from a PC.

Software options R&S SMIQK11 to -K18

For specifications of digital standards with R&S WinIQSIM™ and R&S SMIQB60, R&S SMIQK11 to -K18 please refer to the WinIQSIM™ data sheet PD 0757.6940

Simultaneous modulation

Any combination is possible with the following exceptions:
Simultaneous FM and φM
Simultaneous digital modulation and vector modulation

Overview of digital TDMA standards

The table below summarizes the key data for the digital TDMA standards implemented in R&S SMIQ. Options R&S SMIQB20 and R&S SMIQB11 are required for all standards.

	GSM (GMSK)	GSM-EDGE (8PSK)	DECT	NADC	PDC	PHS
Error vector magnitude, rms	N/A	<1.2 %, 0.6 % typ.	N/A	<1.2 %, 0.4 % typ.	<1.2 %, 0.4 % typ.	<1.2 %, 0.4 % typ.
Phase error (standard), rms Peak value	<1°, 0.3° typ. <3°, 1° typ.	N/A	N/A	N/A	N/A	N/A
Deviation error, rms	N/A	N/A	<1.3 %	N/A	N/A	N/A
Channel spacing/kHz	200	200	1728	30	25	300
Power density spectrum, typ. resolution BW 30 kHz Offset 200 kHz 400 kHz 600 kHz	 –34 dB –70 dB –78 dB	 –34 dB –70 dB –78 dB	 N/A	 N/A	 N/A	 N/A
Adjacent-channel power ratio (ACPR), typ. at adjacent channel at 1st alternate channel at 2nd alternate channel at 3rd alternate channel	 N/A	 N/A	 N/A	 –35 dBc –75 dBc –78 dBc –	 – –74 dBc – –78 dBc	 – –74 dBc –76 dBc –
Burst types	NORMAL, DUMMY, ALL DATA	NORMAL EDGE	FULL (basic), DOUBLE (high capacity), ALL DATA	Up/Down TCH, ALL DATA, Up SHORT	TCH, SYNC, VOX, ALL DATA	TCH (32 kbit and 16 kbit), SYNC, VOX, ALL DATA

Fading simulation with options R&S SMIQB14, R&S SMIQB15[13]

RF bandwidth (–3 dB)	>14 MHz
Additional frequency response	
up to 5 MHz offset from carrier	<0.6 dB, <0.4 dB typ.
Carrier leakage	–45 dBc typ.
Setting time after RF frequency change	<3 ms
Modes	external via I and Q modulation inputs, internal with option R&S SMIQB20
Number of paths and channels	
with option R&S SMIQB14	6 paths, 1 channel
with options R&S SMIQB14 and -B15	12 paths, 1 channel, or 6 + 6 paths, 2 channels with second R&S SMIQ through simple retrofit (for instructions see manual)
Path attenuation	
Range	0 dB to 50 dB
Resolution	0.1 dB
Error (in range 0 dB to 20 dB)	<0.3 dB
Path delay	
Range	0 µs to 1600 µs
Resolution	50 ns
Error	<5 ns
Doppler shift	
Frequency range	0.1 Hz to 1600 Hz
Speed range	$v_{min} = \frac{0,03 \times 10^9 \frac{m}{s^2}}{f_{RF}}$ $v_{max} = \frac{479 \times 10^9 \frac{m}{s^2}}{f_{RF}}$
Example with f_{RF} =1 GHz: v_{min}= 0.1 km/h, v_{max}=1724 km/h	
Resolution	0.1 km/h, m/s, mph
Error	<0.13%
Rayleigh fading	
Pseudo noise interval	>372 h
Deviation from theoretical CPDF[4] for P_{avg} = 0 dB	
Path attenuation from –20 dB to + 10 dB	<1 dB, <0.3 dB typ.
Path attenuation from –30 dB to –20 dB	<2 dB, <0.3 dB typ.
Rice fading	
Power ratio [5]	
Range	–30 dB to +30 dB
Resolution	0.1 dB
Frequency ratio	
Range	–1 to +1
Resolution	0.05
Lognormal fading, Suzuki fading	
Standard deviation	
Range	0 dB to 12 dB
Resolution	1 dB
Local constant	l_{min} to 200 m $l_{min} = \frac{12 \times 10^9 \frac{m}{s}}{f_{RF}}$
Correlation	paths 1 to 6 with paths 7 to 12
Magnitude range	0% to 100%
Resolution	5%
Phase range	0° to 360°
Resolution	1°
General data; thermal loading	specs valid in range 0 °C to 45°C

Enhanced fading functions for WCDMA 3GPP with option R&S SMIQB49

The following data deviate from the specifications for R&S SMIQB14/R&S SMIQB15:

Modes	standard fading, fine delay, moving delay, birth-death
Setting time after RF frequency change	6 ms
Fine delay mode	
RF bandwidth	4.8 MHz
Number of paths	2 (with R&S SMIQB14), 4 (with R&S SMIQB14 + R&S SMIQB15)
Profiles	Rayleigh, pure Doppler
Delay	25 ns to 1637 µs
Delay resolution	1 ns
Moving delay mode	
RF bandwidth	4.8 MHz
Number of paths	2
Delay, path 1	0 to 1000 µs (in 50 ns steps)
Delay, path 2	$\text{delay path1} + \frac{\text{delay variation}_{(pk\text{-}pk)}}{2} \times \sin \frac{2\pi t}{\text{variation period}}$
Delay variation (peak-peak)	150 ns to 50 µs
Variation period	10 s to 500 s
Delay step size	<1 ns
Profiles	none
Birth-death mode	
Number of paths	2
Profiles	pure Doppler
Delay	5 µs to 1000 µs
Delay range (birth-death process)	5 µs to +5 µs (not variable)
Delay grid	1 µs (not variable)
Hopping dwell	100 ms to 5 s

Noise and distortion simulation with option R&S SMIQB17[13)]

RF bandwidth (–3 dB)	>14 MHz
Additional frequency response up to 5 MHz offset from carrier	<0.6 dB, 0.4 dB typ.
Carrier leakage [9)]	–40 dBc typ.
Distortion simulator	
Type of distortion	AM/AM and AM/φM distortion of modulation signal
Distortion characteristic	each characteristic programmable by entering up to 30 input values via IEC/IEEE bus or by entering up to five polynomial coefficients
Resolution	12 bit
Noise generator (AWGN)	
Distribution density	Gaussian, statistically independent for I and Q
Crest factor	14 dB
C/N	
Range	–30 dB to 30 dB
Resolution	0.1 dB
Error for system bandwidth = symbol rate and C/N <20 dB [3)]	
Vector, PSK, QAM modulation	<0.4 dB
FSK, GMSK modulation [10)]	<0.4 dB
System bandwidth	relevant bandwidth for determining noise power N
Range	10 kHz to 10 MHz
Resolution	1×10^{-2}
Output spectrum	white noise
Frequency response up to 0.7 x system bandwidth and 5 MHz offset from carrier at RF output [3)]	<0.8 dB

Bit error rate measurement with option R&S SMIQB21

The data supplied by the DUT are compared with a reference pseudo-random bit sequence.	
Pseudo-random bit sequences (PRBS)	2^9-1, 2^{11}-1, 2^{15}-1, 2^{16}-1, 2^{20}-1, 2^{21}-1, 2^{23}-1
Clock source	supplied by DUT; a clock pulse is required for each valid bit
Clock rate	100 Hz to 30 MHz
Synchronization time	24 clock cycles
Interface	9-pin sub-D connector, sub-D/BNC cable supplied with option
Data	TTL
Data enable	TTL
Clock	TTL
Restart	TTL
Setup time	10 ns
Hold time	2 ns
Polarity	normal and inverted (data, clock, data enable)
Measurement time	selectable through maximum number of data bits or bit errors (max. 2^{31} bit each), continuous measurement
Measurement result	BER in ppm, % or decade values (if selected number of data bits or bit errors is attained) status displays: not synchronized, no clock, no data

Option R&S SMIQB47 for improved adjacent-channel power ratio for WCDMA and CDMA IS-95

Modulation and ACP specifications apply at the respective standard frequency ranges and at levels
≤8 dBm PEP with R&S SMIQ02B/03B
≤5 dBm PEP with R&S SMIQ04B/06B

Selectable baseband filters to improve ACP values (values see at Digital Standards CDMA/WCDMA)	
Bandwidth	OFF, 850 kHz, 2.5 MHz, 5 MHz
WCDMA chiprate 3.84 Mcps /4.096 Mcps, 1DPCH/1DTCH, f = 1800 MHz to 2200 MHz, IQ filter 2.5 MHz	
Offset 5 MHz, low distortion output mode	<–65 dBc, –70 dBc typ. [8)]
Offset 10 MHz, low noise output mode	<–71 dBc, –74 dBc typ. [8)]
IS-95 CDMA, f = 824 MHz to 894 MHz and 1850 MHz to 2000 MHz, IQ filter 850 kHz	
Reverse link	
Offset 885 kHz	<–78 dBc, –82 dBc typ.
Offset 1.25 MHz	<–83 dBc, –87 dBc typ.
Offset 1.98 MHz	<–85 dBc, –89 dBc typ.
9 channels forward link	
Offset 885 kHz	<–74 dBc, –78 dBc typ.
Offset 1.25 MHz	<–80 dBc, –84 dBc typ.
Offset 1.98 MHz	<–83 dBc, –86 dBc typ.
Error vector magnitude, rms	
WCDMA chiprate 3.84 Mcps /4.096 Mcps, IQ filter 2.5 MHz	<3%, 1.8% typ.
IS-95 CDMA IQ filter 850 kHz	<2%, 1.3% typ.

Other data

Memory for instrument settings

50 storable settings

Memory sequence modes	automatic, single shot, manual or external trigger
Step time Resolution	50 ms to 60 s 1 ms

List mode

Frequency and level values can be stored in a list and set in an extremely short time; permissible level variation: 90 dB	
Modes	automatic, single shot, manual or external trigger
Max. number of channels	2000
Dwell time Resolution	0.5 ms to 1 s 0.1 ms

Remote control

System	IEC 60625 (IEEE 488)
Command set	SCPI 1993.0
Connector	24-contact Amphenol
IEC/IEEE-bus address	0 to 30
Interface functions	SH1, AH1, T6, L4, SR1, RL1, PP1, DC1, DT1, C0
System	RS 232
Command set	SCPI 1993.0
Connector	9-contact D-SUB
Baud rate	1.2 kbit/s to 115.2 kbit/s

General data

Power supply	100 V to 240 V (AC) ± 10%, 50 Hz to 400 Hz, autosetting to AC supply, max. 300 VA
Electromagnetic compatibility Immunity to RFI	to EN 55011 and EN 61326-2 (EMC directive of EU) 10 V/m
Ambient conditions Operating temperature range Storage temperature range Climatic conditions	 0°C to 50°C [6] to IEC60068 –40°C to +70°C 95% relative humidity at +25°C/+40°C cyclically, to IEC 60068
Mechanical resistance Sinewave vibrations	 5 Hz to 150 Hz, max. 2 g at 55 Hz, max. 0.5 g in range 55 Hz to 150 Hz, to IEC 60068, IEC 61010 and MIL-T-28800D, class 5
Random Shock	10 Hz to 300 Hz, acceleration 1.2 g (rms) 40 g shock spectrum, to MIL-STD-810D, MIL-T-28800D, class 3 and 5
Safety	to EN 61010-1
Dimensions (W x H x D)	435 mm x 192 mm x 460 mm
Weight	25 kg when fully equipped

Ordering information

Vector Signal Generator	R&S SMIQ02B 300 kHz to 2.2 GHz		1125.5555.02
	R&S SMIQ03B 300 kHz to 3.3 GHz		1125.5555.03
	R&S SMIQ04B 300 kHz to 4.4 GHz		1125.5555.04
	R&S SMIQ06B 300 kHz to 6.4 GHz		1125.5555.06
Accessories supplied		power cable, operating manual	
Options			
Reference Oscillator OCXO		R&S SM-B1	1036.7599.02
FM/φM Modulator		R&S SM-B5[13)]	1036.8489.02
Data Generator		R&S SMIQB11	1085.4502.04
Memory Extension, 32 Mbit		R&S SMIQB12	1085.2800.04
Fading Simulator, 6 paths		R&S SMIQB14[13)]	1085.4002.02
Second Fading Simulator for 12 paths or 2 channels		R&S SMIQB15[13)]	1085.4402.02
Noise Generator and Distortion Simulator		R&S SMIQB17[13)]	1104.9000.02
RF and AF Rear Connectors		R&S SMIQB19	1085.2997.02
Modulation Coder		R&S SMIQB20	1125.5190.02
BER Measurement		R&S SMIQB21	1125.5490.02
Digital Standard IS-95 CDMA		R&S SMIQB42	1104.7936.02
Digital Standard WCDMA to NTT DoCoMo 1.0, ARIB 0.0 standard		R&S SMIQB43	1104.8032.02
Digital Standard WCDMA to 3GPP (FDD)		R&S SMIQB45	1104.8232.02
Low ACP for IS-95 CDMA and W-CDMA		R&S SMIQB47	1125.5090.02
Extended Functions for WCDMA (3GPP)		R&S SMIQB48	1105.0587.02
Extended Fading Functions for WCDMA (3GPP)		R&S SMIQB49	1105.1083.02
Arbitrary Waveform Generator incl. R&S WinIQSIM™		R&S SMIQB60	1136.4390.02
TETRA T1 Simulator		R&S SMIQ-K8	1136.4290.02
Digital Standard IS-95 CDMA	(for option R&S SMIQB60)	R&S SMIQK11	1105.0287.02
Digital Standard cdma2000	(for option R&S SMIQB60)	R&S SMIQK12	1105.0435.02
Digital Standard WCDMA TDD Mode (3GPP)	(for option R&S SMIQB60)	R&S SMIQK13	1105.1231.02
Digital Standard TD-SCDMA	(for option R&S SMIQB60)	R&S SMIQK14	1105.1383.02
OFDM Signal Generation, HIPERLAN/2	(for option R&S SMIQB60)	R&S SMIQK15	1105.1531.02
Digital Standard 1xEV-DO	(for option R&S SMIQB60)	R&S SMIQK17	1154.7800.02
Digital Standard IEEE 802.11	(for option R&S SMIQB60)	R&S SMIQK19	1154.8307.02
Digital Standard 3GPPP FDD incl. HSDPA	(for option R&S SMIQB60)	R&S SMIQK20	1400.5302.02
Hint: R&S SMIQ02B/03B (R&S SMIQ04B/06B) can be equipped with max. three (two) of the following options: R&S SM-B5, R&S SMIQB14, R&S SMIQB15, R&S SMIQB17			
Application software			
PC Software: Generation of data and control lists		R&S SMIQ-K1	*)
PC Software: *Bluetooth* signals for R&S SMIQ		R&S SMIQ-K5	*)
PC Software: User mappings and user filters for R&S SMIQ		R&S User Mod	*)
PC Software: 802.11 packet error rate testing tool			*)
		*) available at www.rohde-schwarz.com	
Recommended extras			
19" Adapter		R&S ZZA-94	0396.4905.00
Service Kit		R&S SM-Z3	1085.2500.02
BNC Adapter for rear panel, D type connector PAR DATA		R&S SMIQ-Z5	1104.8555.02
90° Power Splitter		R&S SMIQ-Z9	1104.9580.02
Trolley for Transit Case		R&S ZZK-1	1014.0510.00
Transit Case		R&S ZZK-944	1013.9366.00

Service Manual R&S SMIQ		1085.2445.24
Instrument upgrades		
R&S SMIQ02B to R&S SMIQ03B	R&S SMIQU03	1125.5855.03
R&S SMIQ03B to R&S SMIQ04B	R&S SMIQU04 [7)]	1125.5855.04
R&S SMIQ04B to R&S SMIQ06B	R&S SMIQU06 [7)]	1125.5855.06
Modification Kit for Low ACP	R&S SMIQU47 [7)]	1125.5149.02

1) PEP = peak envelope power.
2) Data apply to RF≥5 MHz unless specified otherwise and for ATTENUATOR MODE AUTO function.
3) After 1 hour warmup time and recalibration during 4 hours of operation with temperature variations <5 °C.
4) CPDF = cumulative probability distribution function; levels referred to average value of output level.
5) Ratio of discrete and distributed component.
6) Contrast of LCD lower at higher temperature.
7) Factory installation only.
8) Spectrum analyzer settings RBW 30 kHz, VBW 300 kHz, detector RMS.
9) Typical value for QPSK modulation (crest factor approx. 4 dB), referred to average power from sum of carrier and noise power for C/N >5 dB. Carrier leakage deteriorates with increasing crest factor of modulation signal.
10) For symbol rate <300 ksym/s.
11) Spectral components exceeding max. IQ bandwidth will be suppressed.
12) Additional error with ALC OFF <0.3 dB.
13) R&S SMIQ02B/03B (R&S SMIQ04B/06B) can be equipped with up to three (two) of the following options: R&S SM-B5, R&S SMIQB14, R&S SMIQB15, R&S SMIQB17

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