

6062A

SYNTHESIZED RF SIGNAL GENERATOR

Instruction Manual

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Section 1

Introduction and Specifications

1-1. INTRODUCTION

The 6062A Synthesized RF Signal Generator (referred to as the Generator or instrument) is a fully-programmable, precision, synthesized signal generator. The Generator is designed for applications that require good modulation, frequency accuracy, and output level performance with moderate spectral purity. It is well suited for testing a wide variety of RF components and systems including filters, amplifiers, mixers, and radios, particularly on-channel radio testing.

1-2. UNPACKING THE GENERATOR

The shipping container should include a 6062A Synthesized RF Signal Generator, an Operator Information Card, a Getting Started Manual, an Instruction Manual, a line power cord and two BNC dust caps. Any accessories ordered for the Generator are shipped in a separate container.

Section 2, Installation and Operation, gives instructions on inspecting the new Generator and explains what to do if the instrument arrives with shipping damage. Reshipment information is also included.

1-3. SAFETY

This instruction manual contains information, warnings, and cautions that should be followed to ensure safe operation and to maintain the Generator in a safe condition.

The Generator is designed primarily for indoor use, and it may be operated in temperatures from 0 to 50°C without degradation of its safety.

WARNING

TO AVOID ELECTRIC SHOCK, USE A POWER CORD THAT HAS A THREE-PRONG PLUG. IF A PROPER POWER CORD IS NOT USED, THE 6062A CASE CAN DEVELOP AN ELECTRICAL POTENTIAL ABOVE EARTH GROUND.

CAUTION

To avoid damage to the 6062A, check that the rear panel line voltage selection card and fuse are correct for the line voltage in your area. The correct line voltage and fuse combinations are:

LINE VOLTAGE	FUSE
100/120V ac, $\pm 10\%$, 47 to 63 Hz	1.5 amp
220/240V ac, $\pm 10\%$, 47 to 63 Hz	.75 amp

1-4. OPERATOR INFORMATION CARD

The Operator Information Card has an adhesive backing so it can be affixed to the top of the Generator in bench applications or to the operator console in remote applications. A copy of the card is located at the end of Section 7 of this manual as a convenient reference or for duplication.

1-5. GENERATOR DESCRIPTION

Fundamental features of the Generator are as follows:

- 0.10- to 1050-MHz frequency range in 10-Hz steps
- 1050- to 2100-MHz frequency range in 20-Hz steps
- +16 to -137-dBm level range in 0.1-dB steps from 0.10 to 1050 MHz
- +13 to -137-dBm level range in 0.1-dB steps from 1050 to 2100 MHz
- AM, FM, ϕ M, and Pulse, internal or external
- Internal 400- and 1000-Hz modulation oscillator
- Relative frequency and amplitude
- Volts/dBm conversion
- Store/recall memory
- Instrument (or controller) prompted software compensation
- Master/slave for frequency, amplitude, and modulation functions (IEEE-488 Interface controlled.)
- Fluorescent display
- 5¼-inches high, rack mountable

1-6. Controller Functions

The Generator's microprocessor controls all operator interface functions, performs background operations such as status checks, and updates (strokes) the front panel displays. Whether the operator is using local control with the front panel, or remote control with the IEEE-488 Interface, the microprocessor provides self test and diagnostic capability. Economical instrument performance is achieved by using software compensation and accuracy-enhancement circuitry.

1-7. LOCAL CONTROL

The value of the basic output parameters of the Generator (i.e., amplitude, frequency, or modulation) can be controlled in three ways:

- Direct numeric entry
- Incrementing or decrementing the bright digit
- Step-up or step-down entry where the operator can program the step size via the front panel

Other controls provide selection of the modulation control, internal/external frequency reference, instrument status, and special function modes. Refer to Tables 2-1 and 2-2 for a more complete discussion.

1-8. DISPLAY FIELD

The programmed values of modulation, frequency, and amplitude are displayed in the three display fields. In addition, when the operator presses the [STATUS] key, uncal and reject entry codes are displayed. The display also prompts the operator during the software compensation procedures.

1-9. REMOTE-CONTROL PROGRAMMING

The IEEE-488 Interface allows the Generator to be remotely controlled with any IEEE-488 bus controller. The operator can also use the instrument on the IEEE-488 bus without a controller in a listen-only or talk-only mode by selecting the appropriate Generator rear panel IEEE-488 switch settings.

All instrument controls can be controlled remotely except the POWER ON/OFF and the rear panel REF INT/EXT switches. The IEEE-488 Interface provides additional commands not available with local control, such as data transfer and individual control of internal I/O control bits.

The IEEE-488 Interface allows two Generators to track amplitude, frequency, or modulation in a master/slave configuration when the operator uses the front panel step-up and step-down entries on one of the instruments. For instance, frequency tracking is convenient for tests involving mixers, and amplitude tracking is useful for two-tone intermodulation testing.

1-10. Frequency

The specified frequency range is 0.10 to 2100 MHz. The frequency is synthesized from a 10-MHz reference and provides an output resolution of 10 Hz from 0.10 to 1050 MHz, and 20 Hz from 1050 to 2100 MHz. The relative frequency mode allows the operator to program the frequency in relation to a center frequency or an offset frequency. This is convenient for testing filters and mixers. The output frequency stability and accuracy depends on the reference, whether that reference is internal or external.

1-11. Reference

The internal frequency reference is a 10-MHz ambient crystal oscillator. If Option -130 High Stability Reference or Option -132 Medium Stability Reference is installed, the internal oscillator is locked to the option oscillator.

With the REF INT/EXT switch set to EXT, the Generator can be operated from an external 1-, 2-, 2.5-, 5-, or 10-MHz, 0.3 to 4V peak-to-peak sine or square-wave reference applied to the REF IN connector. In either position of the REF INT/EXT switch, the selected reference is available as a 10-MHz signal at the rear panel 10 MHz OUT connector.

1-12. Amplitude

The Generator has two specified signal level ranges:

- +16 to -137 dBm (1.41V to 0.1 μ V) over a frequency range of 0.10 to 1050 MHz
- +13 to -137 dBm (1V to 0.033 μ V) over a frequency range of 1050 to 2100 MHz.

The programming limits are +17 and -147.4 dBm, which correspond to limits of 1.58V to 0.01 μ V. The level entry can be in dBm or volts, or it can be converted from one to the other. The relative amplitude mode allows the operator to account for cascaded gain or loss by displaying the level relative to the initial setting.

1-13. Modulation

The Generator has internal and external amplitude, frequency, phase, and pulse modulation capabilities. The internal modulation oscillator is selectable between 400 and 1000 Hz. AM depths of 0 to 99% are available in 1% steps. FM deviation ranges of 1, 10, 100, and 400 kHz are available in steps of 1, 10, 100 Hz, and 1 kHz, respectively. ϕ M deviation ranges of 0.10, 1.0, 10.0, and 40.0 radians (rad) are available in steps of 0.001, 0.01, 0.1, and 0.1 radians, respectively. The internal Pulse duty cycle is approximately 50%. External Pulse allows rates from dc up to 16 MHz, and duty cycles from 0 to 100%.

1-14. OPTIONS AND ACCESSORIES

The following options are available for the Generator:

- Option -130 High-Stability (Ovened) Reference
- Option -132 Medium-Stability Reference
- Option -830 Rear Panel RF Output, MOD Input, and Pulse MOD Input

Section 6 provides more detailed information on the options.

The following accessories are included with each Generator:

DESCRIPTION	PART NUMBER	QUANTITY
Operator Information Card	797928	1
Getting Started Manual	794859	1
Instruction Manual	794842	1
Line Power Cord	284174	1
BNC Dust Cap	478982	2

The following accessories are available for the Generator:

DESCRIPTION	ACCESSORY NO.
Rack Mount Kit. Includes M05-205-600 (5 1/4-inch Rack Mount Ears) and M00-280-610 (24-inch Rack Slides)	Y6001
IEEE-488 Shielded Cable, 1 meter	Y8021
IEEE-488 Shielded Cable, 2 meters	Y8022
IEEE-488 Shielded Cable, 4 meters	Y8023
Attenuator, 50 ohms, 6 dB, BNC	Y9100
Attenuator, 50 ohms, 14 dB, BNC	Y9101
Attenuator, 50 ohms, 20 dB, BNC	Y9102
50 ohms Feed-through Termination, BNC	Y9103
Coaxial Cable, 50 ohms, 3 feet, BNC (m) both ends	Y9111
Coaxial Cable, 50 ohms, 6 feet, BNC (m) both ends	Y9112
Min-Loss Pad, 50 ohms to 75 ohms	Y9301
Adapter, N to BNC, 75 ohms	Y9307
Adapter, N to BNC, 50 ohms	Y9308
Coaxial Cable, N male to N male, 6 ft	Y9315
Cap, Non-Shorting, BNC	Y9316
50 ohms Termination, N	Y9317
50 ohms Coaxial Switch	PM 2122/02
Test Team Software	PM 2240/002

1-15. RECOMMENDED TEST EQUIPMENT

The test equipment recommended for the performance tests, calibration adjustments, and troubleshooting are listed in Table 4A-1. This equipment is assumed to be calibrated to the manufacturer's specifications. If the recommended test equipment is not available, equivalent test equipment can be substituted.

1-16. MNEMONICS

The mnemonics used in the schematics, block diagrams, wiring diagrams, truth tables, and the text are listed in Figure 7-1.

1-17. SIGNAL GENERATOR SPECIFICATIONS

Unless otherwise noted, the following performance is guaranteed over the specified environmental and ac power line conditions 20 minutes after turn-on. Table 1-1 lists the Generator specifications.

Table 1-1. 6062A Synthesized RF Signal Generator Specifications

Warranted performance, 20 minutes after power-on within operating temperature range.	
FREQUENCY (9 1/2-Digit Display)	
RANGE	0.1 to 2100.0 MHz in 4 bands; 0.1 to 244.99999 MHz, 245 to 511.99999 MHz, 512 to 1049.99999 MHz, 1050 to 2100.0 MHz.
RESOLUTION	10 Hz from 0.1 to 1050 MHz, 20 Hz from 1050 to 2100 MHz.
ACCURACY	Same as reference (See REFERENCE).
REFERENCE (Internal)	The unit operates on an internal free-air 10-MHz crystal oscillator with an ageing rate of $<\pm 0.5$ ppm/month and $<\pm 10$ ppm for 25°C, $\pm 25^\circ\text{C}$. Internal reference signal (10-MHz) available at rear connector, Level >0 dBm, terminated in 50-ohms.
REFERENCE (External)	Accepts 1-, 2-, 2.5-, 5-, or 10-MHz signal. Level of 0.3 to 4.0 Vpp into 50-ohms termination.
AMPLITUDE (3 1/2-Digit Display)	
RANGE (Indicated)	+16 (+16 peak with AM enabled) to -137 dBm, from 0.1 to 1049.99999 MHz. +13 (+13 peak with AM enabled) to -137 dBm, from 1050 to 2100 MHz. (Autoranging 6-dB step attenuator).
RESOLUTION	0.1 dB ($<1\%$ or 1 nV in volts). Annunciators for dB, dBm, V, mV, μV , dB mV, and dB μV .
ACCURACY	± 1 dB from +16 to -127 dBm from 1 to 1049.99999 MHz. * ± 1.5 dB from +13 to -127 dBm from 1050 to 2100 MHz. ± 2.0 dB from +16 to -127 dBm from 0.1 to .99999 MHz. * ± 1.5 dB at temperatures other than $23^\circ\text{C} \pm 5^\circ\text{C}$

Table 1-1. 6062A Synthesized RF Signal Generator Specifications (cont)

SOURCE VSWR	<1.5:1 below +1 dBm, <2.0:1 elsewhere.
SPECTRAL PURITY (CW ONLY)	
SPURIOUS	<-60 dBc for offsets greater than 10 kHz and frequencies from 0.1 to 1049.99999 MHz. <-54 dBc for offsets greater than 10 kHz and frequencies from 1050 to 2100 MHz.
NOTE	
Fixed frequency spurs are <-60 dBc or <-140 dBm whichever is larger.	
NOTE	
dBc refers to decibels relative to the carrier frequency, or in this case, relative to the signal level.	
HARMONICS	<-30 dBc for levels \leq +13 dBm and frequencies \geq 1 MHz, <-25 dBc elsewhere.
SUBHARMONICS	<-45 dBc for output frequencies from 1050 to 2100 MHz.
RESIDUAL FM (rms in 0.3- to 3-kHz band)	<12 Hz for 0.1 to 244.99999 MHz, <6 Hz for 245 to 511.99999 MHz, <12 Hz for 512 to 1049.99999 MHz, <24 Hz for 1050 to 2100 MHz.
RESIDUAL FM (rms in 0.05- to 15-kHz band)	<18 Hz for 0.1 to 244.99999 MHz, <9 Hz for 245 to 511.99999 MHz, <18 Hz for 512 to 1049.99999 MHz, <36 Hz for 1050 to 2100 MHz.
RESIDUAL AM (in 0.05- to 15-kHz Band)	<-60 dBc.
AMPLITUDE MODULATION (2-Digit Display)	
DEPTH RANGE	0 to 99%.
RESOLUTION	1%.
ACCURACY	$\pm(2\% + 4\%$ of setting) for internal rates, to peak amplitude of +13 dBm for frequencies of 1 MHz to 2100 MHz. $\pm(3\% + 5\%$ of setting) for internal rates to peak amplitude of +13 dBm for frequencies of 0.1 to .99999 MHz.

Table 1-1. 6062A Synthesized RF Signal Generator Specifications (cont)

DISTORTION	<p><1.5% total harmonic distortion (THD) to 30% AM, <3% THD to 70% AM, <5% THD to 90% AM for frequencies of 10 to 1049.99999 MHz and peak amplitude <+13 dBm.</p> <p><3% THD to 70% AM, and <5% THD to 90% AM for frequencies of 1050 to 2100 MHz and peak amplitude < +13 dBm.</p> <p><3% THD to 30% AM, <5% THD to 70% AM, and <7% THD to 90% AM for frequencies of 0.1 to 9.99999 MHz and peak amplitude <6 dBm.</p>
BANDWIDTH (3 dB)	<p>20 Hz to 50 kHz.</p> <p>DC to 50 kHz by special function.</p> <p>(Note-- valid for RF frequency - Mod frequency ≥ 150 kHz)</p>
INCIDENTAL FM	<p><0.3 fm for internal rates, 30% or less AM, and frequencies from 0.1 to 1049.99999 MHz.</p> <p><0.6 fm for internal rates, 30% or less AM, and frequencies from 1050 to 2100 MHz.</p>

FREQUENCY MODULATION (3-Digit Display)

DEVIATION RANGES 0 to 999 Hz, 1 to 9.99 kHz, 10 to 99.9 kHz, and 100 to 400 kHz.

MAXIMUM DEVIATION

Mod Rate	Max Dev	RF Frequency
fm $\geq .2$ kHz	400 kHz	1050 to 2100 Mhz
	200 kHz	512 to 1049.99999 MHz
	100 kHz	245 to 511.99999 Mhz
	200 kHz	0.1 to 244.99999 Mhz

fm < .2 kHz	Lower of above or:	
	2 fm fo	245 to 2100 MHz
	2 fm(fo+800)	0.1 to 244.99999 MHz

Where: fm = mod frequency in kHz

fo = RF frequency in MHz
Deviation is in kHz

Specs apply where:

RF Frequency - Dev ≥ 150 kHz
RF Frequency - Mod Rate ≥ 150 kHz

RESOLUTION 3 digits.

ACCURACY $\pm (7\% + 10 \text{ Hz})$ for rates of 0.3 to 20 kHz. Does not include effects of Residual FM.

Table 1-1. 6062A Synthesized RF Signal Generator Specifications (cont)

DISTORTION	<1% THD for rates of 0.3 to 20 kHz. Does not include effects of Residual FM.
BANDWIDTH (3 dB).....	20 Hz to 100 kHz. (Note-- valid for RF Frequency - Mod Frequency ≥ 150 kHz)
INCIDENTAL AM	<1% AM at 1 kHz rate, for the maximum deviation or 50 kHz, whichever is less. Valid for RF Frequency ≥ 1 MHz.
PHASE MODULATION (3 digit display)	
DEVIATION RANGES	0 to .099 rad, .100 to .999 rad, 1.00 to 9.99 rad, and 10.0 to 40.0 rad.
MAXIMUM DEVIATION	20 rad from 0.1 to 244.99999 MHz. 10 rad from 245 to 511.99999 MHz. 20 rad from 512 to 1049.99999 MHz. 40 rad from 1050 to 2100 MHz.
RESOLUTION	3 digits
ACCURACY	$\pm(7\% + .01 \text{ rad})$ at 1 kHz rate. Does not include effects of Residual FM.
DISTORTION	<1% THD for 1 kHz rate. Does not include effects of Residual FM.
BANDWIDTH (3 dB)	20 Hz to 10 kHz. (Note-- valid for RF Frequency - Mod Frequency ≥ 150 kHz)
INCIDENTAL AM	<1% AM at 1 kHz rate.
PULSE MODULATION (RF Frequencies from 10 to 2100 MHz)	
ON/OFF RATIO	80 dB minimum
RISE & FALL TIMES	<15 nsec
LEVEL ERROR.....	for pulse widths ≥ 50 nsec, power in the pulse within ± 0.5 dB of measured CW level.
DUTY CYCLE (ext mod).....	0-100%
REP RATE (ext mod).....	DC-16 MHz
INTERNAL MODULATION	internal rates, approx 50% duty cycle.
EXTERNAL PULSE MODULATION	The pulse input is TTL compatible and 50 ohm terminated with an internal active pull-up. It can be modeled as 1.2 V in series with 50 ohms at the pulse modulation input connector. The instrument senses input terminal voltage and turns the RF off when the terminal voltage drops below $1 \pm$ 0.1 V. Max allowable applied voltage, ± 10 V.
PULSE MODULATION (RF Frequencies <10 MHz)	
RISE & FALL TIMES.....	$\leq 2 \times$ period of RF Frequency.

Table 1-1. 6062A Synthesized RF Signal Generator Specifications (cont)

LEVEL ERROR.....	for pulse widths $\geq 10 \times$ period of RF Frequency, power in the pulse will be within ± 0.5 dB of the measured CW level.
Other specifications are the same as for the 10 to 2100 MHz range.	
NON-VOLATILE MEMORY	
50 instrument states are retained for typically 2 years, even with the power mains disconnected.	
REVERSE POWER PROTECTION	
PROTECTION LEVEL	up to 25 watts from a 50 ohm source. up to 25 VDC. Instrument output is AC coupled.
TRIP/RESET	Flashing RF OFF annunciator indicates a tripped condition. Pushing RF ON/OFF button will reset instrument. Protection is provided when instrument is off.
IEEE-488	
INTERFACE FUNCTIONS	SH1, AH1, T5, TE0, L3, LE0, SR1, RL1, PPO, DC1, DT1, CO, and E1.
MODULATION SOURCE	
INTERNAL	0.4 or 1 kHz, $\pm 3\%$ for 20 to 30°C; add $\pm 0.1\%/^{\circ}\text{C}$ outside this range.
EXTERNAL	$\pm 5\text{V}$ max.; 1V peak provides indicated modulation index. Nominal input impedance is 600 ohms.
MODES	Any combination of AM and FM or $\emptyset\text{M}$, internal or external, may be used. If external AM and FM or $\emptyset\text{M}$ are enabled, the modulation input Z will drop to approximately 560 ohms. Pulse modulation is completely independent and can be used in conjunction with any other form(s) of modulation.
GENERAL	
TEMPERATURE	
Operating	0 to 50°C (32 to 122°F).
Non-Operating	-40 to 75°C (-40 to 167°F).
HUMIDITY RANGE	
Operating	95% to 30°C, 75% to 40°C, 45% to 50°C.
ALTITUDE	
Operating	Up to 10,000 ft.
VIBRATION	
Non-Operating	5 to 15 Hz at 0.06 inch, 15 to 25 Hz at 0.04 inch, and 25 to 55 Hz at 0.02 inch, double amplitude (DA).

Table 1-1. 6062A Synthesized RF Signal Generator Specifications (cont)

SHOCK

Non-Operating Bench handling per MIL T 28800C Class 5, Style E.

ELECTROMAGNETIC

COMPATIBILITY The radiated emissions induce $<3 \mu\text{V}$ ($<1 \mu\text{V}$ of the Generator's output signal) into a 1-inch diameter, 2-turn loop, 1-inch from any surface as measured into a 50-ohm receiver.

Also complies with the following standards:

CE03 of MIL-STD-461B (Power and interconnecting leads), 0.015 to 50 MHz.

RE02 of MIL-STD-461B (14 kHz to 10 GHz).

FCC Part 15 (j), class A.

CISPR 11.

SIZE	Width	Height	Depth
	43 cm	13.3 cm	50.8 cm
	17 in	5.25 in	20 in

POWER 100, 120, 220, 240V ac $\pm 10\%$, 47 to 63 Hz, $<180 \text{ VA}$ ($<15 \text{ VA}$, with Option -130 installed, and the Generator turned off (standby)).

WEIGHT $<15.7 \text{ kg}$ (35 lbs).

OPTION -130 HIGH-STABILITY REFERENCE

AGING RATE $<\pm 5 \times 10^{-10}/\text{day}$, after 21 days continuous operation.

TEMPERATURE STABILITY $<\pm 2 \times 10^{-10}/^\circ\text{C}$ (oven remains powered in standby).

OPTION -132 MEDIUM-STABILITY REFERENCE

AGING RATE $<\pm 1 \times 10^{-7}/\text{month}$ after 5 days continuous operation.

TEMPERATURE STABILITY $<\pm 1 \times 10^{-7}/^\circ\text{C}$ (0 to 50°C) (no powered standby).

OPTION -651 LOW-RATE EXTERNAL FM

MAXIMUM DEVIATION 9.99 kHz.

DROOP $<15\%$ on a 10-Hz square wave.

BANDWIDTH (3dB) 0.5 Hz to 100 kHz (typical).

MAX DC INPUT $\pm 10 \text{ mV}$.

INCIDENTAL AM $<1\%$ AM at 1-kHz rate and deviation $<10 \text{ kHz}$.

Table 1-1. 6062A Synthesized RF Signal Generator Specifications (cont)

SUPPLEMENTAL CHARACTERISTICS

The following characteristics are provided to assist in the application of the Generator and to describe the typical performance that can be expected.

FREQUENCY SWITCHING SPEED	<100 mS to be within 100 Hz.
AMPLITUDE SWITCHING SPEED	<100 mS to be within 0.1 dB.
AMPLITUDE RANGE	Programmable from +17 to -147.4 dBm. Fixed-range, selected by special function, allows for more than 12 dB of vernier without switching the attenuator.
AMPLITUDE ACCURACY.....	+2.0 dB from -127.1 to -137 dBm from 0.1 to 2100 MHz.
NOISE (at 20-kHz offset)	<-116 dBc/Hz from 0.10 to 244.99999 MHz. <-122 dBc/Hz from 245 to 511.99999 MHz. <-116 dBc/Hz from 512 to 1049.99999 MHz. <-110 dBc/Hz from 1050 to 2100 MHz.

RESIDUAL FM (typical)

Freq Range	.3 - 3 kHz	.05 - 15 kHz	CCIT
0.1 - 244.99999 MHz	8 Hz	12 Hz	7 Hz
245 - 511.99999 MHz	4 Hz	6 Hz	3.5 Hz
512 - 1049.99999 MHz	8 Hz	12 Hz	7 Hz
1050 - 2100 MHz	16 Hz	24 Hz	14 Hz

EXTERNAL MODULATION Annunciators indicate when a 1V peak signal is applied, $\pm 2\%$, over a 0.02- to 100-kHz band.

IEEE All controls except the power switch and the internal/external reference switch are remotely programmable via IEEE-488 Interface (Std 488-1978). All status including the option complement are available remotely. The Store/Recall memory data may be transferred via an external controller. In talk-only, the appropriate commands are generated when the front panel step-up and step-down entries are made to control another 6062A, 6060A, 6060B, 6070A, or 6071A. (6070/71A only have FREQUENCY STEP.)

FREQUENCY DRIFT <1 ppm/hr after 1 hour warmup at constant ambient temperature using internal free-air crystal.

PULSE MODULATION

PULSE DELAY.....	OFF/ON	80 nsec typ
	ON/OFF	65 nsec typ

Section 2

Installation and Operation

2-1. INTRODUCTION

This section describes how to install and operate the Generator. This section contains information for an initial inspection, setting up the instrument, general operating information, and local and remote operation.

2-2. INITIAL INSPECTION

The Generator is shipped in a special protective container that should prevent damage during shipment. Check the shipping order against the contents of the container, and report any damage or short shipment to the place of purchase or the nearest Fluke Technical Service Center. Instructions for inspection and claims are included on the shipping container.

If reshipment of the Generator is necessary, please use the original shipping container. If the original container is not available, use a container that provides adequate protection during shipment. It is recommended that the Generator be protected by at least three inches of shock-absorbing material on all sides of the container. Do not use loose fill to pad the shipping container. Loose fill allows the Generator to settle to one corner of the shipping container, which could result in the Generator being damaged during shipment.

2-3. SETTING UP THE GENERATOR

The following paragraphs describe how to set up the Generator for operation. This information includes: line power requirements, line voltage selection procedures, fuse replacement procedures, and rack mounting instructions.

2-4. Line Power Requirements

The Generator uses a line voltage of 100 or 120V ac rms ($\pm 10\%$) with a 1.5A fuse; or 220V or 240V ac ($\pm 10\%$) with a 0.75A fuse. The line frequency must be between 48 to 63 Hz. The power consumption of the instrument is <180 VA with a full option complement.

2-5. Line Voltage and Fuse Selection

CAUTION

Verify that the intended line power source matches the line voltage setting of the Generator before plugging in the line power cord.

Refer to Figure 2-1 to set the line voltage of the Generator to match the available source. Figure 2-1 also shows how to replace the line fuse of the Generator. The correct fuse value for each of the four line voltages is listed on a plate attached to the rear panel of the Generator.

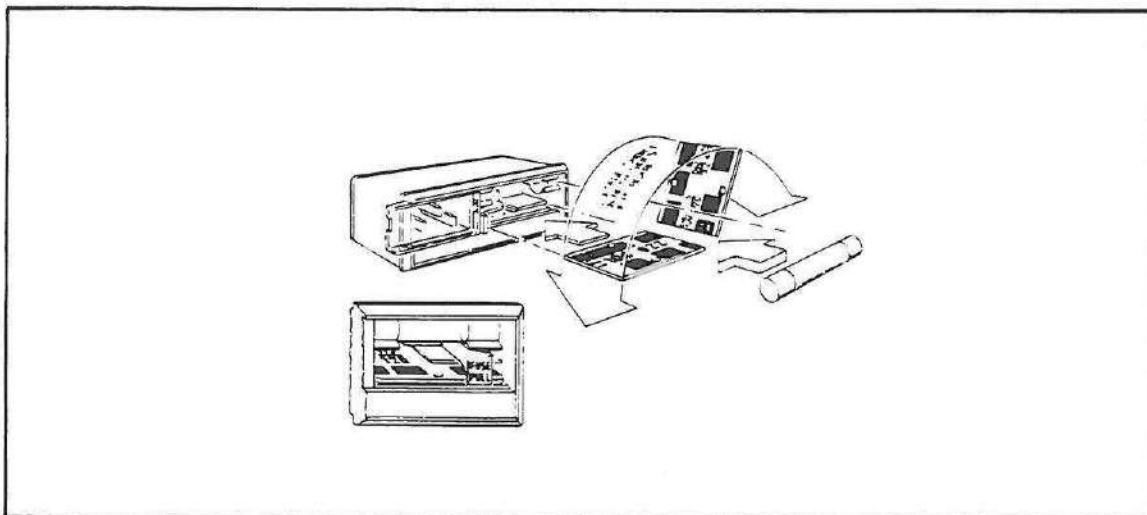


Figure 2-1. Fuse/Filter/Line Voltage Selection Assembly

2-6. IEEE-488 Address

The IEEE-488 address can be selected using the switches located next to the IEEE-488 connector on the rear panel. Talk-only and listen-only modes can also be selected on this switch.

NOTE

To meet the specified radiated emissions, the IEEE-488 connector must be terminated with a shielded IEEE-488 cable, such as a Fluke Y8021.

2-7. RACK OR BENCH MOUNTING THE GENERATOR

CAUTION

Allow at least 3 inches of clearance behind and on each side of the Generator to ensure proper air circulation.

The Generator may be placed directly on a work bench or mounted in a standard (24-inch deep) equipment rack. Use the Fluke Y6001 Rack Mount Kit for mounting the Generator on an equipment rack. Instructions for installing the Generator with the Rack Mount Kit are provided in the kit. The outside dimensions of the Generator are shown in Figure 2-2. The Rack Mount Kit is composed of the following parts:

- 5¼-inch Rack Adapter, P/N M05-205-600
- 24-inch Rack Slides, P/N M00-280-610

2-8. GENERAL OPERATING INFORMATION

The following paragraphs contain general information on the operation of the Generator. This includes all the information required to familiarize the operator with the instrument and the differences between local and remote operation.

2-9. Familiarization

The Generator normally operates on an internal reference oscillator. However, if desired, the Generator can be operated on an external reference by setting the rear panel REF INT/EXT switch to EXT and connecting the external reference to the REF IN connector.

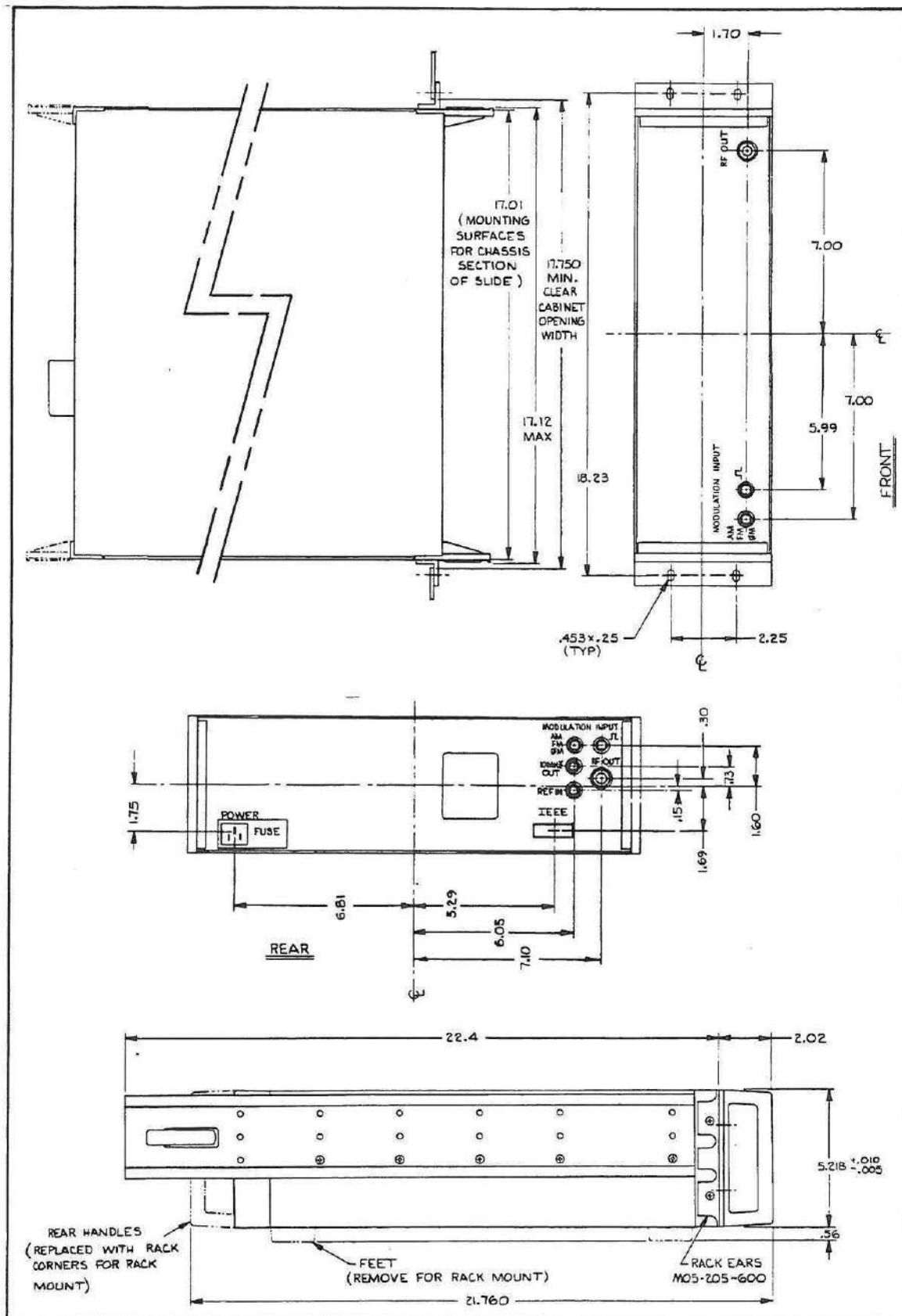


Figure 2-2. 6062A Outside Dimensions

CAUTION

When the Generator is operating on the internal reference, a 10-MHz signal is present at the 10 MHz OUT connector on the rear panel. To meet the specified radiated emissions, this connector must be terminated with a BNC non-shorting dust cap. A dust cap, JF 478982, is supplied with the Generator. If a cable is connected, it must be a double-shielded coaxial cable such as RG-223 terminated in a 50-ohm load.

CAUTION

Output spectral degradation occurs if the Generator is operated on internal reference with an external reference signal applied.

Figure 2-3 shows the front panel controls, indicator, and connectors and Table 2-1 describes the features.

Figure 2-4 shows the rear panel controls, connectors, and switches and Table 2-2 describes the features.

2-10. Local Versus Remote Operation

Two modes of controlling the output of the Generator are local operation and remote operation. In the local operation mode, the operator uses the keys on the front panel to control the Generator. The remote operation mode uses an IEEE-488 controller to control the Generator through the IEEE-488 Interface. An overview of local operation is presented under the heading Local and Remote Control Operations. The local and remote control operations that have similar entry methods are also described under Local and Remote Control Operations. The paragraphs under the heading Remote Operation contain information on commands or descriptions that pertain only to remote operations.

2-11. Power-On Sequence

When the Generator is turned on, a power-on sequence starts. During the power-on sequence, the microprocessor tests the analog circuitry, the program ROM, the scratch-pad RAM, the compensation memory, and the front panel displays. The front panel displays are tested by lighting all segments for a brief period at the same time the rest of the self tests are performed.

If any of the self tests fail, an error code is displayed. If the operator initiates any front panel entry before the power-on sequence is completed, the self test is aborted, and the Generator is set to the state it was in when turned off. Table 2-3 lists the Instrument Preset State. Power-on instrument settings that relate to the IEEE-488 Interface are described in the Remote Operation paragraphs in this section. More detail on the power-on self tests are explained in Section 4D.

2-12. Changing Output Parameters

The six parameters of the Generator (i.e., frequency, amplitude, amplitude modulation (AM), frequency modulation (FM), phase modulation (ϕ M), and pulse modulation) may be individually changed by at least one of three methods:

- Function Entry
- Bright-Digit Edit Entry
- Step Entry

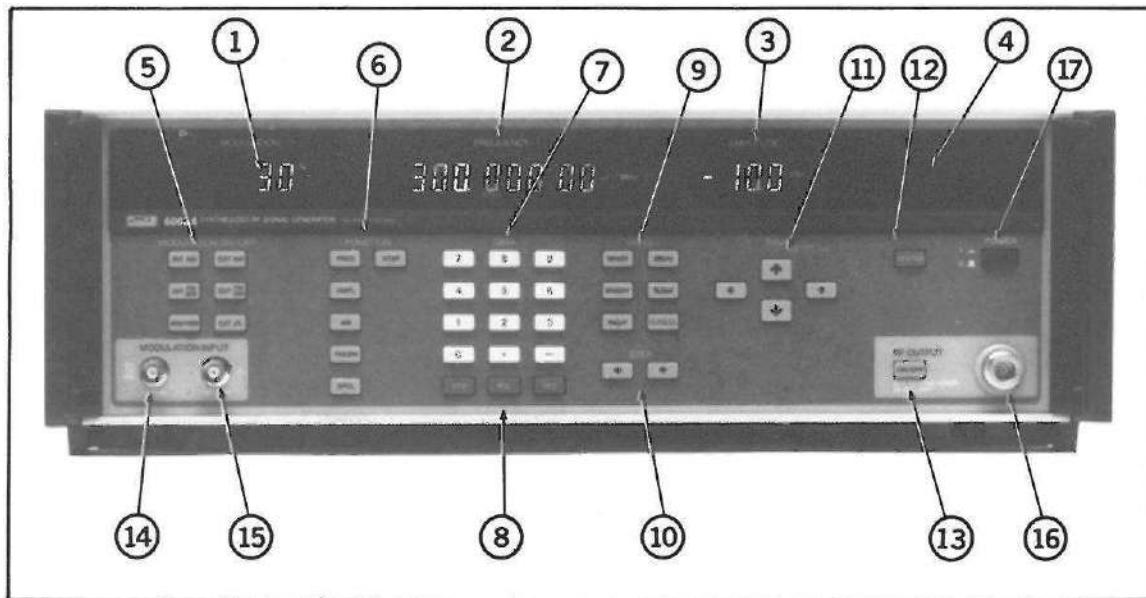


Figure 2-3. Front Panel Controls, Indicators, and Connectors

Table 2-1. Front Panel Controls, Indicators, and Connectors



1	MODULATION DISPLAY FIELD	A three-digit display, with associated indicators used to display the AM depth, FM deviation, source of modulation signal, and modulation frequency.
	INT AM	Indicates that the internal modulation oscillator signal is amplitude modulating the Generator.
	EXT AM	Indicates that the Generator is amplitude modulated by the signal connected to the MOD INPUT connector.
	INT FM	Indicates that the internal modulation oscillator signal is frequency modulating the Generator.
	EXT FM	Indicates that the Generator is frequency modulated by the signal connected to the MOD INPUT connector.
	INT \emptyset M	Indicates that the internal modulation oscillator signal is phase modulating the Generator.
	EXT \emptyset M	Indicates that the Generator is phase modulated by the signal connected to the MOD INPUT connector.
	INT 	Indicates that the internal modulation oscillator signal is pulse modulating the Generator.
	EXT 	Indicates that the Generator is pulse modulated by the signal connected to the PULSE MODULATION INPUT connector.
	STEP	Indicates that the Step [^] or [v] keys (Step Entry) affect the current Modulation display value.
	%	Indicates that the value displayed is the AM Depth in percent.
	kHz DEV	Indicates that the value displayed is the FM Deviation in kHz.
	rad	Indicates that the value displayed is the Phase Modulation Deviation in radians.

Table 2-1. Front Panel Controls, Indicators, and Connectors (cont)

dBm	Indicates that the value displayed is the target level in dBm when performing a level compensation procedure.
400 Hz	Indicates that the internal modulating frequency is 400 Hz.
1000 Hz	Indicates that the internal modulating frequency is 1000 Hz.
EXT HI	Indicates that the external modulation signal is more than 2% above the nominal 1V peak requirement for calibrated operation.
EXT LO	Indicates that the external modulation signal is more than 2% below the nominal 1V peak input requirement.
② FREQUENCY DISPLAY FIELD	A 9-digit display, with six indicators used to display the output frequency of the Generator. Also used to display the special function code, status error codes, or the memory location being stored or recalled, as well as relative and actual frequency, when REL is lit, and step frequency.
STEP	Indicates that the STEP [^] or [v] keys (Step Entry) affect the output frequency.
REL	Indicates that the displayed frequency is relative to a reference frequency.
COMP	Indicates that the compensation memory protection switch on the A2 Controller PCA is set to the enabled position.
FM	Indicates that an FM compensation procedure is in progress
OUT	Indicates that an Output compensation procedure is in progress
ATT	Indicates that an Attenuator compensation procedure is in progress.
③ AMPLITUDE DISPLAY FIELD	A 3 1/2- (and sign) digit display, with six indicators, used to display the output amplitude of the Generator into a 50-ohm Load.
STEP	Indicates that the STEP [^] or [v] keys (Step Entry) affect the output amplitude.
REL	Indicates that the displayed amplitude is relative to a reference amplitude.
dBm	Indicates that the output amplitude is in decibels relative to one milliwatt.
V	Indicates that the output amplitude is in volts.
uV	Indicates that the output amplitude is in microvolts.
mV	Indicates that the output amplitude is in millivolts.
④ STATUS DISPLAY FIELD	The status display field is composed of 11 indicators used to denote the current status of the Generator or instrument entry.
EXT REF	Indicates that the rear panel REF switch is in the EXT (external) position.

Table 2-1. Front Panel Controls, Indicators, and Connectors (cont)


REJ ENTRY	Lights when an invalid entry is made.
UNCAL	Lights when a parameter entry is outside its specified range. This indicator flashes when any of the internal DAC's are over or under-flow or when any abnormal operation is detected.
RF OFF	Lights when the RF OUTPUT is disabled.
REMOTE	Lights when the Generator is in the remote (IEEE-488 Interface) mode of operation.
ADDR	Lights when the Generator is addressed to listen or talk.
SRQ	Lights when the Generator has asserted the IEEE-488 SRQ signal.
⑤ MODULATION ON/OFF	Used to select type, source, and frequency of modulation. With the exception of the [400/1000] key, these keys operate as independent push-on/push-off switches for the given function. Any combination is allowed.
INT AM	Enables internal amplitude modulation at the frequency annunciated by the 400/1000 Hz indicator.
INT FM ØM	Enables internal frequency modulation at the frequency annunciated by the 400/1000 Hz indicator.
EXT AM	Enables external amplitude modulation using the signal applied to the MOD INPUT connector.
EXT FM ØM	Enables external frequency modulation using the signal applied to the MOD INPUT connector.
EXT 	Enables external pulse modulation using the signal applied to the PULSE MODULATION INPUT connector.
400/1000	Alternately sets the internal modulation oscillator's frequency to 400 or 1000 Hz. Selected frequency is displayed only when INT AM, FM, ØM, or internal pulse is enabled.
⑥ FUNCTION	With the exception of the [STEP] and [SPCL] keys, these keys operate as interlocked switches that select the parameter to be entered or edited. For the [FREQ], [AMPL], [AM], and [FM] FUNCTION keys, the bright digit appears in the corresponding display of the selected function.
FREQ	Selects the frequency parameter of the Generator to be programmed by using the DATA, EDIT, or STEP entry keys.
AMPL	Selects the amplitude parameter of the Generator to be programmed by using the DATA, EDIT, or STEP entry keys.
AM	Selects the amplitude modulation (AM) parameter of the Generator to be programmed by using the DATA, EDIT, or STEP entry keys.
FM ØM	Selects the frequency modulation (FM), or phase modulation (ØM) parameter of the Generator to be programmed by using the DATA, EDIT, or STEP entry keys.

Table 2-1. Front Panel Controls, Indicators, and Connectors (cont)

SPCL	Enables the special function mode. Special functions are called up by a two-digit code, that is entered by using the DATA keys. Refer to the paragraphs on Special Function in this section for a detailed description and a list of the special functions.
STEP	After one of the four parameter functions has been selected for programming, pressing this key allows you to program a step-wise change to that parameter. The step increase or decrease is then performed every time the STEP [^] or [v] keys are pressed.
⑦ DATA	A ten-digit (plus sign and decimal key) keypad used for entering a parameter's value, the special function code, or a memory recall/store location.
⑧ Memory	
STO	Used with the DATA keys to store the current instrument state in a memory location. Memory locations 01 through 50 are available.
RCL	Used with the DATA keys to recall an instrument state from a memory location. Memory locations 01 through 50 are available for operator-stored states; memory location 98 contains the Instrument Preset State (see Table 2-3.)
SEQ	Sequentially recalls, in increasing location order, the instrument states stored in memory. While the [SEQ] key is pressed, successive memory locations are displayed. When the key is released, the location last displayed is recalled.
⑨ UNITS	These keys, with the exception of [CLR/LCL], serve as the terminating keystroke of a function entry, thereby causing the Generator to be programmed. The amplitude units keys are also used during Amplitude Units Conversion entries.
MHz V	Used with the [FREQ], [FM], and [AMPL] function keys to program the numerical DATA entries in terms of megahertz (frequency or frequency modulation) or volts (amplitude).
dB(m)	Used with the [AMPL] function key to program the numerical DATA entries in terms of decibels per milliwatt.
kHz mV	Used with the [FREQ], [FM], and [AMPL] function keys to program the numerical DATA in terms of kilohertz (frequency or frequency modulation) or millivolts (amplitude).
% rad	Used with the [AM] function key to program the numerical DATA entries in terms of percentage AM depth. Also used with the [FM ØM] function key to program the numerical DATA entries in terms of radians.
Hz uV	Used with the [FREQ], [FM], and [AMPL] function keys to program the numerical DATA in terms of hertz (frequency or frequency modulation) or microvolts (amplitude).
CLR LCL	When the Generator is in local operation, this key is used to clear the current entry and returns the Generator to the previous state. When the instrument is in remote operation, this key is used to return local control.

Table 2-1. Front Panel Controls, Indicators, and Connectors (cont)

⑩	STEP	These two keys work in conjunction with the STEP Function key. These keys repeat while they remain pressed.
	[^]	After a parameter is set to the step function mode, and the STEP indicator appears in the display field, this key increments the parameter by the step value previously programmed.
	[v]	After a parameter is set to the step function mode, and the STEP indicator appears in the display field, this key decrements the parameter by the step value previously programmed.
⑪	EDIT	These keys are used to position the bright digit within a display field and to increase or decrease the bright digit value. All four keys repeat while they remain pressed. The function keys are used to move the bright digit to the desired display field.
	[^]	Increases the bright-digit value.
	[<]	Moves the bright digit one digit to the left.
	[v]	Decreases the bright-digit value.
	[>]	Moves the bright digit one digit to the right.
⑫	STATUS	A push and hold key that displays the Uncal and Reject Entry status codes in the MODULATION, FREQUENCY, and AMPLITUDE display fields.
⑬	ON/OFF	A push-on/push-off key (with a corresponding RF OFF indicator in the STATUS display field) that enables or disables the output of the Generator.
⑭	Connector	A BNC connector for input of a 1V peak, external modulation signal.
⑮	Connector	A BNC connector for input of a 1V peak, external pulse modulation signal.
⑯	Connector	A standard RF connector at the output of the Generator.
⑰	POWER	A push-on/push-off detent switch that applies line power to the Generator.

These different methods all accomplish the same result but use different approaches for different reasons. One is to reduce the chance of error during complex test procedures that require continuously resetting parameters. Another is in the case when a test is partly under remote control and only some of the parameters require changes.

2-13. FUNCTION ENTRY

Changing an instrument parameter with the Function Entry method consists of:

- Selecting the function to be changed
- Entering the new numerical value of the parameter
- Selecting the units of the numerical value (megahertz, millivolts, etc).

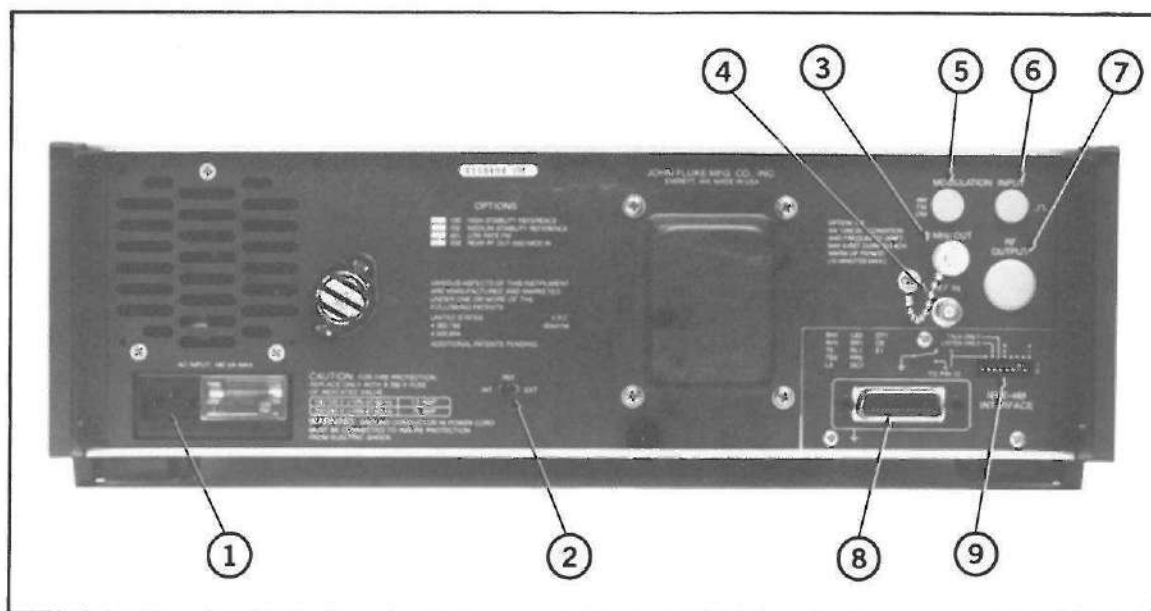


Figure 2-4. Rear Panel Controls, Connectors, and Switches

Table 2-2. Rear Panel Controls, Connectors, and Switches

①	AC INPUT	Permits operation from 100, 120, 220, or 240V ac. The number visible through the window on the selector card indicates the nominal line voltage to which the Generator must be connected. The line voltage is selected by orienting the selector card appropriately. A 1 1/2-ampere fuse is required for 100/120V operation and a 3/4-ampere fuse is required for 220/240V operation.
②	REF INT/EXT	Permits selection of the Generator frequency reference. When set to INT, the Generator operates on the internal reference, which is either the standard oscillator, the high-stability oscillator (if the Option -130 is installed) or the medium-stability oscillator (if the Option -132 is installed). In any case, the internal 10-MHz reference signal is available at the 10 MHz OUT connector. When set to EXT, the Generator reference is the 10-MHz signal applied to the external REF IN connector.
③	10 MHz IN/OUT	Connector (BNC) provides the 10-MHz reference signal.
④	REF IN	Connector (BNC) is present to accept a 1-, 2-, 2.5-, 5-, or 10-MHz, 0.3 to 4V p-p sine or square wave signal into nominally 50 ohms.
⑤	MOD INPUT	Connector (BNC) is present only with the Option -830 to accept a 1V peak external modulation signal.
⑥	MOD INPUT	Connector (BNC) is present only with the Option -830 to accept a 1V peak external pulse modulation signal.
⑦	RF OUTPUT	Connector (type N) is present only with the Option -830 to provide the Generator output signal.
⑧	IEEE CONNECTOR	Allows remote operation of the Generator via the IEEE-488 bus.
⑨	IEEE ADDRESS SWITCH	Allows the selection of the Generator bus address.

Table 2-3. Instrument Preset State

FUNCTION	SETTING
Frequency	300.00000 MHz
Frequency Step	1.00000 MHz
Amplitude	-10.0 dBm
Amplitude Step	1 dB
Modulation Rate	1000 Hz
AM Depth	30%
AM Depth Step	1 %
FM Deviation without/with Option -651	5.0/.5 kHz
FM Deviation Step without/with Option -651	100/10 Hz
Modulation Display	AM Depth
Bright-Digit Location	Frequency Bright Digit
Frequency Bright-Digit Position	1 MHz
Amplitude Bright-Digit Position	1 dBm
AM Bright-Digit Position	1 %
FM Bright-Digit Position without/with Option -651	10/1 Hz
Special Functions	20,30,40,50,60,70,80,90
INT AM	Off
EXT AM	Off
INT FM	Off
EXT FM	Off
EXT PULSE	Off
Step Function	Frequency Step

The command syntax for function entries is:

Select Function — Enter Data — Select Unit

1. Select one of the four parameters using the FUNCTION keys. The bright digit appears in the corresponding display field. The presence of the bright digit in the display field indicates that the value of the selected parameter is ready to be programmed or changed.
2. Enter the numeric data with the DATA keys. The numerics appear in the appropriate display field.
3. Select a UNITS key. This gives the data its absolute value and causes the microprocessor to internally program the Generator to the new state.

For the amplitude and frequency functions, the entered data programs the displayed value. If the relative mode is enabled, the displayed value may be different from the actual output value.

Once a function is selected, the selected parameter or feature remains in the active programming mode until a new function is selected. Data for a selected parameter must be followed by a unit value and must be within the range specified for the function. The display field flashes, and the REJ ENTRY status indicator flashes if the entered data is not within the specified range. A rejected entry does not affect the output of the Generator. The output of the Generator remains at its previous values until a new value is accepted.

The function entry may be terminated at any time by the [CLR|LCL] key or by selecting another function.

2-14. BRIGHT-DIGIT EDIT ENTRY

Changing an instrument parameter by the bright-digit edit entry method is the fastest way to make vernier (incremental) changes to one of the four parameters. The EDIT keys are used

with the four parameter FUNCTION keys to position the bright digit in the desired display field and then increase or decrease the bright-digit value.

The command syntax for bright-digit edit entries is:

Select Display Field — Position Bright Digit — Change Bright-Digit Value

1. Use one of the four FUNCTION keys to position the bright digit in the appropriate display field.
2. Use the [→] or [←] EDIT keys to position the bright digit to the desired resolution, and use the [↑] or [↓] EDIT keys to increase or decrease the value of the bright digit.

The position of the bright digit within a display field is maintained when the bright digit is moved from one display field to another and then back to the original display field.

The repeat rate of the [↑] or [↓] EDIT keys may be changed to a faster or slower rate (a medium repeat rate is the default) with a special function code (see Table 2-4). Refer to the paragraphs on Special Function later in this section for the method and code.

2-15. STEP ENTRY

Changing parameters by the step entry method allows the operator to preset stepwise increments of a parameter, then change that parameter (by the amount programmed in the step function) with a single keystroke.

The command syntax for step entries is:

Select Step Function — Enter Data — Select Units — Change Parameter

1. Select the parameter to be changed stepwise using one of the FUNCTION keys followed by the [STEP] key to enable the step function.
2. Program the numeric step amount using the DATA keys.
3. Select a UNIT key to give the data its absolute value.
4. The parameter value can now be changed by increments of the programmed step amount by using the [↑] or [↓] STEP keys.

While the [STEP] key is pressed, the display field of the selected parameter shows the step amount. The STEP indicator is lit in the display field currently affected by the [STEP] key.

The repeat rate of the [↑] or [↓] STEP keys may be changed to a faster or slower rate (a medium repeat rate is the default) with a Special Function code. Refer to the paragraphs on Special Functions for the method and code.

A step entry is ignored when the result of that step entry would cause the value of the parameter to exceed its programmable limit.

2-16. Status and Clear

The [STATUS] key allows the operator to interrogate the Generator for an explanation of uncalibrated or rejected entry operation (UNCAL or REJ ENTRY indicator is lit). Refer to the paragraphs on Status and Clear Entry later in this section for a complete list of status codes.

Table 2-4. Special Functions

SPECIAL FUNCTION	OPERATION
00	Clear all special functions.
02	Initiate self tests.
03	Display test. See Section 3.
04	Key test. See Section 3.
07	Set SRQ.
08	Reset SRQ.
09	Display software revision level. Information appears in the MODULATION and FREQUENCY display fields for 5 seconds or until another key is pressed.
10	Display IEEE-488 mode and address
11	Display self-test results. Zeros in the display fields indicate that the self tests have passed. See Section 4.
12	Turn on displays.
13	Turn off all displays. All other functions still operate.
14	Initialize memory locations to Instrument Preset State. Sto ? appears in the FREQUENCY display field for 5 seconds. If during this time, the [STO] key is Pressed, all memory locations are initialized.
15	Latch test. See Section 4.
16	Display option loading. See Interrogate Commands in this section.
17	Initiate self test with RF on. See Section 4.
20	Disable Relative Frequency. See Relative Function in this section.
21	Enable Relative Frequency. See Relative Function in this section.
30	Disable Relative Amplitude. See Relative Function in this section.
31	Enable Relative Amplitude. See Relative Function in this section.
40	Disable Internal Pulse Modulation. See Modulation Entry in this section
41	Enable Internal Pulse Modulation. See Modulation Entry in this section
50	Select dBm Amplitude Display Mode. See Amplitude Units Selection in this section.
51	Select dBmV Amplitude Display Mode. See Amplitude Units Selection in this section.
52	Select dBuV Amplitude Display Mode. See Amplitude Units Selection in this section.
60	Disable DC AM mode. See Modulation Entry in this section.
61	Enable DC AM mode. See Modulation Entry in this section.
70	Set repeat rate for EDIT and STEP keys to medium.
71	Set repeat rate for EDIT and STEP keys to fast.
72	Set repeat rate for EDIT and STEP keys to slow.
75	Display Compensation Memory status. See Section 4.
76	Repair Compensation Memory Errors. See Section 4.
77	Load FM MEC PROM data into Compensation Memory. See Section 4
78	Load Output MEC PROM data into Compensation Memory. See section 4.
79	Load Attenuator MEC PROM data into Compensation Memory. See section 4.
80	Enable Amplitude correction. Normal operation.
81	Disable Amplitude correction. RF output level may be up to 12 dB low.
82	Disable attenuator correction. Useful as a troubleshooting tool. RF input to attenuator is flat.
83	Program alternate 24 dB attenuation. See Section 4.
84	Program alternate 24 dB attenuation. See Section 4.
85	Program alternate 24 dB attenuation. See Section 4.
86	Program alternate 24 dB attenuation. See Section 4.
90	Disable Amplitude Fixed Range. See Amplitude Fixed Range in this section.
91	Enable Amplitude Fixed Range. See Amplitude Fixed Range in this section.
95	Initiate FM Keyboard Compensation Procedure. See Section 4E.
96	Initiate Output Keyboard Compensation Procedure. See Section 4E.
97	Initiate Attenuator Keyboard Compensation Procedure. See Section 4E.
98	Initiate Output with Default Attenuator Data Keyboard Compensation Procedure. See Section 4E.

The [CLR|LCL] key may be used to clear a partial DATA entry or clear the flashing REJ ENTRY indicator.

2-17. RF Output On/Off

The RF OUTPUT [ON/OFF] key allows the operator to enable or disable the RF output of the Generator. This feature is useful in zeroing a power meter, finding the noise floor of a system, or determining the presence or source of an unknown signal.

At power-on, the RF output of the Generator assumes the state it was in when the Generator was turned off. Pressing the RF OUTPUT [ON/OFF] key disables the output of the Generator and causes the RF OFF indicator (in the STATUS display field) to light. If the RF ON/OFF was off at power-on, pressing the [ON/OFF] key will enable output.

2-18. Modulation On/Off and Rate

The MODULATION ON/OFF keys allow the operator to select any combination of modulation or no modulation. The MODULATION display field indicates what combination of modulation has been selected. Each modulation key is a push-on push-off type (except the [400/1000] key).

The [400/1000] key toggles the internal modulation oscillator between 400 and 1000 Hz. The 400 Hz and 1000 Hz indicators are lit only when Internal AM, FM, ϕ M, or Pulse modulation is enabled.

2-19. Memory

Memory entry using the [STO] key allows the operator to save up to 50 complete front panel settings for later recall. No memory location needs to be specified for the sequence operation.

The command syntax for memory entries is:

Select Memory Function — Enter Memory Location

1. Store the current front panel setting by pressing the [STO] key (located below the DATA keys). The last memory location stored or recalled is displayed in the FREQUENCY display field.
2. Use the DATA keys to enter the two-digit memory location code. The location code must contain both digits (e.g., 01, 02, ...50). The two-digit code appears in the FREQUENCY display field as it is entered.
3. Recall a front panel setting by pressing the [RCL] key (located below the DATA keys). The last memory location stored or recalled is displayed in the FREQUENCY display field.
4. Use the DATA keys to enter the memory location code of the desired front panel setting. Remember, the location code must contain both digits of the memory location code.

Memory location 98 contains the Instrument Preset State that can be recalled at any time.

The [SEQ] key allows the front panel settings stored in memory to be sequentially recalled. This process is activated by pressing the [SEQ] key at any time. When the [SEQ] key is pressed, the memory location code of the currently recalled setting appears in the FREQUENCY display field, and the location is recalled. When the last memory location is reached (50), the [SEQ] key starts over at 01. The [SEQ] key repeats while pressed.

2-20. Software Compensation Procedures

The software compensation procedures allow the operator to update the instrument-specific compensation data after making related repairs. These procedures are easy to use and may be performed via the front panel (local) or under computer control (remote) through the IEEE-488 Interface. The local procedures enable secondary functions for many of the keys. Each remote procedure enables a special set of compensation commands and disallows much of the standard IEEE-488 command set. See Section 4E, Software Compensation Procedures, for details.

2-21. Special Function

Special function entries allow the operator to enable several special operating functions in the Generator. For example, special functions allow the operator to change the repeat rate of the STEP and EDIT keys, start the self tests, display the results of the power-on self tests, display the IEEE-488 address, enable relative and fixed-range features, and disable or enable special attenuation features. A complete list of the special functions available is presented in Table 2-4.

The command syntax for special function entries is:

Select Special Function — Enter Special Function Code

1. Select the special function by pressing the [SPCL] key.
2. The special function code is entered using the DATA keys.

2-22. LOCAL AND REMOTE CONTROL OPERATIONS

The following paragraphs describe local and remote operations that have similar entry methods for each Generator function. The functions are arranged in alphabetical order. The syntax of the command and allowable data ranges are for each function. Other information is also presented.

2-23. Amplitude and Frequency Entry

The following information describes how to control the carrier frequency and amplitude by the FUNCTION-DATA-UNIT entry sequence. This method applies to both normal and relative operations. The frequency display is a fixed-point display in MHz. The amplitude display is fixed point while displaying dBm but is floating point when displaying voltage units.

The RF OUTPUT [ON/OFF] must be enabled for the Generator to produce an output. (See the reference material on RF OUTPUT ON/OFF Entry.)

Command Syntax:

Select Function — Enter Data — Select Unit

Summary:

	COMMAND	RANGE	RESOLUTION	NOTES
Set Frequency				
Local:	[FREQ] -- DATA -- [MHz V] [kHz mV] [Hz uV]	0.1 to 2100 MHz	10/20 Hz	1,2,3
Remote:	"FR" -- float -- "GZ" "MZ" "KZ" "HZ"	0.1 to 2100 MHz	10/20 Hz	1,2,4
Set Amplitude				
Local:	[AMPL] -- DATA -- [dB(m)] [MHz V] [kHz mV] [Hz uV]	-137 to +13/16 dBm 0.032 uV to 1.41/1.58 V 3 digits	0.1 dBm	3,5,6
Remote:	"AP" -- float -- "DB" "V" "MV" "UV" "NV"	-137 to +13/16 dBm 0.032 uV to 1.41/1.58 V 3 digits	0.1 dBm	4,5,6

Example:

Set Frequency to 10.7 MHz and Amplitude to -7.5 dBm.

Local: [FREQ] [1] [0] [.] [7] [MHz|V]
[AMPL] [-] [7] [.] [5] [dB(m)]

Remote: "FR10.7MZ,AP-7.5DB"

Notes:

1. Frequency ranging occurs at 245, 512, and 1050 MHz.
2. Frequency may be programmed with 10 Hz resolution when frequency <1050 MHz, and with 20 Hz resolution when frequency ≥1050 MHz.
3. FUNCTION ([FREQ] or [AMPL]) remains selected until another FUNCTION or [STEP], [STO], [RCL], or [SPCL] is pressed.
4. Floating-point number is indicated by the word float.
5. Amplitude uncalibrated range from -147.4 to -137.1 dBm. Also uncalibrated from +16.1 to +17 dBm when frequency <1050 MHz, and from +13.1 to +17 dBm when frequency ≥1050 MHz.
6. Amplitude ranging occurs at 1/2V, 1/4V, 1/8V, ... 1/2²³V with AM off and 1/4V, 1/8V, 1/16V, ... 1/2²⁴V with AM on.

Related Operations:

- Amplitude Fixed Range
- Amplitude Units Conversion
- Amplitude Units Selection
- Bright-Digit Edit Entry
- Relative Function
- Step Entry

2-24. Amplitude Fixed Range

The following information describes how to use the Fixed-Range special function. This special function fixes the current amplitude range (holds the currently selected step of the Step Attenuator). This function allows monotonic and nontransient level control over a limited range around those levels where the Step Attenuator normally autoranges. This level control may be accomplished with the Bright-Digit Edit Entry only.

The level vernier in fixed range has at least 18 dB of range (the specified accuracy range is 12 dB).

Command Syntax:

Select Fixed Range — Enable or Disable

Summary:

COMMAND	NOTES
Enable Fixed Range	
Local: [SPCL] [9] -- [1]	1
Remote: "SP" "9" -- "1"	
Disable Fixed Range	
Local: [SPCL] [9] -- [0]	2
Remote: "SP" "9" -- "0"	

Example:

Set the Generator for monotonic and nontransient amplitude control (Bright-Digit Edit only) over the range of the vernier level control below 0.25V.

Local: [AMPL] [.] [2] [5] [MHz|V] [SPCL] [9] [1]
 Remote: "AP.25V,SP91"

Notes:

- 1 The amplitude range is fixed only for Bright-Digit Edit operations. Other methods of changing the amplitude cause the step attenuator to autorange if necessary.
- 2 With amplitude fixed range disabled, amplitude ranging occurs at 1/2V, 1/4V, 1/8V, ... 1/2²³V with AM off and 1/4V, 1/8V, 1/16V, ... 1/2²⁴V with AM on.

Related Operations:

Bright-Digit Edit Entry
Relative Function

2-25. Amplitude Units Conversion

The following information describes how to convert the displayed amplitude level from dBm to volts and from volts to dBm. The output of the Generator does not change during these operations.

Command Syntax:

Select Amplitude Function — Select Unit

Summary:

	COMMAND	NOTE
Convert dBm to volts:		
Local:	[AMPL] -- [MHz V] [kHz mV] [Hz uV]	1
Remote: "AP"	-- "V" "MV" "UV" "NV"	1
Convert volts to dBm:		
Local:	[AMPL] -- [dB(m)]	
Remote: "AP"	-- "dB"	

Example:

Change the displayed amplitude of -10.0 dBm to its voltage equivalent.

Local: [AMPL] [MHz|V]
Remote: "APV"

Note:

1. Any voltage unit is accepted since the microprocessor automatically selects the units appropriate for the value being displayed.

Related Operations:

Relative Function
Amplitude Units Selection

2-26. Amplitude Units Selection

The following information describes how to select the alternate level display units. When the level is displayed as a dB quantity, units of dBm, dBmV or dB μ V may be selected by special function. Entering the special function code converts the displayed quantity to the selected units but does not change the output of the Generator.

Command Syntax:

Select Amplitude Units — Select Unit

Summary:

COMMAND	NOTE
Select dBm Display Units	
Local: [SPCL] [5] -- [0]	
Remote: "SP" "5" -- "0"	
Select dBmV Display Units	
Local: [SPCL] [5] -- [1]	
Remote: "SP" "5" -- "1"	
Select dBuV Display Units	
Local: [SPCL] [5] -- [2]	
Remote: "SP" "5" -- "2"	

2-27. Bright-Digit Edit Entry

The following information describes how to use a Bright-Digit Edit Entry to change an instrument parameter. The output frequency, amplitude, and modulation indexes can be modified with this entry method.

The RF OUTPUT [ON/OFF] must be enabled for the Generator to produce an output. (See the reference material on RF OUTPUT [ON/OFF] Entry.)

Command Syntax:

Select Display Field — Position Bright Digit — Change Bright-Digit Value

Summary:

COMMAND	NOTES
Edit Frequency	
Local: [FREQ] -- EDIT [<]/[>] -- EDIT [^]/[v]	1,2
Remote: "FB" -- float "GZ" -- "KF" float	3,4,5
"MZ"	
"KZ"	
"HZ"	

INSTALLATION AND OPERATION

Edit Amplitude

Local: [AMPL] -- EDIT [<]/[>] -- EDIT [^]/[v] 1,2
Remote: "AB" -- float "DB" -- "KA" float 3,4,5
 "V"
 "MV"
 "UV"
 "NV"

Edit FM/ØM Deviation

Local: [FM|ØM] -- EDIT [<]/[>] -- EDIT [^]/[v] 1,2
Remote: "DB" -- float "GZ" -- "KD" float 3,4,5
 "MZ"
 "KZ"
 "HZ"
 "RD"

Edit AM Depth

Local: [AM] -- EDIT [<]/[>] -- EDIT [^]/[v] 1,2
Remote: "PB" -- float "PC" -- "KP" float 3,4,5

Example 1:

Edit the displayed amplitude of 9.7 to 10.0 dBm.

Local: Put the bright digit in the amplitude display by pressing [AMPL]. Select the least significant digit in that display by pressing EDIT [>] until the bright digit is on that digit. Increase the value of that digit by pressing EDIT [^] three times.

Remote: "AB.1DB,KA3"

Example 2:

Edit the displayed FM Deviation from 5.0 to 3.0 kHz.

Local: Put the bright digit in the FM display by pressing [FM]. Select the 1-kHz digit by pressing the EDIT [>] or EDIT [<] until the bright digit is on that digit. Decrease the value of that digit by pressing EDIT [v] twice.

Remote: "DB1KZ,KD-2"

Notes:

1. The bright-digit field remains selected until another display field is selected.
2. The bright-digit position is maintained for each of the four functions so that the bright digit can be moved from one display to another and back without losing its position in the previous display field.
3. Floating-point number is indicated by the word float.

4. In remote, the bright digit is positioned within a display field using a decade value and associated unit. Minus signs are ignored.
5. In remote, the bright digit is moved to the corresponding field and is increased or decreased by the signed integer following the "KF,KA,KD,KP" messages. The generic edit command "KB" may also be used to edit up or down the current bright-digit position. Positive integers do not require a sign.

Related Operations:

Relative Function
Amplitude Fixed Range

2-28. Memory Entry

The following information describes how to use the memory function to store and recall front panel settings. The Generator has 50 memory locations that are retained for 2 years with the power off.

The sequence feature allows the operator to recall successive memory locations.

Command Syntax:

Select Memory Function — Enter Memory Location

Summary:

	COMMAND	NOTES
Store		
Local:	[ST0] — [n] [n]	1,2,3
Remote:	"ST" -- int	1,4
Recall		
Local:	[RCL] — [n] [n]	1,2,3
Remote:	"RC" -- int	1,4
Sequence		
Local:	[SEQ]	5,6
Remote:	"SQ"	5

Example:

Recall the Instrument Preset State (located in memory location 98). Change the frequency parameter to 302 MHz, then store the new front panel setting in memory location 06.

Local: [RCL] [9] [8] EDIT [^] [^] [ST0] [0] [6]
Remote: "RC98,KF2,ST6"

Notes:

1. The memory locations available for operator use are 01 through 50. Additionally, the following special memory locations are available:

Memory location 00 contains a backup-memory location. After a recall (or sequence) operation it contains the last front panel setting. After a store operation, it contains the data in the stored memory location before the store operation. Thus, a recall operation can be reversed by recalling location 00.

Memory location 98 contains the Instrument Preset State.

Memory location 99 contains the present instrument state.

2. In local control, two data digits must be entered to specify the memory location. The recall or store is performed when the second digit is released.
3. The last memory location specified (used for sequence operations) is displayed while the [STO] or [RCL] button is pressed.
4. An unsigned integer is indicated with int.
5. The sequence operation recalls the next higher memory location, starting from the last memory location stored or recalled. No memory location needs to be specified. When the highest location is reached, the sequence starts over again at location 01.
6. While [SEQ] is pressed, the next memory location number is displayed and the memory location is recalled. This key is repeating.

2-29. Modulation Entry

The following information describes how to preset the modulation index (AM depth or FM/ ϕ M deviation), internal modulation rate (400 or 1000 Hz), and how to select the modulation source (internal and/or external).

The FUNCTION-DATA-UNIT method of selecting the modulation index is summarized in the following command syntax. The indices may also be modified using Bright-Digit Edit or Step Entry. Since there is only one modulation display, the modulation index displayed is determined by the last modulation FUNCTION key pressed.

Command Syntax:

Select Function — Enter Data — Select Unit

Summary:

	COMMAND	RANGE	RESOLUTION	NOTES
Set AM Depth				
	Local: [AM] -- DATA -- [%]	0 to 99%	1%	1,2
	Remote: "AM" -- float -- "PC"	0 to 99%	1%	1,2,3
Set FM Deviation				


Local: [FM|ØM] -- DATA -- [MHz|V] 0 to 400 kHz 3 digits 1,4,5
 [kHz|mV]
 [Hz|uV]
 [%|rad] 0 to 40.0 rad 3 digits

Remote: "FM" -- float -- "GZ" 0 to 400 kHz 3 digits 1,3,4,5
 "MZ"
 "KZ"
 "HZ"
 "RD" 0 to 40.0 rad 3 digits

Summary:

COMMAND	NOTES
Select Modulation Rate	
Local: [400/1000]	6
Remote: "MR" "0" or "1"	7

Summary:

COMMAND	NOTES
Enable or Disable AM, DCAM, FM/ØM or Pulse Modulation	
Local: [INT AM] [INT FM ØM] [EXT AM] [EXT FM ØM] [EXT 	8,9
[SPCL] [4] -- [0] or [1]	10
[SPCL] [6] -- [0] or [1]	11
Remote: "AI" "0" or "1" "FI" "0" or "1" "AE" "0" or "1" "FE" "0" or "1" "PE" "0" or "1"	12
"PI" "0" or "1"	10,12
"DA" "0" or "1"	11,12

Example:

Set the FM deviation to 5 kHz, set the modulation rate to 400 Hz, and modulate the carrier internally.

Local: [FM|ØM] [5] [kHz] [INT FM] [400/1000]
 Remote: "FM5KZ,MRO,FI1"

Notes:

1. This operation does not change the Generator output unless the corresponding modulation is enabled.
2. Uncalibrated if peak amplitude exceeds +16 dBm when frequency < 1050 MHz, or +13 dBm when frequency ≥ 1050 MHz.
3. Floating-point number is indicated with the word float.
4. Uncalibrated if FM is enabled and FM deviation is above (frequency -150 kHz).
5. The maximum FM deviation is dependent on the output frequency. Deviations up to 400 kHz or 40.0 radians may be entered regardless of the output frequency; however, the UNCAL indicator is flashed if the limits specified in Table 4D-14 are exceeded. The maximum deviations are reduced to 40.0 kHz and 4.00 rad if the low-rate FM option is installed.
6. Toggles between 400 or 1000 Hz only. An indicator shows selected rate only if internal modulation is on.
7. "0" selects a modulation rate of 400 Hz; "1" selects 1000 Hz.
8. These are ON/OFF operations; any combination is allowed.
9. Two indicators EXT HI and EXT LO are lit when external AM or FM is on to indicate that the external modulation signal is 2% above or 2% below the nominal 1V peak input requirement.
10. Internal pulse modulation can be enabled by special function or by the alternate IEEE-488 command "PI". It can be enabled with any combination of the above modulations.
11. The dc coupled AM mode can be enabled by special function or by the alternate IEEE-488 command "DA". The dc coupled AM mode works in conjunction with External AM and only affects the output of the Generator if External AM is also enabled.
12. "0" turns the modulation source off; "1" turns it on.

Related Operations:

Bright-Digit Edit
Entry Step Entry

2-30. Relative Function

The following paragraphs describe how to change frequency and amplitude using the relative mode. There are two steps:

- 1 Setting the reference
- 2 Changing the parameter relative to that reference

Setting the reference is done by setting the parameter to the desired value and then enabling the relative mode for that parameter. This causes the REL indicator to light and the displayed value to be zero in the corresponding display. The Generator output does not change during these operations. In the relative mode, the usual means of changing the parameter may be used; i.e., Function, Step, or Bright-Digit Edit Entry.

In the relative frequency mode, the actual frequency is the sum of the reference and the displayed frequency. The actual frequency may be displayed by pressing the [FREQ] key. If the 10/20 Hz frequency resolution boundary is crossed using the step or edit functions while in the relative frequency mode, a 10-Hz rounding quantity may be applied to the displayed frequency.

In the relative amplitude mode, the actual amplitude is the sum of the reference and the displayed amplitude when the reference and the displayed quantities have the same units. However, with mixed units (volts and dB), the actual amplitude is the voltage value scaled by the dB value. The actual amplitude may be displayed by pressing the [AMPL] key.

Command Syntax:

Select Relative Function — Enable or Disable

Summary:

	COMMAND	NOTE
Frequency		
Local:	[SPCL] [2] -- [0] or [1]	1
Remote:	"SP" "2" -- "0" or "1"	1
Amplitude		
Local:	[SPCL] [3] -- [0] or [1]	1
Remote:	"SP" "3" -- "0" or "1"	1

Example:

Set the amplitude to -15 dBV; i.e., 15 dB below 1 volt.

Local: [AMPL] [1] [MHz|V] [SPCL] [3] [1] [AMPL] [-] [1] [5] [dB(m)]
 Remote: "AP1V,SP31,AP-15DB"

Note:

- 1 enables the relative function; 0 disables the relative function.

Related Operations:

Amplitude and Frequency Entry
 Bright-Digit Edit Entry
 Step Entry

2-31. RF OUTPUT ON/OFF Entry

The following information describes how to enable the output of the Generator using the RF OUTPUT [ON/OFF] key and the corresponding remote code.

Command Syntax:

RF Output On/Off

Summary:

COMMAND	NOTE
RF Output On	
Local: RF OUTPUT [ON/OFF] when RF OFF is on	1
Remote: "R01"	1
RF Output Off	
Local: RF OUTPUT [ON/OFF] when RF OFF is off	
Remote: "R00"	

Note:

1. Turning the RF Output on resets the RPP circuitry if it has tripped.

2-32. Special Function Entry

The following information describes how to use the Special Function Entry to use the special operating functions of the Signal Generator. Table 2-4 lists the special functions available.

The special function code is a two-digit number. The first digit indicates the classification of the special function, and the second digit specifies the particular special function.

The special function is executed when the second special function code digit is entered. There are ten classes of special functions. The special functions in the 0(n) and 1(n) class cause an action to be performed. Classes 2(n) through 9(n) cause an instrument state to change. The status of classes 2(n) through 9(n) appears (left to right) in the frequency display field when the [SPCL] key is pressed. Table 2-4 describes the special functions available with the Generator.

Command Syntax:

Select Special Function — Enter Special Function Code

COMMAND	NOTE
Local: [SPCL] -- [n] [n]	
Remote: "SP" -- int	1

Example:

Change the repeat rate of the EDIT and STEP keys to slow.

Local: [SPCL] [7] [2]

Remote: "SP72"

Note:

1. Unsigned integer is indicated with int.

Related Operations:

Fixed Range
Relative Function

2-33. Status and Clear Entry

The status entry allows the operator to interrogate the Generator for an explanation of either uncalibrated operation (UNCAL indicator is lit) or rejected entry operation (the REJ ENTRY indicator is lit).

When either the UNCAL or REJ ENTRY indicator is lit, press and hold the [STATUS] key to display the Uncalibrated or Rejected Entry Error Code Message. These messages provide detailed information on the nature of the uncalibrated or rejected entry condition. Table 2-5 contains a list and explanation of all the Uncalibrated Error Code Messages. Table 2-6 contains a list and explanation of all the rejected entry error code messages.

The [CLR|LCL] key may be used to clear a partial DATA entry or clear the flashing REJ ENTRY indicator. Press the [STATUS] key while an UNCAL indication exists to display the uncal error codes in three fields:

Flashing codes (denoted by *) indicate abnormal operation or aberrated output. Non-flashing codes indicate operation outside the specified range.

Press the [STATUS] key while the REJ ENTRY indication exists to display the reject entry error codes:

2-34. Step Entry

The following information describes how to use the step entry function to change an instrument parameter. The RF OUTPUT [ON/OFF] must be enabled for the Generator to produce an output. (See the RF OUTPUT [ON/OFF] Entry information covered earlier in this section.)

Command Syntax:

Select Step Function — Enter Data — Select Units — Change Parameter

Summary:

	COMMAND	RANGE	RESOLUTION	NOTES
Frequency	Local: [FREQ][STEP]--DATA --[MHz V] --STEP [^]/[v] [kHz mV] [Hz uV]	0 to 2100 MHz	10 Hz	

```
Remote: "FS"      --float--"GZ"    --"FU"/"FD"
                  "MZ"
                  "KZ"
                  "HZ"
                  0 to 2100 MHz    10 Hz          1,2
```

Amplitude

```
Local: [AMPL][STEP]--DATA --[dB(m)] -- STEP [^]/[v]
                  [MHz|V]
                  [kHz|mV]
                  [Hz|uV]
                  0 to 164 dB      0.1 dB
                  0 nV to 1999 V  3 digits
```

```
Remote: "LS"      --float--"dB"    --"LU"/"LD"
                  "V"
                  "mV"
                  "uV"
                  "nV"
                  0 to 164 dB      0.1 dB          1,2
                  0 nV to 1999V   3 digits         1,2
```

FM/DM deviation

```
Local: [FM|DM][STEP] --DATA --[MHz|V] -- STEP [^]/[v]
                  [kHz|mV]
                  [Hz|uV]
                  [%|rad]
                  0 to 400 kHz     3 digits          3
                  0 to 40.0 rad    3 digits
```

```
Remote: "DS"      --float--"GZ"    --"DU"/"DD"
                  "MZ"
                  "KZ"
                  "HZ"
                  "RD"
                  0 to 400 kHz     3 digits          1,2,3
                  0 to 40.0 rad    3 digits
```

AM depth

```
Local: [AM][STEP] --DATA --[%]      -- STEP [^]/[v]
                  0 to 99%          1%
```

```
Remote: "PS"      --float--"PC"    --"PU"/"PD"
                  0 to 99%          1%          1,2
```

Example:

Recall the Instrument Preset State: [RCL] [9] [8]. Step the displayed frequency of 300 MHz, in 10-MHz steps, to 270 MHz.

```
Local: [FREQ] [STEP] [1] [0] [MHz|V] [v] [v] [v]STEP
Remote: "FS10MZ,FD,FD,FD"
```

Notes:

1. Floating-point number is indicated with the word float.

Table 2-5. UNCAL Error Codes

CODE	DESCRIPTION
000 000 000	= No UNCAL conditions
002 000 000	= FM Dev/ØM > (freq -150 kHz)
*004 000 000	= Excess FM Dev/ØM, main or reference PLL unlocked
*010 000 000	= FM DAC at full scale
200 000 000	= Multiple COMP memory checksum errors. See Compensation Memory Status in Section 2.
*000 010 000	= Main or reference PLL unlocked
000 000 001	= Level vernier > 12 dB below bottom of range
000 000 002	= Peak amplitude > +16 dBm for freq < 1050 MHz or > +13 dBm for freq ≥ 1050 MHz
*000 000 004	= Amplitude unleveled
*000 000 010	= Fixed-range level vernier at 0
*000 000 020	= Fixed-range level vernier at full scale
*000 000 040	= RPP tripped
000 000 100	= Level < -137 dBm
000 000 200	= Level correction disabled
*000 000 400	= RF off
Flashing codes (denoted by *) indicate abnormal operation or aberrated output. Non-flashing codes indicate operation outside specified range.	

Table 2-6. REJECT ENTRY Codes

CODE	DESCRIPTION
000 000 000	= No rejected entries
001 000 000	= FM Dev/ØM not between 0 and 400 kHz (40.0 kHz with Option -651)
002 000 000	= FM Dev/ØM Step not between 0 and 400 kHz (40.0 kHz with Option -651)
004 000 000	= AM Depth not between 0 and 99%
010 000 000	= AM Depth Step not between 0 and 99%
020 000 000	= IEEE-488 command syntax error
040 000 000	= IEEE-488 input value out of range
100 000 000	= MEC compensation PROM error
200 000 000	= IEEE edit or step operation beyond allowed range
400 000 000	= Invalid frequency in COMP memory
000 001 000	= Frequency not between 100 kHz and 2100 MHz
000 002 000	= COMP switch not enabled or low-rate FM option not disabled
000 004 000	= Frequency Step not between 0 and 2100 MHz
000 010 000	= COMP data may not be stored if procedure incomplete
000 020 000	= Invalid compensation command
000 040 000	= Invalid memory location
000 100 000	= Invalid data in memory
000 200 000	= Special function not allowed
000 400 000	= COMP data range error
000 000 001	= Output amplitude not between 10 nV and 1.58V
000 000 002	= Insufficient resolution for units conversion
000 000 004	= Units conversion to volts not allowed with reference in volts
000 000 010	= Units conversion to dB not allowed with reference in volts
000 000 020	= Amplitude Step not between 0 and 164 dB or 0 and 1999V
000 000 040	= Units conversion of Amplitude or FM/ØM Step not allowed
000 000 100	= Amplitude or FM/ØM step and current display not in same units
000 000 200	= COMP data from IEEE-488 out of range or edit beyond COMP limits
000 000 400	= Internal compensation data transfer error

2. Entering the step size from IEEE-488 does not select the step function. For example, "FS10MZ" does not select the step function; "FD" or "FU" must be used to select the frequency step function. The generic step up/down commands "SU" and "SD" may be used to step the current step function.
3. If the Low-Rate FM option is installed, the range of FM deviation steps is 0 to 40.0 kHz, and the range of ØM deviation steps is 0 to 4.00 rad.

Related Operations:

Relative Function

2-35. REMOTE OPERATION (IEEE-488 INTERFACE)

The following paragraphs describe how to operate the Generator using the IEEE-488 Interface (referred to as remote operation). The interface allows the operator to program the Generator and operate instrument functions via the IEEE-488 bus (with the exception of the front panel POWER switch and the rear panel REF INT/EXT switch). The IEEE-488 Interface also provides additional programming features not accessible from the front panel.

The rest of this section is divided into two parts: the first part describes how to set up the Generator for operation on the IEEE-488 bus and gives some typical programming examples. The first part also includes a complete list of the programming commands recognized by the Generator software.

The second part describes the implementation of the IEEE-488 Interface and programming features that are accessible only from the IEEE-488 Interface. The second part also includes typical timing data, provided as an aid to system programmers. This information can assist in writing programs that have greater speed and efficiency.

The Generator can be used with any IEEE-488 controller in the normal addressed mode. The following two additional modes are available for operation without a controller:

Listen-only mode
Talk-only mode

In the listen-only mode, the Generator responds to all data messages on the IEEE-488 bus. In the talk-only mode, the Generator sends commands on the IEEE-488 bus to program another 606xA Generator (or a 607xA with some restrictions).

2-36. Setting Up the IEEE-488 Interface

Figure 2-5 shows a 6062A Signal Generator connected to a 1722A via the IEEE-488 bus.

Use the following procedure to set up the Generator with the IEEE-488 Interface:

1. Connect a standard IEEE-488 cable between the Generator and the IEEE-488 device.

NOTE

The IEEE-488 Interface signal SHIELD (pin 12) can be disconnected (when using an IEEE-488 cable without a metallic hood) from instrument ground. To do this, use the left most address switch (as viewed from the rear panel).

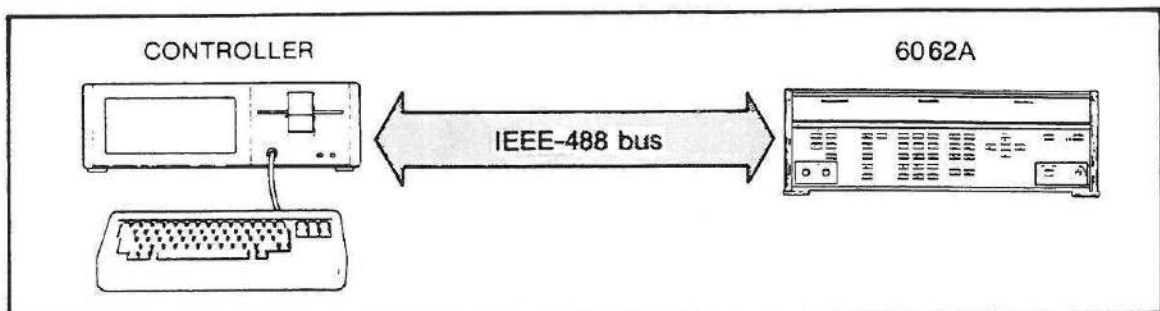


Figure 2-5. 6062A Synthesized RF Signal Generator Connected to a 1722A

2. Select the IEEE-488 address and mode as follows:
 - a. Set both the LISTEN ONLY and TALK ONLY switches (located on the rear panel of the Generator) to 0 (down). Set switches a1 through a5 to the desired address 0 through 30. For example, for an address of 1, set switches a2, a3, a4, and a5 to 0 (down), and set switch a1 to 1 (up).
 - b. For talk-only operation, set the TALK ONLY switch to 1 (up).
 - c. For listen-only operation, set the LISTEN ONLY switch to 1 (up) and set the TALK ONLY switch to 0 (down).
3. Verify the address and mode:
 - a. Press the [SPCL] and the [1] [0] keys. Verify that the selected address appears in decimal in the FREQUENCY display field.
 - b. If the talk-only mode or listen-only mode has been selected, "to" or "lo" appears to the left of the address in the FREQUENCY display field.

NOTE

*The address switches are continuously monitored except when in remote.
The TALK ONLY and LISTEN ONLY switches are only read when the
Generator is powered on.*

2-37. Programming Commands

After the address and mode have been set, the Generator can be programmed by an IEEE-488 controller or from another generator. Tables 2-7 and 2-8 and the programming examples following them provide the basic information on how to program the Generator.

Table 2-7. Index of IEEE-488 Commands

FUNCTION	COMMAND HEADERS
Amplitude Entry	AP, SP3x, RA, SP5x, SP8x, SP9x
Binary Learn Commands	LI, LM
Clear Commands	CB, CE, CL
Compensation Mode Commands	CM
Compensation Procedure Commands	CF, CP, IC, IH
Edit Entry	AB, DB, FB, PB, KB, KA, KD, KF, KP
Frequency Entry	FR, SP2x, RF
Interface Mode Commands	EM, RM, TM, VM, UM, @
Interrogate Commands	IA, ID, IE, II, IL, IO, IR, IS, IT, IU, IV, IZ
Memory Entry	RC, ST, SQ
Modulation Entry	AM, AE, AI, SP6x, DA, FM, FE, FI, MR, MF, PE, SP4x, PI
Monitor Commands	IB, OB, OD, RB, RW, DW, WB, WW, XA, XB, XD, XR
RF ON/OFF Entry	RO
Special Function Entry	SP
SRQ Commands	IM, SM, XF
Step Entry	FS, LS, PS, DS, SU, SD, FU, FD, LU, LD, PU, PD, DU, DD
Trigger Commands	CT, TR

Table 2-8. IEEE-488 Commands

COMMAND USE	COMMAND			COMMENTS
	HEADER	NUMERIC	SUFFIX	
AMPLITUDE ENTRY				
Program Amplitude	AP	float	V MV UV NV DB	Program displayed amplitude in units of: volts millivolts microvolts nanovolts dB, dBm, dBmV or dBuV
Convert Amplitude Units	AP	none	V MV UV NV DB	Change amplitude units to: volts volts volts volts dB, dBm, dBmV or dBuV
Relative Amplitude	SP	30/31	none	Disable/enable relative amplitude operation
Relative Amplitude	RA	0/1		Alternate programming command for disable/enable relative amplitude operation.
Amplitude Display Units	SP	50 51 52	none	Select dBm units Select dBmV units Select dBuV units
Level Correction	SP	80 81 82	none	Enable all level correction. Disable all level correction. Disable attenuator correction.
Amplitude Fixed Range	SP	90/91	none	Disable/enable amplitude fixed-range operation.
BINARY LEARN COMMANDS				
Store a Front Panel Setup	LI	int	string	The Generator stores the string into the memory location specified by int. See the Command Description paragraph for decoding the learn string.
Send a Front Panel Setup	LM	int	none	The Generator responds with the contents of the memory location specified by int. See the Command Descriptions paragraph for decoding the learn string.
CLEAR COMMANDS				
Clear IEEE-488 Output Buffer	CB	none	none	Clears IEEE-488 output buffer.
Clear error	CE	none	none	Clears the IEEE-488 rejected entry status.

Table 2-8. IEEE-488 Commands (cont)

Device Clear	CL	none	none	Clears the instrument state and exits all compensation procedures.
COMPENSATION MODE COMMANDS				
Compensation Mode	CM	none	AT FM OD OT SV EX	Begin Attenuator compensation procedure. Begin FM compensation procedure. Begin Output with default attenuator compensation procedure. Begin Output compensation procedure. Save compensation data and exit compensation procedure. Exit compensation procedure without saving the data.
COMPENSATION PROCEDURE COMMANDS				
Compensation FM Entry	CF	float	GZ MZ KZ HZ	Accept FM deviation reading from modulation analyzer during FM compensation procedure.
Compensation Amplitude Entry	CP	float	DB	Accept amplitude reading from power meter during level compensation procedure.
Interrogate Compensation Step Data	IC	none	FR TG	Interrogate the frequency of the current compensation procedure step. Interrogate the target level or FM deviation of the current compensation procedure step.
Interrogate Het Adjust-ment Data	IH	none	none	Interrogate the frequency and level offset necessary to make the het compensation adjustment. For example, "+0000120000.,+00000000.30"<EOR> indicates the adjustment should be made at 120 kHz and the level needs to be adjusted up .3 dB.
EDIT ENTRY				
Position Amplitude Bright Digit	AB	float	V MV UV NV DB	Position the bright digit in the AMPLITUDE display with the stated resolution. For example, enter "AB10MV" for a 10-mV resolution.
Position FM Bright Digit	DB	float	GZ MZ KZ HZ RD	Position the bright digit in the FM display with the stated resolution. For example, enter "DB1KZ" for a 1-kHz resolution.

Table 2-8. IEEE-488 Commands (cont)

Position Frequency Bright Digit	FB	float	GZ MZ KZ HZ	Position the bright digit in the FREQUENCY display with the stated resolution. For example, enter "FB1MZ" for a 1-MHz resolution.
Position AM Bright Digit	PB	float	PC	Position the bright digit in the AM display with the stated resolution. For example, enter "PB1PC" for a 1% resolution.
Edit	KB	float	none	Edit the current bright digit by float counts.
Edit Amplitude	KA	float	none	Move the bright digit to the AMPLITUDE display and edit amplitude by float counts.
Edit FM	KD	float	none	Move the bright digit to the FM display and edit FM by float counts.
Edit Frequency	KF	float	none	Move the bright digit to the FREQUENCY display and edit frequency by float counts.
Edit AM	KP	float	none	Move the bright digit to the AM display and edit AM by float counts.
FREQUENCY ENTRY				
Frequency Programming	FR	float	GZ MZ KZ HZ	Program displayed frequency in units of: gigahertz megahertz kilohertz hertz
Relative Frequency	SP	20/21	none	Disable/enable relative frequency operation.
Relative Frequency	RF	0/1	none	Alternate programming command for disable/enable relative frequency operation.
INTERFACE MODE COMMANDS				
Error Mode	EM	0/1	none	Disable/enable the clear error mode. If disabled, the IEEE-488 error status is cleared only when interrogated. If enabled, the error status is cleared when a new message is processed.
Record Mode	RM	0/1	none	Disable/enable the record mode. If disabled, the message unit is a command. If enabled, a message unit is a record. The message unit is the smallest group of characters that the Generator processes.

Table 2-8. IEEE-488 Commands (cont)

Record Terminator Mode	TM	0/1	none	Selects the LF/CR character as the record terminator. The record terminator is used on input in the record mode and is sent following all output.
Output Valid Mode	VM	0/1	none	Disable/enable the output valid mode. In the output valid mode, the Generator waits to process commands until the RF output has become valid.
Unbuffered Mode	UM	0/1	none	Disable/enable the unbuffered mode. If disabled, all input is buffered. If enabled, only one message unit is buffered.
"@" Modes	@	int	none	The "@" command may be used as an alternate method of programming interface modes.
INTERROGATE COMMANDS				
Interrogate Attenuator Counts	IA	none	none	The Generator responds with seven counts. Each count indicates the total number of actuations for one of the seven attenuator sections in the Generator.
Instrument Identification	ID	none	none	The Generator responds with its model number, for example, "6062A".
Elapsed Time Indicator	IE	none	none	The Generator responds with the total operating time since the Generator was manufactured.
Interface Modes	II	none	none	Interrogate the interface modes selected. The Generator responds with an unsigned integer.
Interrogate Error Log	IL	none	none	The Generator responds with ten error log entries. Each entry is an uncal error code or a self test result and the elapsed time of when the error was logged.
Option Loading	IO	none	none	Interrogate the option loading. The Generator responds with the message: d1, d2, d3 d1 is the instrument code. d2 is the digital and synthesizer options. d3 is the output options. See the Interrogate Commands paragraphs for details.

Table 2-8. IEEE-488 Commands (cont)

Rejected Entry	IR	none	none	Interrogates the rejected entry error codes. The Generator responds with three octal fields: "AAAAAA,BBBBBB,CCCCC". See Table 2-6 for a list of rejected entry error codes.
Serial Number	IS	none	none	Interrogates the instrument serial number.
Self Test	IT	none	none	Interrogates the results of the self tests. The Generator responds with the self-test results. See paragraph 4D-20 for self-test codes.
UNCAL	IU	none	none	Interrogates the uncalibrated output error codes. The Generator responds with three octal fields: "AAAAAA,BBBBBB,CCCCC". See Table 2-5 for a list of uncal error codes.
Software Version	IV	none	none	Interrogate the software version. The Generator responds with the status message: "Vxx.x" where x's are decimal digits representing the current software revision level.
Compensation Memory Status	IZ	none	none	Interrogates the compensation memory status. The Generator Responds with three fields: "AAAAAA,BBBBBB,CCCCC". See Table 4D-7 for a list of status codes.
MEMORY ENTRY				
Recall	RC	int	none	Recall the front panel setup stored at the memory location specified by int.
Store	ST	int	none	Store the current front panel setup at the memory location specified by int.
Sequence	SQ	none	none	Sequence (recall) to the next higher memory location.
MODULATION ENTRY				
Program AM	AM	float	PC	Program AM depth in percent.
External AM	AE	0/1	none	Disable/enable external AM
Internal AM	AI	0/1	none	Disable/enable internal AM

Table 2-8. IEEE-488 Commands (cont)

DC coupled AM	SP	60/61	none	Disable/enable DC coupled AM
DC coupled AM	DA	0/1	none	Alternate programming command for disable/enable DC coupled AM operation
Program FM	FM	float	GZ MZ KZ HZ RD	Program FM deviation in units of: gigahertz megahertz kilohertz hertz radians
External FM	FE	0/1	none	Disable/enable external FM
Internal FM	FI	0/1	none	Disable/enable internal FM
Program Mod Freq	MR	0/1	none	Program modulation frequency to 400 Hz/1000 Hz.
Program Mod Freq	MF	float	GZ MZ KZ HZ	Program modulation frequency in units of: gigahertz megahertz kilohertz hertz
External Pulse	PE	0/1		Disable/enable external pulse modulation
Internal Pulse	SP	40/41		Disable/enable internal pulse modulation
Internal Pulse	PI	0/1		Alternate programming command for disable/enable internal pulse modulation operation
MONITOR COMMANDS				
Input Bit	IB	none	BIT Designator	Respond with the value of the designated hardware bit.
Output Bit	OB	0/1	BIT Designator	Set the designated hardware bit to 0 or 1.
Output Dac	OD	int	DAC Designator	Set the value of the designated hardware DAC to the value specified by int.
Read Byte	RB	int	none	Read the value of the addressed byte. The Generator responds with an unsigned integer.
Read Word	RW	int	none	Read the value of the addressed word. The Generator responds with an unsigned integer.

Table 2-8. IEEE-488 Commands (cont)

Define Write Address	DW	int	none	Defines the address to be used by the write byte/word commands.
Write Byte	WB	int	none	Write int into the address specified with the define write address command.
Write Word	WW	int	none	Write int into the address specified with the define write address command.
Read Attenuation	XA	none	none	Read the current attenuation. The Generator responds with an unsigned integer.
Write Attenuation	XB	none	none	Change attenuation to 6dB times the unsigned integer. The integer can be 0 to 23.
Set Frequency Direct	XD	float	GZ MZ KZ HZ	Set the frequency hardware directly to the specified synthesizer frequency.
RF Output	XR	0/1	none	"XR0" programs all attenuation. "XR1" restores attenuation to its previous state.
RF ON/OFF ENTRY				
RF Output	RO	0/1	none	Turn RF output off/on.
SPECIAL FUNCTION ENTRY				
Special Functions	SP	00 02 03 04 07/08 09 10 11 12/13 14 15 16 17 20/21 30/31 40/41 50 51 52 60/61 70 71 72 75 76 77-79		Clears all special functions Initiates self test Display check Key check Set/reset SRQ Display S/W rev and instr ID Display IEEE-488 address Display self-test results Turn on/off display Initialize memory Latch test Display option loading Initiates self test with RF on Disable/enable relative freq Disable/enable relative ampl Disable/enable internal pulse Select dBm ampl display units Select dBmV ampl display units Select dBuV ampl display units Disable/enable DC AM Medium key repeat rate Fast key repeat rate Slow key repeat rate Display COMP memory status Repair COMP memory errors Copy MEC PROM data

Table 2-8. IEEE-488 Commands (cont)

		80 81 82 83-86 90/91 95-98		Enable all level correction Disable all level correction Disable attenuator correction Program alternate 24dB attens Disable/enable ampl fixed-rng Local COMP procedures
SRQ COMMANDS				
Interrogate SRQ Mask	IM	none	none	Interrogate the SRQ mask. The Generator responds with the decimal value of the SRQ mask.
Set SRQ	SM	int	none	The SRQ mask is set to int.
Local Operation Alert Mode	XF	0/1	none	Disable/enable a mode to set SRQ each time a Local entry is made. This SRQ is enabled by setting the front panel bit in the SRQ mask.
STEP ENTRY				
Program FREQ STEP Size	FS	float	GZ MZ KZ HZ	Program frequency step size in units of: gigahertz megahertz kilohertz hertz
Program AMPL STEP Size	LS	float	V MV UV NV DB	Program amplitude step size in units of: volts millivolts microvolts nanovolts dB
Program AM STEP Size	PS	float	PC	Program AM step size in percent.
Program FM STEP Size	DS	float	GZ MZ KZ HZ RD	Program FM step size in units of: gigahertz megahertz kilohertz hertz radians
Step Up/Down	SU/SD	none	none	Step the currently selected step function up/down one step.
Step Up/Down Frequency	FU/FD	none	none	Change the current step function to frequency and step frequency up/down one step.
Step Up/Down Amplitude	LU/LD	none	none	Change the current step function to amplitude and step amplitude up/down one step.

Table 2-8. IEEE-488 Commands (cont)

Step Up/Down AM	PU/PD	none	none	Change the current step function to AM and step AM up/down one step.
Step Up/Down FM	DU/DD	none	none	Change the current step function to FM and step FM up/down one step.
TRIGGER COMMANDS				
Configure Trigger	CT	string	none	Configures the trigger. Each time a trigger command or a group execute trigger interface message is received, the Generator executes the string of commands. The string record must end with a record terminator.
Trigger	TR	none	none	Trigger command. Equivalent to the group execute interface message. Upon processing the trigger command, the Generator executes the string, which has been preprogrammed with the configure trigger command.

More details about the commands can be found in two places. Commands that are available from the front panel are described earlier in this section (Local and Remote Operation). Those commands that are only available from the IEEE-488 Interface are described in the Command Descriptions paragraphs later in this section.

Table 2-7 is an index for the IEEE-488 Commands used in Table 2-8. This index is a list of the command headers according to function. Table 2-8 lists all the remote commands that are recognized by the Generator. The commands are listed alphabetically by function.

2-38. Programming Examples

The following three examples show how to use the IEEE-488 bus and how to use a variety of controllers to program the Generator. In the first example, a Fluke 1722A Controller is used to program the Generator. In the second example, two Generators are configured to track each other in frequency. In the third example, a 1722A is used to program the Generator with the frequency step up controlled by the trigger command.

2-39. PROGRAMMING EXAMPLE 1

Use the following procedure to program the Generator with a Fluke 1722A Instrument Controller to this state:

Frequency	210 MHz
Amplitude	6 dBm
Modulation Freq.	1000 Hz
FM	5 kHz
Internal FM	ON
AM	15%
External AM	ON

1. Connect the Generator to the controller with an IEEE-488 cable.
2. Set the address switch of the Generator as follows (as viewed from the rear of the instrument):

00000010

3. Enter the following program into the controller:

```

1  ! Fluke 1722A BASIC program to control a 6062A.
2  ! The Address of the 6062A is 2.
3  AZ = 2%
10 ! Clear the 6062A so that it is in a known state.
15 INIT PORT 0
20 REMOTE @AZ \ CLEAR @AZ
100 ! SET THE 6062A.
110 PRINT @AZ, "FR210MZ, AP6DB, MR1, FM5KZ, FI1, AM15PC, AE1"
999 END

```

4. Run the program by typing on the controller "RUN (RETURN)".

2-40. PROGRAMMING EXAMPLE 2

The 6062A Synthesized RF Signal Generator can be connected to another signal generator in a master-slave configuration. In the following example, two generators are configured to track each other in frequency. This configuration may be used to track frequency, amplitude, AM, or FM.

1. Connect two signal generators together with an IEEE-488 cable.
2. Set the rear panel address switch of the first generator (talker) as follows:

00100000

Cycle the power of the first generator so the software will read the talk only switch.

3. Set the rear panel address switch of the second generator (listener) as follows:

01000000

Cycle the power of the second generator so the software will read the listen only switch.

4. Manually program the talker generator as follows:

FUNCTION	VALUE	KEY SEQUENCE
Frequency	210 MHz	[FREQ] [2] [1] [0] [MHz V]
Step Function	Frequency	[FREQ] [STEP]
Frequency Step	1.25 kHz	[1] [.] [2] [5] [kHz mV]

5. Manually program the listener generator as follows:

FUNCTION	VALUE	KEY SEQUENCE
Frequency	195 MHz	[FREQ] [1] [9] [5] [MHz V]
Step Function	Frequency	[FREQ] [STEP]
Frequency Step	1.25 kHz	[1] [.] [2] [5] [kHz mV]

6. On the talker generator, press the [↑] STEP or [↓] STEP keys. Each time the key is pressed, the frequency of both generators increases or decreases by 1.25 kHz (the Frequency Step) at frequencies 15 MHz apart.

Different functions on each generator can be programmed to track in the master-slave configuration. In other words, while the master generator can be programmed to step increase 25 kHz FM, the slave generator can be programmed to step 25% AM.

NOTE

To use the step feature for other functions, change the step function on the generators to the desired functions.

2-41. PROGRAMMING EXAMPLE 3

In the following example, the Generator is programmed by a Fluke 1722A Instrument Controller (via the IEEE-488 bus) to the same state as in Programming Example 1. Additionally, the frequency step size is set to 1.25 kHz, and the trigger buffer is programmed to execute the step up command when the trigger command is received. The SRQ mask of the Generator is set to generate an SRQ when the RF output has settled and the Generator is ready for more input from the bus.

The program then enters a loop where it waits for the ready SRQ, sends the GET (group execute trigger) interface message to step up the frequency, and waits again. At this time, do the following:

1. Connect the Generator to the controller with an IEEE-488 cable.
2. Set the rear panel address switch of the Generator as follows:

00000111

3. Enter the following program into the controller:

```

1  ! Fluke 1722A BASIC program to control a 6062A.
2  ! The address of the 6062A is 7.
3  AZ = 7%
10 ! Clear the 6062A so that it is in a known state.
15 INIT PORT 0
20 REMOTE @AZ \ CLEAR @AZ
100 ! Set the 6062A.
110 PRINT @AZ, "FR210MZ, AP6DB, MR1, FM5KZ, FI1, AM15PC, AE1"
120 ! Set the frequency step, output valid mode,
121 ! and configure the trigger buffer.
130 PRINT @AZ, "FS1.25KZ, VM1, CTSU"
140 ! Set the SRQ mask to enable "output valid" SRQ
150 PRINT @AZ, "SM16"
160 ! Wait for above commands to finish processing
170 WAIT 1000 \ S% = SPL (AZ)
180 ! Trigger the first step up
190 TRIG @AZ
300 ! Wait for SRQ
310 ON SRQ GOTO 800
320 WAIT FOR SRQ
800 OFF SRQ
810 ! Check the serial poll response
820 S% = SPL(AZ)
830 IF (S% AND 64%+16%) (> 80% THEN PRINT s%: " Bad Serial Poll Response"
840 ! Trigger the next step up
850 TRIG @AZ
860 ! Resume operation-- waiting for next SRQ
870 RESUME 300
999 END

```

4. Run the program by typing on the controller "RUN <RETURN>".

2-42. IEEE-488 Interface Functions

The Generator implements a subset of IEEE-488 Interface functions defined by the IEEE Standard 488-1978. Table 2-9 summarizes the IEEE-488 Interface functions implemented.

2-43. Address Mode

In the address mode, the Generator may be operated from local (using the front panel keys) or from remote (using the IEEE-488 Interface). The following paragraphs describe the operation of the Generator in both states and transitions between the states.

The available IEEE-488 messages and their descriptions for the address mode of operation are presented in Table 2-10.

2-44. LOCAL OPERATION

The Generator powers up in the local mode. When in local mode, the following conditions are present:

The front panel indicator REM, is not lit.

Device trigger (GET), device clear (DCL), and selected device clear (SDC) interface messages are ignored.

All device-dependent messages are ignored.

If the data output was requested while the Generator was in the remote mode, the data output of a talker may be sent.

2-45. GOING FROM LOCAL TO REMOTE

The Generator switches from local to remote when the "my listen address message" (MLA) is received, and the Remote Enable (REN) signal is true.

2-46. REMOTE OPERATION

When in the remote mode, the following conditions are present:

The front panel REM indicator is lit.

Device trigger (GET), device clear (DCL), and selected device clear (SDC) interface messages are processed.

All device-dependent messages are processed during the remote mode.

Table 2-9. IEEE-488 Interface Function List

FUNCTION	DESCRIPTION
SH1	Complete source handshake capability
AH1	Complete acceptor handshake capability
T5	Basic talker, Talk only, Serial poll, Unaddressed if MLA
TE0	No extended talker capability
L3	Basic listener, Listen only, Unaddressed if MTA
LE0	No extended listener capability
SR1	Complete service request capability
RL1	Complete remote/local capability
PP0	No parallel poll capability
DC1	Complete device clear capability
DT1	Complete device trigger capability
C0	No controller capability
E1	Open-collector drivers

Table 2-10. IEEE-488 Address Mode Message Descriptions

MESSAGE	DESCRIPTION
<p>pon Power-On</p> <p>Talker Operation</p> <p>Listener Operation</p> <p>Service Request Operation</p> <p>MTA My Talk Address</p> <p>Talker Operation</p> <p>Listener Operation</p> <p>MLA My Listen Address</p> <p>Talker Operation</p> <p>Listener Operation</p> <p>Data</p> <p>Talker Operation</p> <p>Listener Operation</p> <p>IFC Interface Clear</p> <p>Talker Operation</p>	<p>When powered up, the Generator generates a Power-On message (pon) and clears its output buffer. The Generator is not addressed to talk when powered up.</p> <p>The Generator is not addressed to listen when the power is turned on.</p> <p>The state of the Service Request (SRQ) signal on pon is determined by the SRQ mask. The SRQ mask is the same as when the power was removed. Therefore, if the SRQ mask enables the power on, output valid, or ready SRQs, the SRQ signal will be true during pon.</p> <p>The Generator is addressed to talk upon receipt of the MTA message. The front panel ADDR indicator is lit while the Generator is addressed to talk.</p> <p>The Generator unlistens when the MTA message is received.</p> <p>The Generator untalks when the MLA message is received.</p> <p>The Generator is addressed to listen when the MLA message is received. The front panel ADDR indicator of the Generator is lit while the Generator is addressed to listen.</p> <p>The Generator sends data to the IEEE-488 bus only when requested by a programming data message. Message formats are described in the Command Description paragraphs. An End of Record (EOR) character is sent with EOI asserted following all outputs. The EOR character is either a carriage return or a line feed, depending on the setting of the terminator mode. The parity bit is always zero. Multiple output requests are buffered until the buffer is full. Processing of programming data messages is stopped until the buffer is no longer full. The buffer can be cleared with the Clear Buffer command ("CB"). The buffer is also cleared on power up (pon), with a Clear Command ("CL"), or with a Device Clear interface message (DCL or SDC).</p> <p>Command syntax, error processing, and input buffer overflow are described in the paragraphs on Command Processing. Refer to Table 2-8 for a list of IEEE-488 commands that are recognized by the Generator.</p> <p>The Generator untalks and unlistens when the IFC message is received.</p>

Table 2-10. IEEE-488 Address Mode Message Descriptions (cont)

MESSAGE	DESCRIPTION
Listener Operation	The Generator unlistens and untalks when the IFC message is received.
OTA Other Talk Address	
Talker Operation	The Generator untalks when the OTA message is received.
SPE Serial Poll Enable	
Talker Operation	After receiving the SPE message, the Generator responds with the serial poll status byte, if addressed to talk.
SPD Serial Poll Disable	
Talker Operation	After receiving the SPD message, the Generator resumes normal talk operation.
ULA Unlisten Address	
Listener Operation	The Generator unlistens when the ULA message is received.
RSV Request Service	
Service Operation	The front panel SRQ indicator is lit when the rsv message is sent. The Generator may request service for several reasons. Each reason for service request can be individually masked with the set mask command ("SM"). The service request mask can be interrogated with the interrogate mask command ("IM").
DCL Device Clear	
Clear Operation	The DCL message is ignored when in local. When the DCL message is received (during remote operation) the Generator is cleared. Any characters in the input buffer are cleared followed by the same operation as the clear command ("CL"). The operation of the DCL message is identical to the operation of the selected device clear (SDC) message. The cleared state of the Generator is described in the paragraphs on Power-On Conditions.
SDC Selected Device Clear	
Clear Operation	The SDC message is ignored during local operation. When the SDC message is received (during remote operation), the Generator is cleared. Any characters in the input buffer are cleared followed by the same operation as the clear command ("CL"). The operation of the SDC message is identical to the operation of the device clear (DCL) message. The cleared state of the Generator is described in the paragraphs on Power-On Conditions.

Table 2-10. IEEE-488 Address Mode Message Descriptions (cont)

MESSAGE	DESCRIPTION
GET Group Execute Trigger Trigger Operation	The GET message is ignored during local operation. When the GET message is received (during remote operation), the Generator executes a command string that has been preprogrammed with the Configure Trigger command ("CT"). The operation of the GET message is identical to the operation of the Trigger ("TR") command.
Undefined IEEE-488 Commands	All undefined IEEE-488 commands are acknowledged by the Generator handshake sequence, but no action is taken.

2-47. GOING FROM REMOTE TO LOCAL

The Generator switches from remote to local mode in one of the following ways: The IEEE-488 Go To Local (GTL) message is received, the remote enable signal REN is false, or a Return To Local (rtl) message is generated by pressing the front panel [CLR|LCL] key (if the Generator is not in the local lockout mode).

The Generator enters the local lockout mode when the Local Lockout message (LLO) is received. The Generator exits the local lockout mode to the local mode when REN is false.

When switching from remote to local, unprocessed commands in the input buffer are processed until the input buffer is cleared or a front panel entry is made. Switching to local has no effect on the contents of the output buffer.

2-48. Talk-Only Mode

Figure 2-6 shows two generators connected together with the IEEE-488 Bus.

To select the talk-only mode, set the TALK ONLY address switch to 1 (up). If the TALK-ONLY address switch and the LISTEN-ONLY address switch are set to 1, the talk-only mode is selected.

In the talk-only mode, the listener, remote/local, service request, device clear, and device trigger interface functions do not apply.

If the talk-only mode is selected, the Generator is always addressed to talk and the front panel ADDR indicator is always lit. The Step Up ("SU") or Step Down ("SD") message is sent when the [↑] STEP or [↓] STEP front panel keys are pressed. This output is not buffered, and if no listener is connected to the IEEE-488 Interface, no output will be sent. A carriage return followed by line feed (with the EOI signal true) are always sent as the end of record. The Generator must be cycled off/on to exit from the talk-only mode.

2-49. Listen-Only Mode

To select the listen-only mode, set the LISTEN ONLY address switch to 1 (up). If the talk-only address switch and the listen-only address switch are set to 1, the talk-only mode is selected.

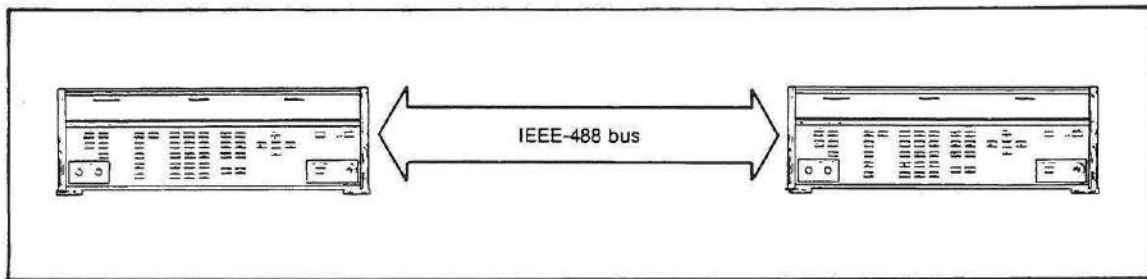


Figure 2-6. 6062A IEEE-488 Bus Connected to a 6062A IEEE-488 Bus

If the listen-only mode is selected, the Generator is always addressed to listen, and the front panel ADDR indicator is always lit. The Generator listens and responds to all data messages on the IEEE-488 Interface. The response to data messages is the same as in the addressed mode of operation except that requests for talker output are ignored.

In the listen-only mode, the talker, remote/local, service request, device clear, and device trigger interface functions do not apply.

2-50. Command Syntax

The Generator IEEE-488 bus commands alphabet consists of the letters A through Z (upper and lower case letters are treated equally), digits 0 through 9, and the following special characters:

@ . , ; + - CR LF

Spaces, tabs characters, and the parity bit are ignored.

The IEEE-488 commands for the Generator consist of the following three parts:

Header
Numeric
Suffix

The header is always required, but the numeric and suffix may be optional. This rule gives the following four possible combinations:

<HEADER>
<HEADER> <NUMERIC>
<HEADER> <NUMERIC> SUFFIX
<HEADER> <SUFFIX>

Multiple commands may be separated with one of the end of string (EOS) characters “,” or “.”. Use of EOS characters facilitates recovery in the event of a syntax error and will also enhance readability.

2-51. COMMAND HEADER SYNTAX

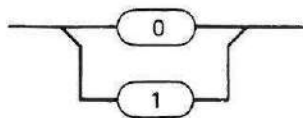
The command header is a two alpha-character string. A list of the IEEE-488 command headers used on the Generator is presented in Table 2-8. The header determines the syntax of the numeric and suffix as listed in the table.

2-52. NUMERIC DATA SYNTAX

There are four types of numeric data: Boolean, unsigned integer, floating point, and trigger string. The following paragraphs describes each of the four numeric data formats. A syntax diagram is included for each format.

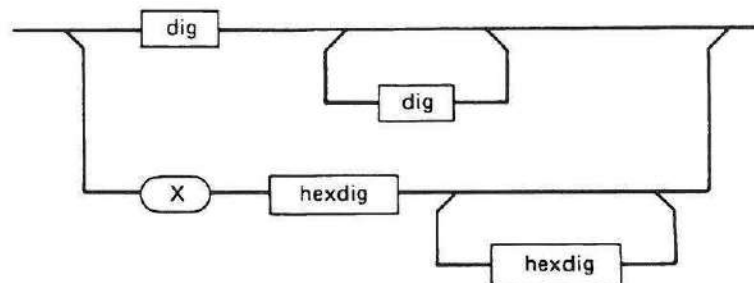
1. Boolean

Boolean numeric data must be either a "0" or a "1". All other characters will result in a syntax error.



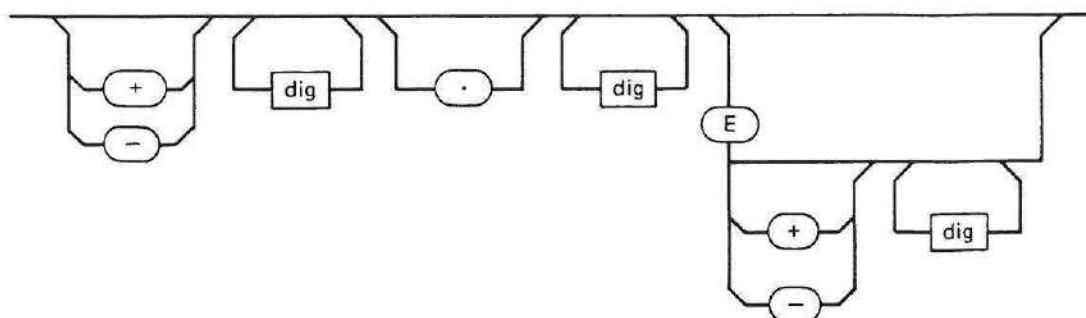
2. Unsigned Integer

Unsigned integers may be specified in decimal or in hexadecimal. Any number of decimal digits are accepted. However, values greater than 65,535 are rejected. Hexadecimal numbers are preceded by an "X". Only four hexadecimal digits are accepted. Specifying a number in hexadecimal for the read word and read byte commands causes the response to be sent in hexadecimal. Decimal digits may be the numerals 0 through 9. Hexadecimal digits may be the hexadecimal digits 0 through F.



3. Floating Point

The floating-point numeric data format is the most flexible format. Digits may be the numerals 0 through 9. Any number of digits is accepted for both the number and the exponent. However, numbers greater than 2,147,483,629 are truncated, and exponents greater than 32,749 are rejected.

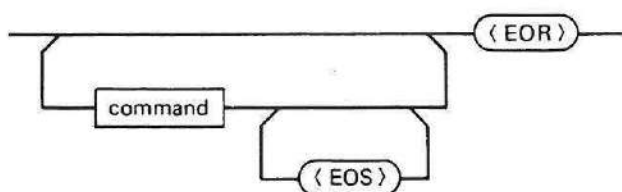


4. Trigger String

The trigger string numeric data is a string of Generator commands terminated with an EOR. The string may be up to 71 characters, not including the EOR. Commands in the string are not checked for validity until the trigger string is executed with the trigger command.

EOR is the end of record character. This character is selected with the terminator mode command. "TM0" selects the linefeed character. "TM1" selects the carriage return character. The IEEE-488 interface signal EOI asserted with any other character is also considered an end of record.

EOS is an end of string character, use either ";" or ",".



2-53. SUFFIX SYNTAX

Suffixes are always one or two alpha-characters. Certain suffixes are used to scale the numeric (the same as the front panel UNITS keys). Other suffixes mnemonically designate hardware components. The five types of suffixes are described in Table 2-11.

2-54. Command Descriptions

The following paragraphs describe the remote IEEE-488 Interface operating commands that are not accessible from the front panel of the Generator. IEEE-488 Interface commands that are accessible from the front panel of the Generator are described earlier in this section (Local and Remote Operation).

2-55. BINARY LEARN COMMANDS

Front panel setups are stored in the memory of the Generator in a packed binary format. The binary learn commands are used to transfer this binary data between an IEEE-488 controller and the Generator. These commands allow the operator to minimize the amount of programming commands needed to program the entire instrument state. The binary learn commands are:

- "LM" Learn Memory
- "LI" Learn Interface

The syntax for the Learn Memory ("LM") command is as follows:

"LM" Memory Location Code

The Generator responds to the "LM" command with a string of 66 ASCII characters followed by an <EOR> (end of record character). This string represents the front panel settings (in a packed binary format) that were stored in the memory location specified.

Table 2-11. Suffix Types

SUFFIX TYPE	SUFFIX	MNEMONIC	EQUIVALENT EXPONENT
Frequency	GZ	gigahertz	9
	MZ	megahertz	6
	KZ	kilohertz	3
	HZ	hertz	0
Amplitude	V	volts	0
	MV	millivolts	-3
	UV	microvolts	-6
	NV	nanovolts	-9
	DB	dB, dBm, dBmV, or dBuV	0
AM	PC	percent	0
FM	GZ	gigahertz	9
	MZ	megahertz	6
	KZ	kilohertz	3
	HZ	hertz	0
	RD	radians	0
DAC/BIT Designators	DAC and BIT designators are two alpha-character mnemonics that refer to hardware dacs and bits. Refer to the paragraphs on Monitor Commands for a complete list of designators.		
Learn Suffix	A learn suffix is a string of ASCII characters that contain coded memory location contents. Refer to the paragraphs on Binary Learn Commands for decoding of the learn string.		

NOTE

The <EOR>, end of record character, is sent with EOI asserted. "TM0" selects the linefeed character, and "TM1" selects the carriage return character.

Example:

IEEE-488 Command: "LM98"

Response:

"BOABAAAAPPJMAAAKAAAAAAAAABBOBKDAAAAAPECEAAABPEAAABAACEBB
AGCLKKABAEAM" EOR

Refer to Figure 2-7 for information on how to decode this learn string.

The syntax for the Learn Interface "LI" command is as follows:

"LI" Memory Location Code: Learn String

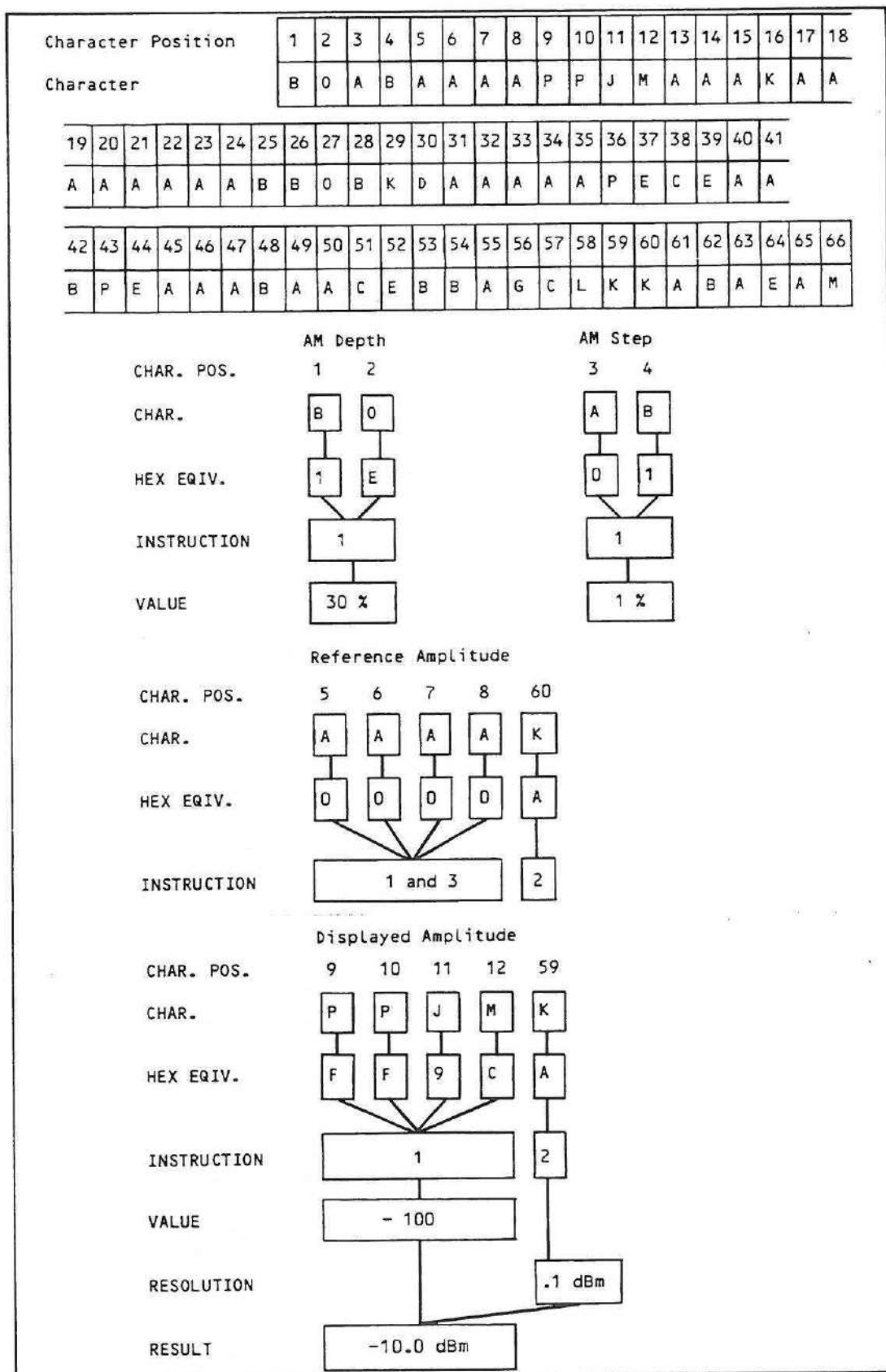


Figure 2-7. Learn String Example

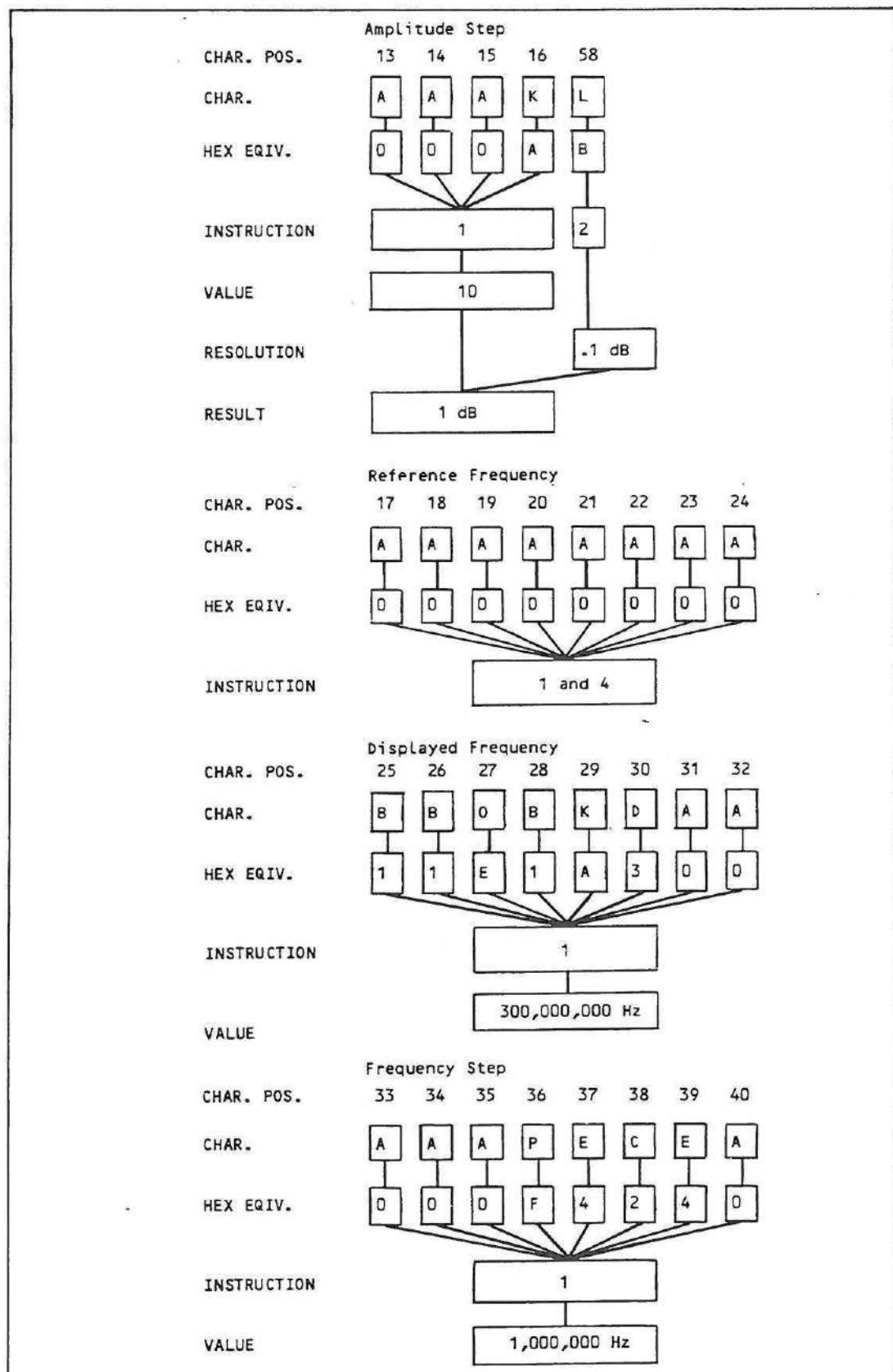


Figure 2-7. Learn String Example (cont)

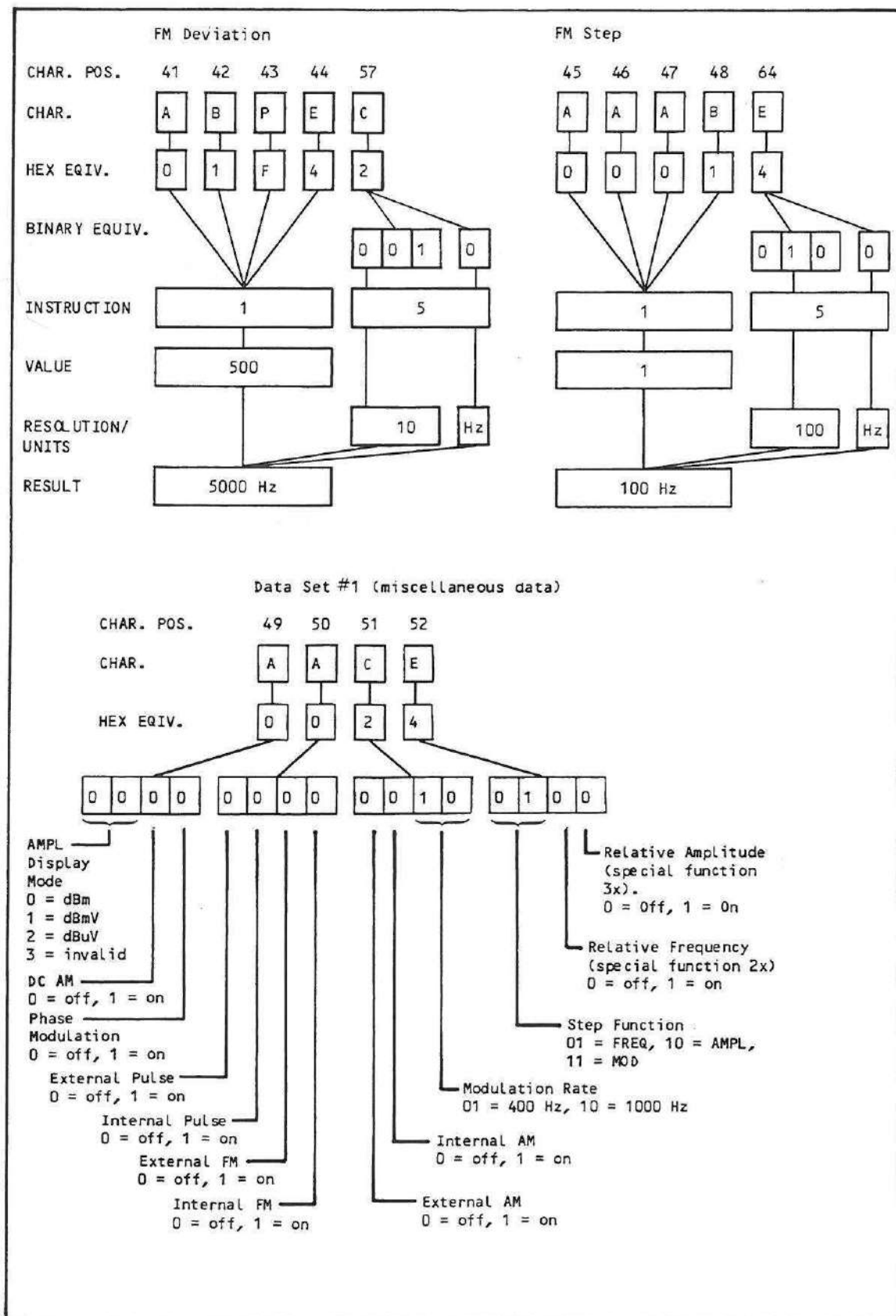


Figure 2-7. Learn String Example (cont)

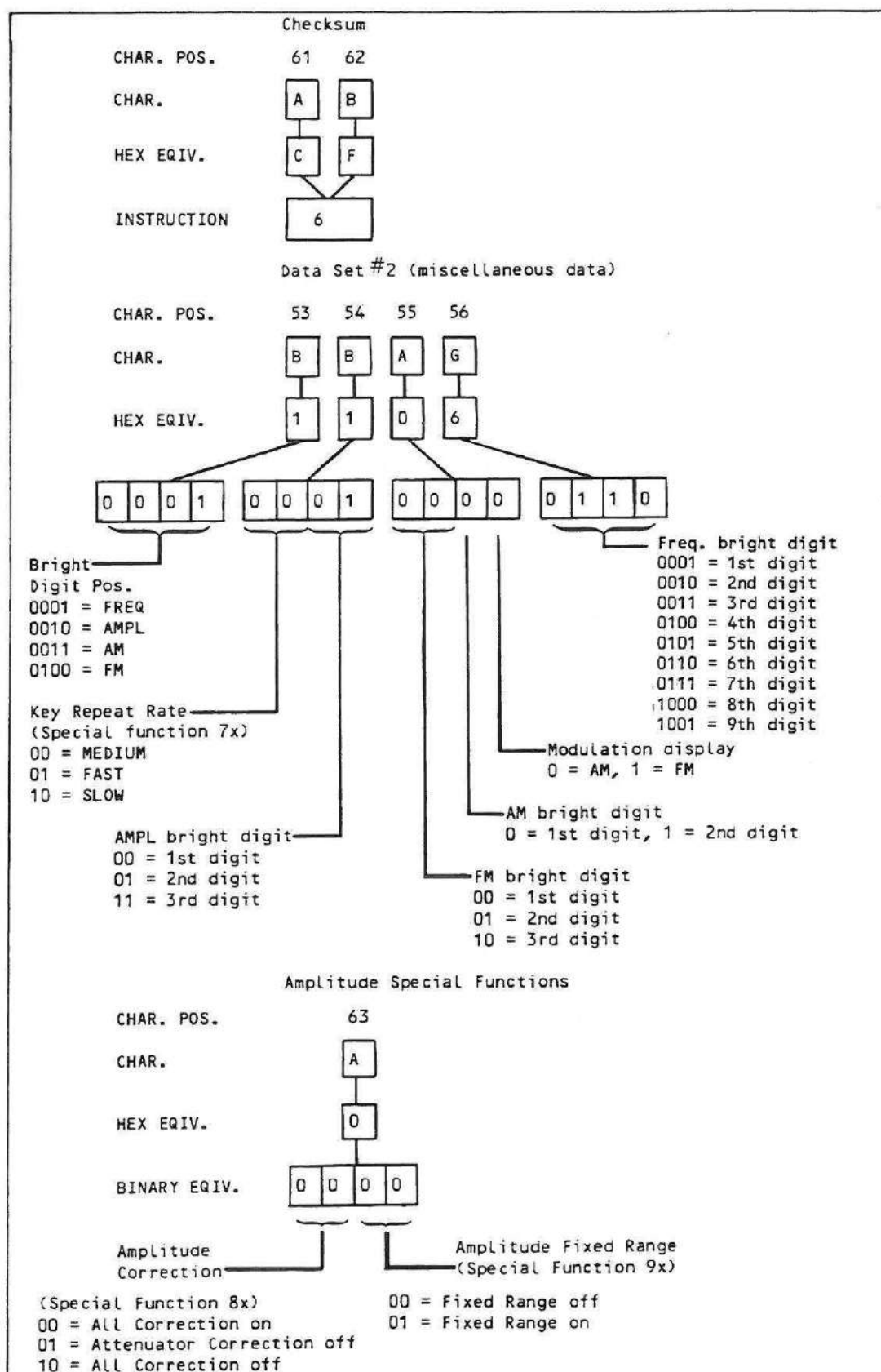


Figure 2-7. Learn String Example (cont)

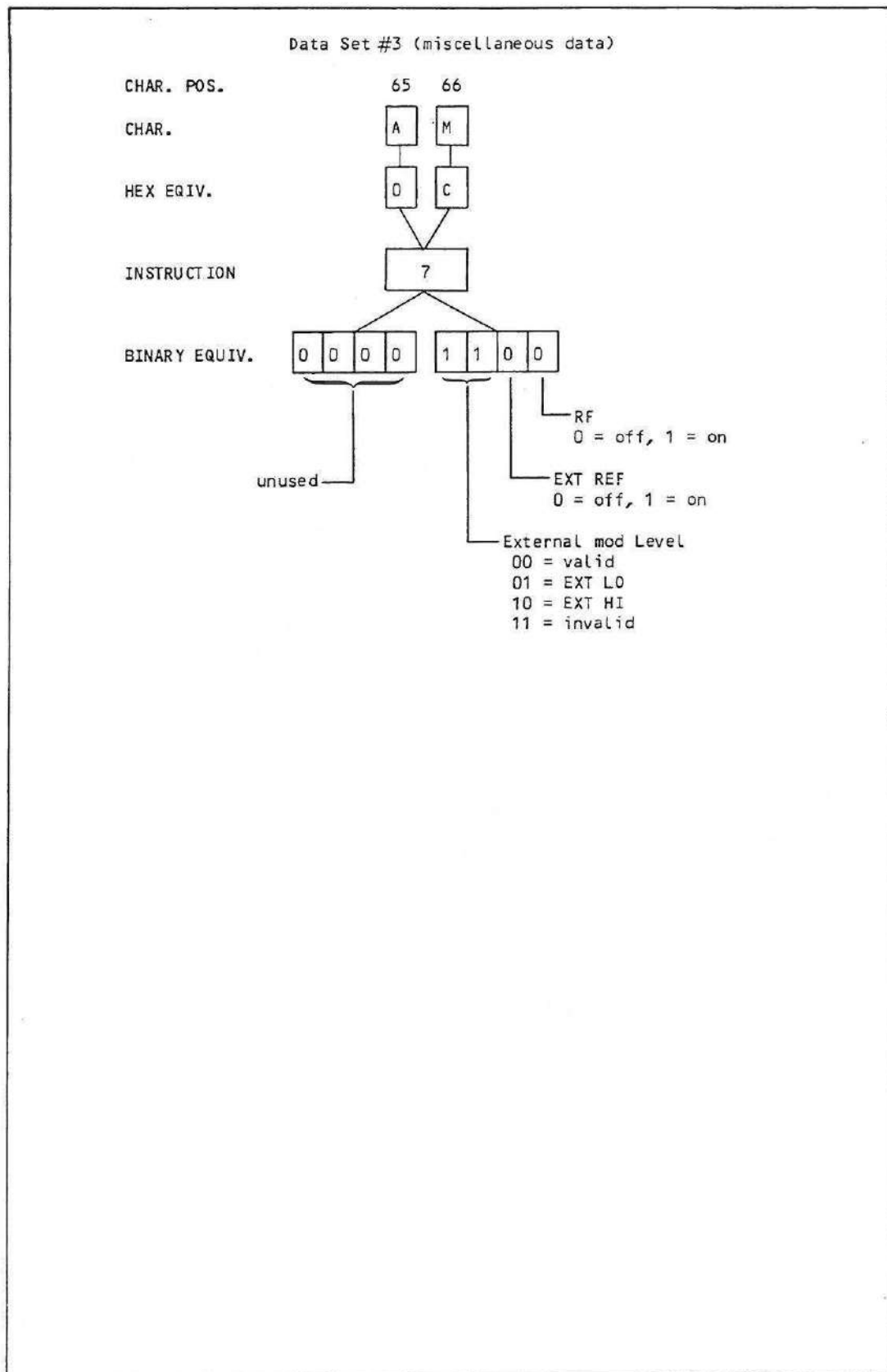


Figure 2-7. Learn String Example (cont)

The Generator stores the learn string in the memory location designated by the memory location code. If the memory location specified is 99, the instrument is programmed to the data sent in the learn string.

Example:

To program the Generator to the Instrument Preset State:

"LI99BOABAAAAPPJMAAAKAAAAAAAAABBOBKDAAAAAPECEAABPEAAABAAC
EBBAGCLKKABAEAM"

Note that the binary learn string in this example is the same as the learn string returned from memory location 98, which contains a record of the Instrument Preset State.

Instructions:

1. Convert the hexadecimal number to a signed decimal number as follows:
 - a. Multiply the most significant hexadecimal digit by 16.
 - b. Add the next significant digit to the value obtained in step a.
 - c. Multiply the sum of step b by 16, and add in the next hexadecimal digit until the least significant hexadecimal digit has been added.

NOTE

If the hexadecimal number started with an 8 through F, the number is negative. Perform step d for negative numbers.

- d. Subtract 16 from the number raised to the power of the number of digits.

Example of Instruction 1:

To convert hexadecimal number 1E:

$$(1 * 16) + 14 = 30$$

(Since the most significant digit is 1, the number is not negative.)

To convert hexadecimal number FF9C:

$$((((15 * 16) + 15) * 16) + 9) * 16 + 12 = 65436$$

(Since the most significant digit is F, the number is negative.)

Using Step d; $16^4 = 65536$, $65436 - 65536 = -100$. The signed decimal equivalent to FF9C is -100.

2. Amplitude quantities have a number and a resolution associated with them. This applies to the Displayed Amplitude, Reference Amplitude, and Amplitude Step.

Use the following procedure to identify the resolution of an amplitude quantity:

- a. If the resolution is A or B (hexadecimal), the resolution of the number is 0.1 dBm or 0.1 dB units.

Example of Instruction 2:

The Displayed Amplitude (in Figure 2-7) is -100 with a resolution of A.

- a. The actual displayed amplitude is -10.0 dBm.
- b. If the stored resolution is 0 through 9 (hexadecimal), the amplitude quantity is in volts. To convert the number to the actual amplitude in nanovolts, multiply the amplitude number by the power of ten represented by the resolution.

Example: An amplitude step of 12, with a resolution of 6 would be an actual amplitude step of 1.2,000,000 nV or 12 mV.)

3. If the Relative Amplitude mode is off, the data stored in the reference amplitude location is not used.
4. If the Relative Frequency mode is off, the data stored in the reference frequency location is not used.
5. FM and FM Step quantities have a number and a resolution associated with them. The resolution nibble is comprised of a 3-bit resolution value and a single bit units value. If the units value is 0, the FM quantity is in Hertz. If the units value is 1, it is a ϕ M quantity in radians.

To convert the number to the FM quantity in Hz, multiply the number by the power of ten represented by the resolution.

Example: The FM number (in this figure) is 500 with a resolution of 10 Hz. The actual FM deviation is 5000 Hz.

To convert the number to the ϕ M quantity in radians, multiply the number by the power of ten represented by the resolution, then divide the result by 1000.

Example: A deviation of 123 with a resolution of 1 and units of 1 would be a ϕ M deviation of 1.23 radians.

If the Low-Rate FM option is installed, the FM quantity adjusted by the resolution has a resolution of 0.1 Hz. The ϕ M quantity is derived exactly as it is without the option. The resolution remains the same, however, the range is reduced by a factor of 10.

6. The checksum data is calculated by adding the data in the learn string, two hexadecimal digits at a time. The total, including the checksum, should add up to a number whose least significant two hexadecimal digits are 01.
7. Only the current state of these parameters is relevant, so they are only valid when learning memory location 99. Learn strings from all other memory locations have characters 65 and 66 set to "AM". These parameters are provided as status information only and cannot be set using the learn interface command.

NOTE

The memory location code must be an unsigned integer indicating the memory location to be learned. Memory location 99 refers to the current instrument settings. Memory location 98 refers to the Instrument Preset State as listed in Table 2-3.

The characters in the learn string correspond to each Generator function. A description of how to interpret the characters in the learn string is given in Figure 2-7. Table 2-12 shows the conversion from the learn string to the hexadecimal character.

2-56. CLEAR COMMANDS

The following IEEE-488 clear commands are recognized by the Generator:

- "CB" Clear IEEE-488 input buffer
- "CE" Clear IEEE-488 rejected entry error status
- "CL" Clear instrument

The "CB" command can be used to clear the Generator output buffer. The output buffer should be cleared at the beginning of any remote program to synchronize data output. The output buffer is also cleared at power-on with the "CL" clear Generator command, or the buffer is cleared by the SDC and DCL clear interface messages.

The "CE" command can be used to explicitly clear the error status. The error status is also cleared when it is interrogated with the "IR" command, or the "CL" clear Generator command, or the SDC and DCL clear.

The "CL" command is used to clear the instrument state. The same actions are performed with the SDC and DCL clear interface messages. (In addition, the input buffer is cleared with the clear interface messages.) The following IEEE-488 commands are performed with the clear Generator command: "RC98, RO1, CE, CB, RM0, TM0, EM0, VM0, UM0, SM192, SP08, XF0, DW0, CT"(EOR). If a Software Compensation Procedure is in progress, it is exited immediately and all measured data is discarded.

2-57. INTERFACE MODE COMMANDS

Interface mode commands are used to configure the Generator for different modes of IEEE-488 interface operation. Since the Generator knows when its RF output has settled, it can be configured to synchronize itself with the controller. This eliminates WAIT statements in the program, which are normally used to allow time for the output of the controlled device to settle. Table 2-13 lists the interface mode commands.

Table 2-12. Learn Character to Hexadecimal Conversion

LEARN CHARACTER	HEXADECIMAL EQUIVALENT	DECIMAL EQUIVALENT
A	0	0
B	1	1
C	2	2
D	3	3
E	4	4
F	5	5
G	6	6
H	7	7
I	8	8
J	9	9
K	A	10
L	B	11
M	C	12
N	D	13
O	E	14
P	F	15

Table 2-13. Interface Mode Commands

COMMAND	DESCRIPTION	COMMAND STATUS
"EM"	Error Mode	1 = on, 0 = off
"RM"	Record Mode	1 = on, 0 = off
"TM"	Select Terminator	1 = CR, 0 = LF
"UM"	Unbuffered Mode	1 = on, 0 = off
"VM"	Valid Mode	1 = on, 0 = off

The error mode selects when the IEEE-488 rejected entry status is cleared. If turned on, the error status is cleared when a new message is processed. If turned off, the status is cleared only when interrogated with the "IR" (interrogate rejected entry) command or when explicitly cleared with the "CE" (clear error command).

The record mode selects whether the message unit is a record or a command. When turned on, the message unit is a record. When turned off, the message unit is a command.

The terminator mode selects the character used as the record terminator. The terminator character is not used for command processing unless the record mode is enabled. When turned on, the record terminator CR (carriage return) is used. If turned off, the record terminator LF (line feed) is used. The record terminator character is the last character in all IEEE-488 messages sent from the Generator.

The unbuffered mode selects when messages from the IEEE-488 interface are processed. When turned on, messages are read from the IEEE-488 interface only when the microprocessor is ready to process them. In this mode, the input buffer will contain a maximum of one message. (A message may be one command or one record, depending on the setting of the record mode.) When turned off, messages are read from the IEEE-488 Interface to the input buffer of the Generator at the fastest rate. In this mode, the input buffer may contain up to 80 characters.

The valid mode selects when messages are processed by the Generator microprocessor. When the valid mode is turned on, processing of a new message is begun only after the RF output has settled and become valid. When the valid mode is turned off, a new message is processed immediately after the completion of the previous message.

The interface modes can also be programmed using the command, "@n" (where n is an integer). The interface modes are set to the value of n, where n is the sum of the codes for the desired modes. The integer codes for the interface modes that can be programmed using the "@n" commands are as follows:

```

Terminator Mode= 1
Record Mode    = 2
Valid Mode     = 4
Unbuffered Mode= 8
Error Mode     = 16

```

For example, to select the record mode and valid mode, the command is "@6".

Interface Mode Example:

In this example, the RF output of the Generator is connected to a circuit that is being measured by a voltmeter. The output of the Generator must be settled before the voltmeter is given the command to make a measurement.

A Fluke 1722A program might look something like:

```
PRINT @1, "CL, TM1, RM1, VM1, UM1" : initialize the 6060, select modes
PRINT @1, "FR1QOMZ, AP-25DB"      : program the 6060
PRINT @2, "?"                      : trigger the voltmeter
INPUT @2, "R$"                    : get the reading
```

The entire record is transferred into the Generator before processing begins. In this example, processing begins when the record terminator CR is received. The following character (LF in this case) will not be received into the Generator until the entire record is processed and the output has settled. No wait statement is needed between setting up the Generator and taking the measurement because the Generator will not handshake the LF character until its output has settled.

NOTE

A record is a string of characters separated by (EOR). A message is the smallest group of characters that the Generator can process when it is programmed from the IEEE-488 Interface.

NOTE

The output valid state of the Generator occurs 45 ms after any hardware has been changed.

2-58. INTERROGATE COMMANDS

Interrogate commands allow the status of the Generator to be given over the IEEE-488 Interface. These commands consist of headers only. The interrogate commands available on the IEEE-488 interface are:

- "IA" Attenuator Actuation Log
- "ID" Instrument ID
- "IE" Elapsed Time Indicator
- "II" Interface Mode Status
- "IL" Hardware UNCAL and Self-Test Error Log
- "IO" Option Loading
- "IR" IEEE-488 Rejected Entry Status
- "IS" Serial Number
- "IT" Self-Test Results
- "IU" UNCAL (uncalibrated) Status
- "IV" Software Revision Level
- "IZ" Compensation Memory Checksum Status

The "IA" command interrogates the attenuator actuation log. Each time an attenuator pad is removed from the RF path, the corresponding counter in non-volatile memory is incremented. This means that if an attenuator pad is removed then inserted, the count will only increment once. The Generator responds to the "IA" command with seven counts (one for each pad). The counts are separated by commas, and the response is terminated with the end of record character (EOR).

Example:

Attenuator Section	Number of Actuations
A6DBL	102
A12DBL	99
A241L	1028
A242L	9
A243L	19,092
A244L	3908
A245L	412

The response to the "IA" command will be:

```
0000000102,0000000099,0000001028,0000000009,0000019092,0000003908,
0000000412<EOR>
```

When the "ID" command is sent, the Generator responds with its instrument model number (and end of record character) as in "6062A"<EOR>.

When the "IE" command is sent, the Generator responds with the time the instrument has been in operation since it was manufactured. The time is 9 digits, a decimal point, a 10th digit and the <EOR> character. The time is in .1 hour units. For example, "000003459.3"<EOR> indicates 3459.3 hours of operation since manufacture.

The "II" command interrogates the current selection of interface modes. A 5-digit integer followed by the <EOR> character is the sum of the modes selected as follows:

```
Terminator mode= 1
Record mode    = 2
Valid mode     = 4
Unbuffered mode= 8
Error mode     = 16
```

The "IL" command interrogates the ten-entry error log in non-volatile memory. An error can be either a failed self test or an uncalibrated condition as a result of a hardware status line (UNLOKL or UNLVLL). The elapsed time since the instrument was manufactured is also logged with the error.

Each of the ten error log entries consists of an uncalibrated (uncal) code or self-test code, followed by a semicolon (";") followed by the elapsed time, followed by the end of record character (<EOR>). The format of the uncal code, self-test code, and elapsed time are identical to the response when interrogating the uncal code ("IU"), the self-test code ("IT"), and elapsed time ("IE") respectively.

In response to the IEEE-488 command interrogate error log ("IL") command, ten error log entries will be sent, with the most recent error sent first.

Example of an error log:

```
000,000,002,000;000039012.2<EOR> Self-test error (non-volatile memory failure) at
39012.2 hours since manufacture.
```


000,-000,002,000;000039012.1 (EOR) Self-test error (non-volatile memory failure) and the self-test sequence was aborted as indicated by the "-". This error occurred at 39012.1 hours since the Generator was manufactured.

000000,000000,000006;000020243.2 (EOR) Amplitude became unleveled at 20243.2 hours since manufacture (third number = 4). In addition, the peak amplitude was set to > +13 dBm as indicated by the second uncal code (third number = 2).

000000,000000,000000;0000000000.0 (EOR) This special code means that there are no errors. One or more of the error log entries will be this code when less than ten errors have been logged since the Generator was manufactured.

The number of characters in this response is much larger than the 80 characters in the output buffer. Therefore, if the entire response is not transferred to the IEEE-488 controller, the Generator will not be able to respond to further programming commands. Any remaining portion of the response that is not to be transferred can be cleared only by sending a device clear to the Generator.

The "IO" command interrogates the Generator for its option complement. The returned record contains three integers, separated by commas, that indicate configuration of options. For the 6062A, the first number is always 4, which indicates that the Generator being interrogated is in fact a 6062A. The two remaining numbers are the sum of the option related numbers as follows:

2nd Number

- 4 = -130 High-Stability Reference Option
- 8 = -651 Low-Rate FM Option
- 16 = -132 Medium-Stability Reference Option

3rd Number

- 2 = -830 Rear Panel RF Output, MOD Input, and Pulse MOD Input Option

For example, "4,12,2" would indicate a 6062A with Options -651, -130, and -830.

The "IR" command interrogates the Generator for rejected entry error status. (See Table 2-6 for a list of rejected entry codes.) The returned record is the sum of errors that have been detected while processing IEEE-488 commands. The status is cleared when interrogated with the "IR" command. The status can also be explicitly cleared with the "CE" command and is also cleared on the "CL" command and the clear interface messages DCL and SDC.

The "IS" command interrogates the Generator for the serial number. The serial number is an eight character ASCII string terminated with an (EOR). The returned record is similar to the following:

"01230000" (EOR)

The "IT" command interrogates the Generator for the self-test results. The self-test results are reported in four fields. Any non-zero codes indicate that some tests have failed. Table 2-14 shows the self-test error code format. The self tests are performed at power-on and can also be initiated with special function 02. Further details of the self-test results are listed in the Section 4 of this manual.

Table 2-14. Self-Test Error Codes

MOD	FREQ	AMPL
aaa	-bbb ccc	ddd
aaa = Modulation (AM and FM) test results		
- = If displayed, indicates the self test did not complete		
bbb = Frequency test results		
ccc = Digital test results		
ddd = Amplitude test results		

The "IU" command interrogates the Generator for the UNCAL entry status. (See Table 2-5 for a list of the UNCAL Error Codes.)

The "IV" command interrogates the Generator for its current software revision level. The returned record is similar to the following:

"V1.0" (EOR).

This means that the software revision level is Version 1.0.

The "IZ" command interrogates the Generator for the compensation memory status. The status is returned as three fields similar separated by commas, to the following:

"000010,100001,000000" (EOR)

See the paragraphs on Compensation Memory Status located in Section 4D for details.

2-59. MONITOR COMMANDS

The Generator monitor commands are intended for troubleshooting and maintenance procedures. They allow the instrument hardware to be programmed to states not normally possible with the regular programming commands.

CAUTION

The output of the Generator is not guaranteed if the Generator hardware has been changed with these monitor commands.

There are three types of monitor commands: Input/Output, Read/Write, and Hardware Control. Table 2-15 lists the Input/Output types of monitor commands. Table 2-16 lists the Read/Write types of monitor commands. Table 2-17 lists the Hardware Control types of monitor commands.

2-60. SRQ COMMANDS

The Generator asserts the SRQ bus management line on the IEEE-488 Interface bus whenever the Generator requires service. The controller can then perform a serial poll to determine the need for service. The set mask command is used to designate those needs that require service. The SRQ commands are as follows:

"SM" Set SRQ Mask
 "IM" Interrogate SRQ Mask
 "XF" Local Operation Alert Mode

Table 2-15. Input/Output Monitor Commands

COMMAND NAME	COMMAND	VALUE	DESIGNATOR NAME	LOCATION
Read Input Bit	"IB"		"EX"	exrefl A2, U11-7
			"HS"	hsoptl A2, U11-17
			"IE"	ieinl A2, U11-8
			"LF"	lrfm A2, U40-8
			"MH"	mlevhi A2, U40-13
			"ML"	mlevlo A2, U40-7
			"RO"	roptl A2, U40-17
			"RP"	rppl A2, U11-11
			"RT"	rptrpl A2, U11-14
			"SR"	shrefl A2, U40-4
			"TQ"	trseql A2, U40-3
			"UK"	unlok A2, U11-3
			"UV"	unlvl A2, U11-18
			"XA"	extra-a A2, U40-14
			"XB"	extra-b A2, U40-18
Set Output Bit	"OB"	0 or 1	"CL"	clr A2, U17-12
			"DA"	dcam A6, U13-19
			"EA"	extam A6, U13-12
			"EF"	extfm A6, U13-6
			"EP"	ext pulse A6, U13-16
			"FE"	fmen A4, U19-19
			"FP"	front-panel A2, U17-5
			"HO"	haocth A6, U9-2
			"HT"	het A6, U9-6
			"IA"	intam A6, U13-9
			"IF"	intfm A6, U13-5
			"IP"	int pulse A6, U13-15
			"LP"	mid A6, U9-5
			"MA"	rmux0 A4, U32-5
			"MB"	rmux1 A4, U32-2
			"MF"	mf400L A6, U13-2
			"MG"	module-gate A2, U17-15
			"MS"	trmodl A4, U32-15
			"NE"	nven A2, U17-9
			"PM"	phase mod A6, U23-12
			"RI"	rinh A4, U32-9
			"RR"	rprst A2, U17-6
			"SE"	shen A4, U32-12
			"SH"	shet A4, U32-16
			"VH"	vcohl A6, U23-9
			"VQ"	vcoq A6, U23-15
			"XA"	x2l A6, U9-19
			"XB"	x2fl1 A6, U9-16
			"XC"	x2fl2 A6, U9-15
			"XD"	x2fl3 A6, U9-12
			"XE"	x2fl4 A6, U9-9
			"XO"	xoenl A4, U32-6
			"YA"	extra-a A2, U17-19
			"YB"	extra-b A2, U17-2
			"YC"	extra-c A2, U17-16
Set Output DAC	"OD" (integer value)	(DAC Location)		
		0-255	"AM"	AM dac A6 U19
		0-127	"AT"	attenuator A2, U27-5, 6, 9, 12, 15, 16, 19
		0-4095	"FM"	FM dac A6, U25-4 through 15
		0-7	"FR"	FM range A4, U19-12, 15, 16
		0-1023	"KN"	kn dac A4, U27-4 through 13
		0-1023	"KV"	kv dac A4, U29-4 through 13
		0-16383	"LE"	level dac A6, U21
		0-255	"TC"	temp.comp.dac A6, U19

Table 2-16. Read/Write Monitor Commands

COMMAND NAME	COMMAND SYNTAX	NOTES
Read byte	"RB" memory location	1
Read word	"RW" memory location	1
Define write address	"DW" memory location	2
Write byte	"WB" value	2
Write word	"WW" value	2

Notes

1. The Generator responds to these commands with the value of the byte or word in the memory location addressed. The memory location must be an unsigned integer. The value returned is followed by an <EOR>. If the memory location is specified in hexadecimal, then the value is returned in hexadecimal preceded by an "X".
2. The Define Write Address command specifies the write address used with the Write Byte and Write Word commands. When the Write Byte and Write Word commands are used, the specified data will be written to that write address.

Table 2-17. Hardware Control Monitor Commands

COMMAND NAME	COMMAND SYNTAX	NOTES
Read attenuation	"XA"	1
Set attenuation	"XB" integer	1
Set synthesizer frequency	"XD" floating point decimal "MZ"	2
RF on/off	"XR" "0" or "1"	3

Notes

1. The current settings of the Attenuator can be read or set. The attenuation is a number from 0 to 23 where the number specifies the multiple of 6-dB attenuation. Zero indicates no attenuation, and 23 is the maximum attenuation. Only the attenuators are changed with the "XB" command. The value of the attenuation will be output on the "XA" command as an unsigned integer followed by <EOR>.
2. The "XD" command can be used to program the Generator to the specified frequency. Only the synthesizer circuits on the Synthesizer PCB is programmed. No offset is added, no filters are programmed, no VCO compensation is calculated, and no level correction is calculated.
3. "XR0" programs all attenuation, and "XR1" restores the attenuator to its previous state.

The SRQ mask is set to the sum of the reason values listed in Table 2-18. These reason values correspond to the allowable reasons that will be requiring service. The SRQ Mask is set by the following command sequence:

"SM" Sum of Reasons

Table 2-18. SRQ Mask and Status Values

VALUE	REASON	TRUE	FALSE
1	Ready	Input buffer is empty and no commands are being processed.	Input buffer is not empty or commands are being processed.
2	Rejected Entry	IEEE-488 rejected entry; error code is not zero.	IEEE-488 rejected entry error; code is zero.
4	Uncalibrated	RF output is not calibrated. (Front panel 'UNCAL' indicator is lit.)	RF output is calibrated. (Front panel 'UNCAL' indicator is not lit.)
8	Power on	Instrument has powered up.	Special function 08.
16	Output valid	RF output is settled.	RF output is not settled.
32	Not Used		
64	RQS	SRQ mask ANDed with currently set values is not zero.	Reason for SRQ goes away or serial poll is performed.
128	Front panel	Special function 07.	Special function 08.

The Generator asserts SRQ when one of the allowed reasons becomes true. The serial poll response is the sum of those values for reasons that are currently true, independent of the setting of the SRQ mask. For example, if the rejected entry SRQ is enabled with "SM2" and a rejected entry occurs, the serial poll response will indicate that the Generator generated the SRQ (value of 64) and that a rejected entry occurred (value of 2). In addition, other values may be set. The default SRQ mask is 192.

The "IM" command interrogates the current SRQ mask, and an integer is returned.

The "XF" command enables a mode that causes an SRQ to be generated any time the Generator processes an entry. In this mode, a front panel SRQ is generated (i.e., the serial poll response indicates that a front panel SRQ is the cause of the SRQ). This mode is enabled and disabled with its own command, not through the Set SRQ Mask commands (as are all other SRQs). The Alert Mode is enabled/disabled as follows:

"XF0" = Alert Mode off, "XF1" = Alert Mode on.

2-61. TRIGGER COMMANDS

The Generator has the ability to preprogram a command string of arbitrary Generator programming commands. This command string is executed whenever the trigger command "TR" or the IEEE-488 group execute trigger interface message (GET) is received. This method of programming the Generator can be used when a long string of commands is to be sent to the Generator over and over. The programming time is shortened by the time required to transmit the string of characters from the controller to the Generator.

The trigger commands are as follows:

- "CT" Configure Trigger
- "TR" Execute Trigger Buffer

The configure trigger command is followed by a string of any Generator programming commands up to 71 characters in length. The validity of the programming commands is not checked until the trigger buffer is executed. The power-on value of the trigger string is null (nothing).

The trigger command causes execution of the trigger buffer, which has been preprogrammed with the configure trigger command "CT". The trigger buffer can also be executed by sending the IEEE-488 group execute trigger interface message (GET).

2-62. Command Processing

The following paragraphs describe how IEEE-488 commands are processed by the Generator. Command processing is a term for how commands are executed and how errors are handled.

2-63. COMMAND EXECUTION

The execution of the IEEE-488 commands depends on the selection of interface modes with one exception: if an IEEE-488 input is buffered and the buffer becomes full, command execution starts and no further input is accepted until there is room in the input buffer. For more details, refer to the paragraphs on Interface Modes.

2-64. ERROR HANDLING

The Generator detects two types of errors while processing IEEE-488 commands: syntax errors and processing errors. All errors are accumulated until the error status is interrogated or is explicitly cleared. The IEEE-488 rejected entry status is interrogated with the "IR" command. The error status is cleared with one of the following commands:

- "CE" Clear Error Command
- "CL" Clear Command
- "DCL" or "SDC" Clear Interface Messages

The error status is also cleared at power-on.

The SRQ mask can be set to assert SRQ when an error is detected. The SRQ is unasserted when the error status is cleared.

Syntax errors are commands that do not have the correct syntax for the specified header. For example, "FE5" is a syntax error because the external FM command requires a Boolean numeric field. Unrecognized headers are also syntax errors. An IEEE-488 syntax error causes all commands from the point of the error up to the next string terminator or record terminator to be ignored.

Processing errors are commands that are syntactically valid, but the requested value is outside the range of programmable values. For example, "FR99GZ" is syntactically correct, but the Generator cannot be programmed to a frequency of 99 gigahertz. Command processing continues with the next command.

2-65. Timing Data

The programming time can be broken down into four groups: transfer of commands to Generator, command parsing time, software programming time, and instrument settling time.

The total programming time depends on the selection of the interface modes. In some modes, programming steps are performed in parallel and can increase throughput. The following paragraphs give some typical timing data for the above four programming steps and describes how the interface modes affect their relative timing.

2-66. TRANSFER OF COMMANDS TO GENERATOR

The maximum rate of transfer is 0.4 to 0.5 ms per character. With most IEEE-488 controllers, all characters sent with a single output or print statement is transferred together at the maximum rate. The total time to transfer commands to the Generator is obtained by multiplying the number of characters by the rate of transfer.

2-67. COMMAND-PARSING TIME

Command-parsing time is the sum of the time required to process the header, the numeric, and the suffix. Some commands do not have numerics or suffixes. Table 2-19 gives the typical time it takes to process the different components of a command.

2-68. SOFTWARE PROGRAMMING TIME

The minimum time required to process a command is 25 ms. Most of the commands that do not program the hardware (such as storing step values) are programmed in 25 ms. Table 2-20 gives the typical time value for programming the different functions in the Generator.

2-69. INSTRUMENT-SETTLING TIME

The software-programming time typically exceeds the setting time of the RF circuitry, therefore the instrument-settling time can generally be excluded from throughput calculations. The exception is when level correction is disabled. Disabling level correction reduces the software-programming time by 10 ms but does not affect the switching time of the frequency programming circuitry. A 10 ms settling delay should be added if level correction is disabled and a settled RF output is required.

2-70. TIMING OPTIMIZATION

Timing depends upon the interface modes selected. Refer to the paragraphs on Interface Mode Commands for a complete description of the interface modes.

The transfer of commands from the IEEE-488 controller to the Generator can never be processed in parallel with anything else. The transfer of commands usually happens simultaneously, regardless of which interface mode is selected.

The parsing of the command and programming the new instrument state is performed one message unit at a time. The record mode selects a command or a record as the message unit. The record mode off ("RMO") is slower since there is extra processing between message units, and the message unit is smaller.

If the valid mode is enabled, the processing of message units is delayed until the Generator has settled from the previous message. While the RF output is typically settled before the software finishes processing the command, enabling the valid mode adds an additional delay to ensure that the output is settled before the next command is processed.

If the output of the Generator does not need to be settled between programming strings, the valid mode should be turned off to speed up processing. If the output does not need to be settled between commands, but needs to be settled between records, enable the valid mode and the record mode. The instrument processes commands within the record as fast as possible and waits for the output to settle only between records.

2-71. Power-On Conditions

The instrument parameters at power-on are listed in Table 2-21. The remote clear commands can be used to reset all parameters except the last memory location and the remote/local state.

Table 2-19. Command-Parsing Time

COMMAND COMPONENT	TIME
Header	2 ms
Boolean Numeric	1 ms
Unsigned Integer Numeric	2 ms + 1 ms per character
Floating-Point Numeric	2 ms + 1 ms per character
Trigger-String Numeric	10 ms + 0.5 ms per character
Suffix	1 to 1.5 ms
Learn-Interface Suffix	40 ms

Table 2-20. Typical Programming Time of the Generator Functions

FUNCTION	TIME (IN ms)	NOTES
Frequency	70	1, 2, 3, 4
Amplitude in Volts	60	1, 2, 4, 5
Amplitude in dBm	95	1, 2, 4, 5
AM Depth	50	2, 4
FM Deviation	35	4
Modulation Frequency	25	
Enable/Disable AM	55	2,
Enable/Disable FM	25	
Recall 98	190	6, 5
RF Output On	50	2
RF Output Off	35	
<p>Notes</p> <ol style="list-style-type: none"> 1. May take up to 5 ms longer if the relative mode is enabled. 2. Can save 10 ms if all level correction is disabled with special function 81. 3. Add 20 ms when frequency changes from greater than or equal to 245 MHz to less than 245 MHz. 4. Edits and steps may take up to 5 ms longer than the programming function directly. 5. Add 15 ms when the Attenuator settings change. 6. Recalls vary considerably depending on the stored data. Maximum is approximately 250 ms. 		

Table 2-21. IEEE-488 Power-On State

INSTRUMENT PARAMETER	STATE	NOTES
Memory location parameters	same as power off	1
RF on/off	same as power off	
Last memory location	same as power off	2
Remote/local state	local	
IEEE output buffer	cleared	
IEEE input buffer	cleared	
Valid mode	same as power off	
Record terminator	same as power off	
Unbuffered mode	same as power off	
Record mode	same as power off	
SRQ mask	same as power off	
Trigger configuration	same as power off	
SRQ interface signal	may be asserted	3
<p>Notes</p> <ol style="list-style-type: none"> 1. The contents of memory location 98 (Instrument Preset State) is listed in Table 2-3. 2. The last memory location is used for sequence operations. 3. If the SRQ mask has the power on, output valid, or ready SRQ enabled, the SRQ interface signal is asserted on power-on. 		

Section 3

Theory of Operation

3-1. INTRODUCTION

This section of the manual describes the theory of operation for the Generator. The following four major topics are covered:

- General Description
- Functional Description
- Software Operation
- Detailed Circuit Descriptions

The General Description briefly explains the functions and components of the three major sections of the Generator. The Functional Description covers the main output parameters, amplitude, frequency, and modulation. The Software Operation section describes the software and how it affects the hardware. The Detailed Circuit Description is a comprehensive explanation of the operation of each circuit assembly.

3-2. GENERAL DESCRIPTION

The Generator has three major sections. The front panel section includes the keyboard and display for local control. The module section includes the frequency, level, modulation, and control circuits. The rear panel section includes the power supply, cooling fan, and assorted external connectors.

3-3. Front Panel Section

The front panel section of the Generator provides the operator interface. It includes the primary controls, connectors, and indicators of the Generator. All front panel keys and displays (except the power switch that controls the power supply directly) are monitored and handled by the A2 Controller PCA located in the module section.

3-4. Module Section

The module section is a multi-compartmented, shielded enclosure that includes the circuits that generate the instrument stimulus functions: frequency, modulation, and amplitude. The A2 Controller PCA is also located here. The A2 Controller PCA governs the Generator operation and determines if any options are installed by checking the option status bits at power-on. The optional Low-Rate FM PCA is also located in this section of the instrument.

3-5. Rear Panel Section

The rear panel section includes the power supply, the cooling fan, various external connectors, the IEEE-488 Interface, and the optional High-Stability Reference PCA.

The power supply is a linear design providing two +15V, -15V, +5V, +37V, +18V, and 6V ac to the Generator. All the power supplies are series-pass regulated except the 6V ac filament supply and the +18V supply, which provides power to the Attenuator/Reverse Power Protector (RPP) relays. A fuse/filter/line-voltage selector allows the Generator to operate from any common supply voltage.

The dc fan is powered from the unregulated +5V supply. The fan operates only when line power is available and the front panel POWER switch is ON.

The Option -130 High-Stability Reference operates whenever the instrument is plugged into an active ac outlet, regardless of the position of the instrument POWER switch.

3-6. FUNCTIONAL DESCRIPTION

The following paragraphs describe the key output parameters of the Generator: level, amplitude modulation, frequency, frequency modulation, phase modulation, and pulse modulation.

3-7. Level

Level control is provided by two separate circuits, a step attenuator and a vernier level DAC. The A8 Attenuator/RPP Assembly provides coarse level control in 6.02-dB steps. Fine level control is provided by a vernier level DAC that varies the leveling-loop control voltage. The microprocessor automatically controls the step attenuator and the vernier level DAC. The microprocessor also applies level correction to compensate for the Generator frequency response.

Each Generator has level correction data for both the Output Assembly (A6 + A7) and the A8 Attenuator/RPP Assembly. The level correction data is stored in the compensation memory located on the A2 Controller PCA. The level correction data is based on the measurements of each assembly during calibration of the Generator.

The level correction data is applied only to the vernier level DAC and does not affect the coarse level control provided by the Attenuator/RPP Assembly. In other words, all Generators have the same attenuator pads inserted at a selected level even though the correction data is different for each Generator.

To improve level accuracy in relation to temperature, the Generator uses a software temperature compensation technique. This technique uses data that is the same for all Generators.

3-8. Amplitude Modulation

The output of the level DAC is the leveling loop control voltage. The Generator output signal is amplitude modulated by varying this control voltage with the modulating signal. A 1V peak modulating signal from the internal modulation oscillator or from the external MOD INPUT connector is applied to the AM DAC (a multiplying digital-to-analog converter). The multiplying factor of this DAC, corresponding to the programmed percentage of modulation, is calculated by the A2 Controller PCA.

The modulation signal from the AM DAC is summed with a fixed dc reference voltage. The composite signal (dc plus modulation) is applied to the level DAC (a level control-multiplying DAC). The multiplying factor for this DAC is also handled by the A2 Controller PCA and corresponds to the programmed signal level. The multiplying factor also includes the level correction information stored in the compensation memory.

The operation of the ALC loop causes the amplitude of the RF signal to conform to this varying control voltage, thus causing amplitude modulation of the Generator output.

3-9. Pulse Modulation

An input pulse signal triggers a comparator that drives a TTL gate. The gate output is converted to a differential drive signal that controls transistor level shifters. These differential signals then drive a single-pole single-throw switch located in the main signal path before the final amplifier. An input voltage below 1V turns the switch off if pulse is enabled; otherwise, the switch is on.

3-10. Frequency

The 0.1-MHz to 2100-MHz frequency coverage is divided into the following four bands:

HET band	0.1 to 244.99999 MHz
mid band	245 to 511.99999 MHz
high band	512 to 1049.99999 MHz
X2 band	1050 to 2100 MHz

The high and mid bands are derived directly from a voltage-controlled oscillator (VCO) followed by a binary divider which is part of the main phase-locked loop (PLL).

The PLL synthesizes the mid band using a modified N-divider loop with a single-sideband mixer (SSB) in the feedback path. The reference frequency for the loop is 1 MHz, which would normally provide 1-MHz steps in a conventional N-divider loop. However, this Generator provides 0.02-MHz steps by using a modified N-divider with a pulse deletion circuit which is controlled by a rate multiplier.

Additional resolution is gained by introducing a signal from the subsynthesizer circuit into the main PLL through the SSB mixer in the feedback path. This signal provides internal frequency steps of 5 Hz. The subsynthesizer consists of a 14-bit rate multiplier followed by a divide-by-1000 circuit.

The main PLL bandwidth varies with the programmed frequency (due to N changing and variations in the VCO tuning coefficient). The A2 Controller PCA uses compensation to program the phase detector gain via the KN DAC to maintain constant loop bandwidth. By keeping the loop bandwidth constant, loop stability and modulation transfer is controlled, thus ensuring accurate, wideband FM.

3-11. Frequency Modulation

Frequency modulation is achieved by applying the modulation signal simultaneously to both the PLL VCO and the phase detector. Modulating either the PLL VCO or the phase detector alone results in FM with a high-pass filter characteristic, or phase modulation with a low-pass filter characteristic. The filter characteristic cutoff frequencies are equal to the PLL bandwidth.

The modulating signal applied to the PLL VCO and the phase detector is adjusted in amplitude by the KV DAC to compensate for variations in the VCO tuning coefficient. This compensation is done automatically by the A2 Controller PCA using compensation data measured on the VCO in each Generator. The compensation data is stored in the compensation memory.

By integrating the modulation signal applied to the phase detector and simultaneously applying the modulation signal to the PLL VCO, the two effects are complementary and result in a flat FM response.

3-12. Phase Modulation

Phase modulation is achieved by differentiating the modulation input signal (internal or external) before applying the modulating signal to the frequency modulating circuits. The display is correspondingly changed to indicate deviation in radians.

3-13. SOFTWARE OPERATION

The Generator software is executed on a Texas Instruments TMS 9995 microprocessor located in the A2 Controller PCA. The instrument program is stored in 64K bytes of ROM. A battery backed CMOS RAM contains 4K bytes of scratch pad RAM, 2K bytes of non-volatile memory for front panel setups, and 2K bytes of non-volatile compensation memory. A 2K byte EEPROM contains a redundant copy of the instrument specific compensation data. In addition, there are 250 bytes of scratch pad RAM built into the microprocessor. The software provides the following general functions:

- Interfaces with the front panel keys and the IEEE-488 Interface to provide access to the Generator functions.

- Configures the Generator functional blocks to produce the required output, and then applies linearization and compensation data to optimize the instrument performance and resolution.

- Implements a set of self test and diagnostic functions.

- Provides software compensation procedures that allow the user to generate the instrument specific compensation data.

3-14. User Interface

The Generator software is implemented with a simple operating system that allows several tasks to operate in a round-robin fashion on an equal priority basis. Input and output to both the front panel and the IEEE-488 Interface execute at a higher priority and are handled as interrupt routines.

At power-on, the software performs an instrument self test and initializes both the RAM and the Generator hardware. Three tasks are continuously in operation:

- Service task
- Key task
- IEEE-488 task

The service task checks the status signals. The key task and IEEE-488 task process user input. A fourth task is activated only when needed to process certain UNCAL (uncalibrated) or REJ ENTRY (rejected entry) conditions that cause the instrument STATUS display to flash.

3-15. Amplitude Control

Amplitude is programmed using a 23-step (6.02 dB per step) A8 Attenuator/RPP Assembly and a 14-bit vernier level DAC (located on the A6 Output Control PCA). The level DAC settings depend on a combination of the programmed output level and amplitude correction data.

The amplitude correction data compensates for level variations that are dependent on the output frequency of the Generator. Correction factors are stored in the compensation memory.

3-16. Attenuators

There is one 6-dB, one 12-dB, and five 24-dB sections on the A8 Attenuator/RPP Assembly. These sections are programmed in combination to provide coarse level control in 6-dB steps. The indicated voltages at which the attenuator sections change steps are $2 \cdot x$ volts, where

$x = 1, 2, 3, \dots, 23$ for non-AM, or

$x = 2, 3, 4, \dots, 24$ for AM operation

Table 4D-18 lists the attenuator sections programmed for various displayed levels.

3-17. Level DAC

The level DAC setting is calculated from the Generator output level. If level correction is enabled, the level DAC setting is further modified by the data stored in the compensation memory.

To minimize level transients that could damage external circuitry, the following sequence is used in programming the attenuator sections and the level DAC when the attenuator setting is changed:

1. The level DAC is programmed to zero.
2. The attenuator sections are reprogrammed for correct attenuation.
3. After a 5-ms wait to allow the attenuator sections to settle, the level DAC settings are programmed.

3-18. Temperature Compensation

The temperature compensation DAC (TC DAC) data is stored in the Generator software as a function of the output frequency (F_o). This data is the same for each Generator.

3-19. Reverse Power Protector

The reverse power protector (RPP) protects the Generator from damaging voltages applied to the RF OUTPUT connector. The status line RPTRPL indicates whether the RPP circuitry has tripped. If the RPP trips, the RF output is programmed off, and the RF OFF indicator flashes. The RPP circuitry is reset by the operator turning the RF OUTPUT on. This causes the A2 Controller PCA to reset the RPP by toggling RPRSTL and programming the RF on.

3-20. Frequency Reference Control

Programming of the frequency reference control bits depends on the setting of the INT/EXT reference switch as well as if either the Option -130 High-Stability Reference or the Option -132 Medium-Stability Reference are installed.

3-21. Frequency Control

The output frequency (F_o) is programmable with 10-Hz resolution from 0.1 MHz to 1050 MHz and with 20-Hz resolution from 1050 MHz to 2100 MHz. The filter and band control bits are programmed in nine bands and are determined by the output frequency (F_o). A synthesizer frequency is determined for each band.

The programming data of the KV and KN DACs are calculated from the synthesizer frequency and the instrument-specific VCO compensation data.

3-22. Modulation On/Off

The nine modulation modes are:

- Internal AM
- External AM
- External DC AM
- Internal FM
- External FM
- Internal ϕ M
- External ϕ M
- Internal Pulse
- External Pulse

The modulation modes can be programmed separately or in combination (only FM and ϕ M are exclusive). The AM depth and FM deviation DACs are always programmed whether or not modulation is enabled. When enabling or disabling modulation, only the modulation control bits are programmed. The External DC AM mode is enabled if both external AM and the DC AM special function are enabled. Table 4D-20, Modulation ON/OFF Control, lists the control states for each modulation choice.

3-23. Modulation Frequency

The two internal modulation frequencies of 400 Hz and 1000 Hz are programmed with a single control bit MF400L. Table 4D-21, Modulation Frequency Control, lists the MF400L control states.

3-24. Amplitude Modulation

The Generator allows amplitude modulation depth programming from 0 to 99% with 1% resolution. When the combination of signal amplitude and programmed AM depth exceeds +16 dBm peak from 0.1 MHz to 1050 MHz or +13 dBm peak above 1050 MHz, the UNCAL indicator lights to warn that the output level is no longer guaranteed. Amplitude modulation depth is programmed using the 8-bit AM DAC. A setting of 200 on the AM DAC corresponds to 100% AM modulation.

3-25. Frequency Modulation

Frequency modulation (FM) is programmable with three digits of resolution in four decade ranges. The FM DAC is a 12-bit DAC programmed to the FM deviation in Hz divided by the resolution. In addition, the FM DAC is scaled to compensate for the effects of the frequency doubling circuitry. The FM/ ϕ M modes are selected by the control bit PMODL. Table 4D-15, FM Deviation Control, lists the FM DAC, FM Range, and PMODL bit settings.

The maximum programmable FM deviation is dependent on the RF output frequency. FM deviations up to 400 kHz may be entered regardless of the output frequency. However, the UNCAL indicator is flashed and the FM DAC is clamped at full scale if the entry is beyond the allowed upper limit for that frequency band. Table 4D-14, FM Deviation Limits, lists the maximum programmable deviation in each frequency band.

3-26. ϕ Modulation

Phase modulation (ϕ M) is programmable with three digits of resolution in four decade ranges. Phase modulation is internally normalized to 10 kHz, then programmed as FM deviation. The ϕ M index is multiplied by 10 kHz (regardless of the modulation frequency) to get the equivalent FM deviation used to calculate the FM DAC and FM range bits. Table 4D-16, Phase Modulation Control, lists the FM DAC, FM range, and PMODL bit settings.

The maximum programmable phase modulation deviation is dependent on the RF output frequency. Phase modulation deviations up to 40.0 radians may be entered regardless of the output frequency. However, the UNCAL indicator is flashed and the FM DAC is clamped at full scale if the entry is beyond the allowed upper limit for that frequency band. Table 4D-14, FM Deviation Limits, lists the maximum programmable phase modulation deviation in each frequency band.

3-27. Compensation Memory

The compensation memory contains the instrument specific compensation data for the VCO, Output, and Attenuator assemblies. The integrity of this data is crucial to the performance of the Generator. Two redundant copies of the data are kept in two separate non-volatile memory ICs.

Hardware and software protection schemes guard against accidental destruction of the data. The compensation switch on the A2 Controller PCA must be set to the ON position when updating the compensation memory.

The compensation memory self test verifies the CRC checksums of each data segment. A detailed report of the compensation memory status can be interrogated from the front panel or the IEEE-488 interface. If compensation memory errors are detected by the self test, the Generator only uses the valid data segments. See Section 4D for more information on the compensation memory.

3-28. Self Test

At power-on, the Generator automatically self tests the digital and analog circuits. If the Generator fails any self test, the test results are automatically displayed as error codes. Several special functions are available for additional tests (see Section 4D). In addition, the microprocessor continuously monitors two status signals, UNLVL (unleveled) and UNLOCK (unlocked).

The self tests can also be initiated by using the [SPCL][0][2] keys. The results of the self test can be displayed in the four display fields with [SPCL][1][1] keys and can also be transmitted using the IEEE-488 Interface.

Self tests 1 through 5 are digital checks that indicate the general functionality of the A2 Controller PCA. Self tests 6 through 10 use the two status signals UNLVL and UNLOCK to test the general functionality of the RF circuitry.

During the self-test sequence, all attenuator sections are programmed ON (maximum attenuation) to prevent unwanted signals at the output. In addition, the Generator is programmed to the internal frequency reference.

The self-test error codes and descriptions are listed in Section 4D. A brief description of the different Generator self tests are as follows:

- Test 1. The Generator's RAM is verified by writing to each memory location and reading the data back. Both the off-chip RAM and the on-chip RAM are tested.

- Test 2. The compensation data in the compensation EEPROM and in the battery-backed RAM is verified. The CRC checksum in each data segment (VCO, Output, and Attenuator) in both memory ICs are checked.
- Test 3. Each memory location of the non-volatile RAM is checked with a checksum.
- Test 4. The data in each word of the two software EPROMs is successively summed and rotated. The result of this procedure is compared with a checksum for each EPROM.
- Test 5. The IEEE-488 circuitry is verified by the microprocessor writing data to the IEEE-488 chip and then reading it back.
- Test 6. The low-pass and band-pass filters on the A6 Output Control PCA are tested by setting the frequency within the pass band of the filters and verifying that the output is leveled. The frequency is then set outside of the pass band of the filters and the output should be unleveled.
- Test 7. The synthesizer operation is verified by programming the Generator to a normal operating frequency and checking to see that the instrument is locked. Next the Generator is programmed to a synthesizer frequency below 225 MHz and then above 550 MHz and is checked to see that the instrument becomes unlocked.
- Test 8. The PLL operation is verified by forcing a large change in frequency. When this is done, the Generator should become unlocked and then lock again.
- Test 9. Frequency modulation is verified by simulating an overmodulation condition, then checking the unlocked indicator. This is done by programming internal FM off and the KN DAC to zero.
- Test 10. Amplitude modulation is verified by overmodulating the carrier and then checking the unleveled indicator. This is done by programming a high output level and programming INT AM on with a high AM depth.

3-29. Special Functions

There are several special function self tests that are used as troubleshooting aids. Refer to Section 4 for detailed information regarding these troubleshooting tests.

3-30. Software Compensation Procedures

The software compensation procedures provide the user with the ability to generate the instrument-dependent FM, output, and attenuator compensation data. Section 4E, Software Compensation Procedures, describes the procedures in detail.

3-31. Status Signals

Four status signals indicate which option is installed in the Generator. These status bits are interrogated at power-on as follows:

- HSOPTL indicates Option -130 High-Stability Reference is installed.
- MSREFL indicates Option -132 Medium-Stability Reference is installed.
- LRFML indicates Option -651 Low-Rate FM is installed.
- ROPTL indicates Option -830 Rear Panel RF Output, MOD Input, and Pulse MOD connectors are installed.

The status of the rear panel REF EXT/INT reference switch is continuously monitored with the EXREFL bit. The state of this bit is used by the A2 Controller PCA to display the EXTREF indicator on the front panel and to program the reference source.

The RF output of the Generator is considered calibrated whenever the UNCAL indicator is off. If the calibrated limit of the Generator is exceeded, the UNCAL indicator is lit but not flashing. However, the RF output is still considered usable.

The UNCAL indicator flashes when the output of the instrument is considered unusable. This is the result of a severe overrange condition or when one of the following analog status signals becomes active.

- RPTRPL indicates that the RPP circuitry has tripped. If this occurs, the RF output is programmed off to provide additional protection to the instrument. The RF OFF and UNCAL indicators flash to indicate that RPP has tripped.
- UNLOKL indicates one of several conditions. The PLL could be out-of-lock, the reference circuits could be out-of-lock, or the signal could indicate FM over-modulation (if FM is on). The UNCAL indicator flashes for any of these circumstances.
- UNLVLL indicates that the output is unlevelled. This could also be the result of amplitude over-modulation. With this condition, the UNCAL indicator flashes.

3-32. DETAILED CIRCUIT DESCRIPTIONS

The Generator is divided into three major sections: the front panel section, the module section, and the rear panel section.

The front panel section is mounted in a sheet metal housing and consists of the A1 Display PCA, a switch circuit board and elastomeric switches. The front panel section also includes the display lens, the MOD INPUT connector, and the PULSE INPUT connector.

All the front panel control keys, except the POWER ON/OFF button, consist of an elastomeric membrane sandwiched between the switch circuit board and the front panel sheet metal housing. The switch circuit board consists of a 6-by-8 matrix of open switch contact pads. When a key is pressed, a conductive pad on the back of the elastomeric membrane connects a set of contact pads. The A2 Controller PCA software senses what row and column of the matrix are connected when a key is pressed.

The module section consists of a cast module frame with gasketed covers and includes the following electrical assemblies:

- A2 Controller PCA
- A4 Synthesizer PCA
- A5 VCO PCA
- A6 Output Control PCA
- A7 Output PCA
- A8 Attenuator/RPP Assembly

The rear panel section consists of a fuse, a filter, a line-voltage selector circuit board, a transformer, the POWER ON/OFF switch and cable, the A9 Power Supply PCA, the A3 IEEE-488 Interface PCA, and a fan. The line-voltage selector circuit board accommodates four line voltages: 100, 120/220, and 240 volts. The line-voltage is determined by the orientation of the line-voltage selector circuit board.

The transformer, with its two primary windings, accepts the four voltages and produces the necessary five secondary voltages. The A9 Power Supply PCA rectifies, filters, and regulates these secondary voltages to produce the dc voltages required by the Generator. The dc fan is connected to the unregulated +5V supply.

NOTE

The power supply for Option-130 High-Stability Reference is separate. It has an automatic changeover switch for different input line voltages.

3-33. A1 Display PCA

The A1 Display PCA provides a readout of the programmed modulation, frequency, amplitude parameters, and status information. This displayed information and the bright digit are controlled by the A2 Controller PCA under the direction of the instrument software. The display is comprised of two vacuum fluorescent displays and their associated control circuitry. The two displays are refreshed as four groups of nine display fields (usually a digit) each. The four groups share the digit (grid) strobes but have individual segment (anode) strobes.

3-34. DATA COMMUNICATIONS

Display data is sent through a byte-wide bidirectional data bus from the A2 Controller PCA and is latched by U1 through U5, and U19 on the A1 Display PCA. Latch select signals DIG1L, DIG2L, SEG1L, SEG2L, SEG3L, and SEG9L determine which latch receives the data. Level-shifting buffer drivers U6 through U10 interface the TTL latches directly to the +37V anodes of the vacuum fluorescent displays.

3-35. DISPLAY FILAMENT VOLTAGE

The 6.0V ac filament voltage for the display is derived from a center-tapped winding on the Power Supply PCA transformer (T1). The ac filament voltage is biased at +6.2V above ground by circuitry on the A9 Power Supply PCA to provide a cutoff potential for the displays.

3-36. BRIGHT-DIGIT EFFECT

The bright-digit effect is achieved by providing three extra refresh cycles (strokes) to the specified digit. A grid current-limiting resistor (R3) provides uniform digit brightness by controlling electron depletion from the display cathode filaments.

3-37. SWITCHBOARD INTERFACE

The digit strobe data latched by U1 is buffered by open collector inverters (U13 and U15) and strobes the front panel switch matrix. The switch columns are strobed in unison with the display fields. The switch matrix status is read by the tri-state buffer (U14).

3-38. DISPLAY BLANKING

Monostable (U11) and NOR gate (U12) clear the display if new field or segment strokes are not received. This protects the display if the microprocessor stops refreshing. The display can be blanked manually by pressing [SPCL] [1] [3], which sets the signal CLRL and the output of U11 low, thus clearing latches U2 through U5. To restore the display, press [SPCL] [1] [2].

3-39. MODULATION-LEVEL INDICATOR

The external modulation-level indicator warns the operator when the modulation signal is not set to 1V peak ($\pm 2\%$ typically). The external modulation signal is compared in the dual-comparator (U16) with internal references of 0.98 and 1.02V. Two status bits, MLEVLO and MLEVHI, are at the output of the 0.5 second dual one-shot (U17). If either of these reference voltages are exceeded, the two status bits are sensed by the A2 Controller PCA, which controls the EXT HI and EXT LO indicators in the MODULATION display field.

3-40. A2 Controller PCA

The A2 Controller PCA, under the direction of the instrument software, handles the data interface between the front panel, remote interface, and Generator functions. The A2 Controller PCA is located in a top side compartment of the module section.

The A2 Controller PCA consists of the following functional groups:

- Microprocessor and its interface circuitry
- Attenuator control interface.
- Front panel interface
- IEEE-488 Interface
- Memory ICs and addressing circuitry
- Module I/O circuitry
- Reset circuit
- Status and control latches

3-41. MICROPROCESSOR

The heart of the A2 Controller PCA is U1, a TMS9995 16/8 bit microprocessor. The digital system clock signal is generated by an oscillator comprised of gates from U5 and crystal U41. L3 and C61 form a band-pass filter, which ensures that the crystal will oscillate at its fundamental frequency. When bidirectional buffer U4 is enabled, it provides additional drive current to the data bus signals. When U4 is disabled, it isolates the microprocessor from the system data bus. Buffers U33, U34, and U10 provide extra drive current to the microprocessor address and control signals.

3-42. ATTENUATOR CONTROL INTERFACE

The attenuator control signals are latched by U27. Darlington drivers U30 and U31 control the A8A1 Relay Driver/RPP PCA.

3-43. FRONT PANEL INTERFACE

Data is transferred to and from the front panel circuitry through tri-state bidirectional data buffer U18. This buffer is active when a front panel latch is addressed and the buffer control signal from U17 is low. Otherwise, the buffer is in the high-impedance state. The front panel latch select lines are decoded by U36. To reduce RF emissions from the Generator, low-pass filters and bypass capacitors comprised of the following components are used on the following signals:

SIGNALS	COMPONENTS
Signal CLRL	R6 and C51
Latch select SEG1L	R7 and C53
Latch select SEG2L	R8 and C54
Latch select SEG3L	R9 and C55
Latch select SEG9L	R10 and C56
Latch select DIG1L	R11 and C57
Latch select DIG2L	C46
Buffer select KBINL	C49
Signal MLEVHI	C47
Signal MLEVLO	C48

In addition, capacitors C58 and C59 bypass the display filament supplies, and capacitor C43 is used to bypass the -15 volt supply. LC filters comprised of L1 and C50, and L2 and C52 are used on the +5 volt and +37 volt supplies to the front panel circuitry.

3-44. IEEE-488 INTERFACE

Tri-state bidirectional buffer U3 buffers the data bus to the IEEE-488 interface. Address and control lines to the interface are buffered by tri-state buffer U2. These buffers are in the high-impedance state when the interface is not addressed.

The active low interrupt signal IEINTL, from the IEEE-488 interface, is connected to the level four interrupt on the microprocessor. R1 and C22 form a low-pass filter to suppress digital emissions from the Generator.

3-45. MEMORY

The program instructions and constant data are stored in two 32K-byte EPROMs, U21 and U22. The scratch pad RAM, non-volatile front panel setups, and one copy of the compensation memory are contained in the battery-backed CMOS RAM U25. The 2K-byte EEPROM U24 contains a redundant copy of the compensation memory.

The compensation switch protects the compensation memory from accidental destruction. The switch is connected directly to the enable input of tri-state buffer U46. When disabled, the buffer disconnects the write enable input of the EEPROM from the microprocessor write enable signal. The decoder PAL U20 decodes the memory selects and provides the bank switching logic. U20 also contains the write protection logic for the battery-backed portion of the compensation memory.

3-46. MODULE I/O

Control data is transferred to the RF circuitry (located in the module section) through a byte-wide unidirectional data bus. Control data is retained on the RF circuitry in latches. Select lines BSEL0L and BSEL1L, and address lines BAB2 through BAB0 are decoded into individual latch enables on the various RF circuitry. Tri-state buffers U15 and U16 on the

address and data lines provide extra drive current and allow these signals to float when inactive.

Flip-flop U42 gates the module I/O select pulse from U8 with the system clock, to delay the leading edges of BSEL0L and BSEL1L, thus providing adequate latch setup times. D-flip-flop U9 latches address lines BAB2 through BAB0 to provide adequate address hold times.

3-47. RESET

Comparator U7 and its associated circuitry generate the active low reset signal to the TMS9995. The reset signal is generated on power-up or if the +5V supply drops below +5V.

At power-up, R5 and C4 provide a slow-rising input signal to the Schmitt Trigger inverter pair U44. The output of the second inverter pulls the microprocessor reset input low, then drives it high after approximately 100 milliseconds. When the +5V supply is up, a reference voltage is set at the negative terminal (U7 pin 2). This reference voltage is one diode drop below the voltage at the positive terminal (U7 pin 3). When power is lost, the voltage at the positive terminal falls below the reference voltage held by C3, and the output of U7 is immediately pulled low. This discharges C4 and resets the microprocessor.

3-48. STATUS AND CONTROL

Tri-state buffers U11 and U40 read the three hardware fault detector status signals (UNLVL, UNLOKL, and RPTRPL), the four option status signals (HSOPTL, MSREFL, LRFML, and ROPTL), and the status of the REF INT/EXT and compensation memory switches. Control and buffer enable signals are latched by U17.

3-49. A3 IEEE-488 Interface PCA

The IEEE-488 Interface uses an NEC μ PD7210 Talker/Listener IC (U1) to handle all IEEE-488 standard communications protocol. All data, address, and control lines to the 7210 are buffered on the A2 Controller PCA. Two MC3447 open-collector bus drivers (U3 and U4) interface the 7210 directly to the IEEE-488 bus.

Tri-state buffer U6 provides the status of the IEEE-488 rear panel address switches when interrogated. These switches determine the IEEE-488 bus address and talk-only (to) or listen-only (lo) modes. When opened, the switch just to the left of the IEEE-488 bus connector disconnects the bus shield ground from the system ground.

3-50. A4 Synthesizer PCA

The A4 Synthesizer PCA provides frequency control and modulation of the Generator output. The A4 Synthesizer PCA is located on the top side of the module section. Together with the A5 VCO PCA and a 10-MHz reference frequency, the A4 Synthesizer PCA simultaneously generates a high-band signal that spans 490 to 1050 MHz and a mid-band signal that spans 245 to 512 MHz.

The high-band and mid-band signals are coupled to the A6 Output Control PCA. Heterodyning extends the Generator frequency coverage down to 0.1 MHz, and multiplying extends the Generator frequency coverage up to 2100 MHz.

The A4 Synthesizer PCA consists of the following functional circuits that are described in the following paragraphs:

- 10-MHz Reference
- Sub-Harmonic Reference
- Main PLL
- FM Processing
- 800/40 MHz PLL
- Subsynthesizer

3-51. 10-MHZ REFERENCE

The Generator's reference is the internal 10-MHz crystal oscillator. If Option -130 High-Stability Reference or Option -132 Medium-Stability Reference is installed, the internal crystal oscillator is locked to that oscillator. The internal oscillator can also be locked to an external reference of 1, 2, 2.5, 5, or 10 MHz.

The internal 10-MHz crystal oscillator (XO) is a crystal Y1 and a FET transistor Q39. The frequency is adjusted by C240 and R230. The oscillator signal from Q39 is buffered by Q40, converted to TTL by U55-B, and branches to both the subharmonic phase detector U68, and the 10-MHz rear output via the 10-MHz bandpass filter C247 and L73. In addition, the 10-MHz reference is sent to both the 800/40-MHz loop-phase detector via U59, and the main loop-phase detector via U58.

3-52. Subharmonic Reference

Comparator U67 forms an ac-to-TTL converter. Diodes CR20 and CR21 precondition the REF IN signal to protect the comparator. Resistors R148, R149, R153, and R217 provide hysteresis to prevent oscillation when there is no input.

MOS switch U70 connects the control voltage of the 10-MHz crystal oscillator to a bias network R229, R230, and R231, or to the loop amplifier, thus closing a phase-locked loop.

The phase detector and loop amplifier are made up of U68, Q26, Q27, Q38, and U69. The signal from the external reference input, through the ac-to-TTL converter or the enhanced stability options, is applied to the flip-flop clock input U68-3 via U54. The 10-MHz signal from the crystal oscillator goes to the other flip-flop clock input, U68-11. The two flip-flops are connected. Therefore, the width of the pulse that switches Q38 is the difference in time of these two signals (U68-3 and U68-11). The phase detector operating point is set by R223 and R224.

The output of the loop amplifier is applied as the control signal to the frequency control input of the 10-MHz crystal oscillator through the control switch U70. The control switch U70 is controlled by the A2 Controller PCA through the control line SHENL. This line is enabled when the rear panel REF INT/EXT switch is set to EXT or when the enhanced stability options are present.

An out-of-lock detector is formed with one-shot U71. The out-of-lock detector provides a status output to the A2 Controller PCA that indicates the 10-MHz oscillator is not locked. An out-of-lock condition causes the loop amplifier to have a low-frequency beat note that triggers the one-shot to act as a pulse stretcher.

The output of the one-shot is an active-low signal and is combined through diode CR29 with other signals to form the UNLOCK status signal.

3-53. MAIN PHASE-LOCK LOOP

The main phase-lock loop (PLL) is a fractional divider PLL with a single-sideband mixer (SSB) in the feedback path. The oscillator for this loop is a separate PCA, the A5 VCO PCA. All the remaining PLL circuitry is on the A4 Synthesizer PCA.

The key signals to the main PLL are the 1-MHz reference signal from the 10-MHz reference circuit, the 245- to 512-MHz signal from the binary divider, and the 20- to 40-kHz signal from the subsynthesizer circuit. The fractional division technique provides 20-kHz frequency resolution.

The SSB mixer, in conjunction with the subsynthesizer, provides additional 5-Hz resolution at the synthesizer frequency. This corresponds to 10-Hz resolution on the high band.

The main PLL consists of the VCO, the binary divider, the SSB mixer, the triple-modulus prescaler, the N-Divider, the phase detector, and the loop amplifier. All but the VCO are described in the following paragraphs. The VCO is discussed in paragraphs 3-62.

3-54. Binary Divider and Single-Sideband Mixer

The 490- to 1050-MHz signal from the VCO, via J107, is coupled to the binary divider U1. Regulator Q1 provides +5V for the divider. One output of U1 is coupled to the A6 Output Control PCA through J104. The other output is amplified by Q2 and Q3. This signal is split into two quadrature (90 degrees phase difference) signals by a 3-dB coupler U6.

This signal, and two other audio quadrature signals from U10, are summed in the double-balanced mixers U7 and U8 to produce two double-sideband suppressed-carrier signals. Because of the phase relationship of the outputs of the mixers, the summing of the two composite signals (in resistor network R21 and R22) results in the upper-sideband component being suppressed. The predominate remaining signal is the lower-sideband signal.

The lower-sideband signal, spanning 245 to 512 MHz in 20-kHz steps, is amplified by U9 and applied to the N-Divider where it is divided down to 1 MHz.

3-55. N-Divider

The main components of the N-Divider are:

Triple-Modulus Prescaler (divide by 20/21/22) U18, U19, U20 and N-Divider Custom Gate Array U17

The triple-modulus prescaler (see Figure 3-1), consists of a divide by 10/11 U20, divide by 2 U18A, synchronizing flip-flop U18B, and quad NOR gates U19. If all the inputs (E1, E2, E3, E4, and E5) to the 10/11 divider are low, the prescaler divides by 11, and the total division to the output (U20 pin 7) is 22. If any of the inputs are high, the prescaler divides by 10, and the total division is 20.

If inputs E1 and E3 are low, the modulus of the 10/11 divider is controlled by the output of the divide by 2 U18A. Consequently, the prescaler divides by 10 half the time and by 11 the other half, resulting in a divide by 21. U20 contains the ECL-to-TTL converter. U18B synchronizes the changing of the modulus with the clocking of the subsequent stages. The N-divider is clocked by the composite prescaler output U18A.

The operation of the triple-modulus prescaler is shown in Figure 3-1. The prescaler operates in conjunction with the N-divider gate array shown in Figure 3-2.

The N-Divider gate-array contains two 5-bit binary counters (A and N), a BCD two-decade rate multiplier, and latches to interface to the microprocessor. The operation of the N and A counters is described in the following paragraphs.

At the beginning of a count cycle, a number is loaded into the A and N counters. The A counter is not at its terminal count, so the output is high, and the mode line (MODE L) is low. This

THEORY OF OPERATION

causes the prescaler to divide by 21 (or 22, TRMODL = low). The mode line stays low for 31-A counts, where A is the programmed number. The mode line goes high, and the prescaler divides by 20 (or 21, TRMODL = low) for 31-N counts.

The total division is:

$$(P+1)*(31-A) + P*((31-N)-(31-A)) \text{ or } P*(31-N) + (31-A)$$

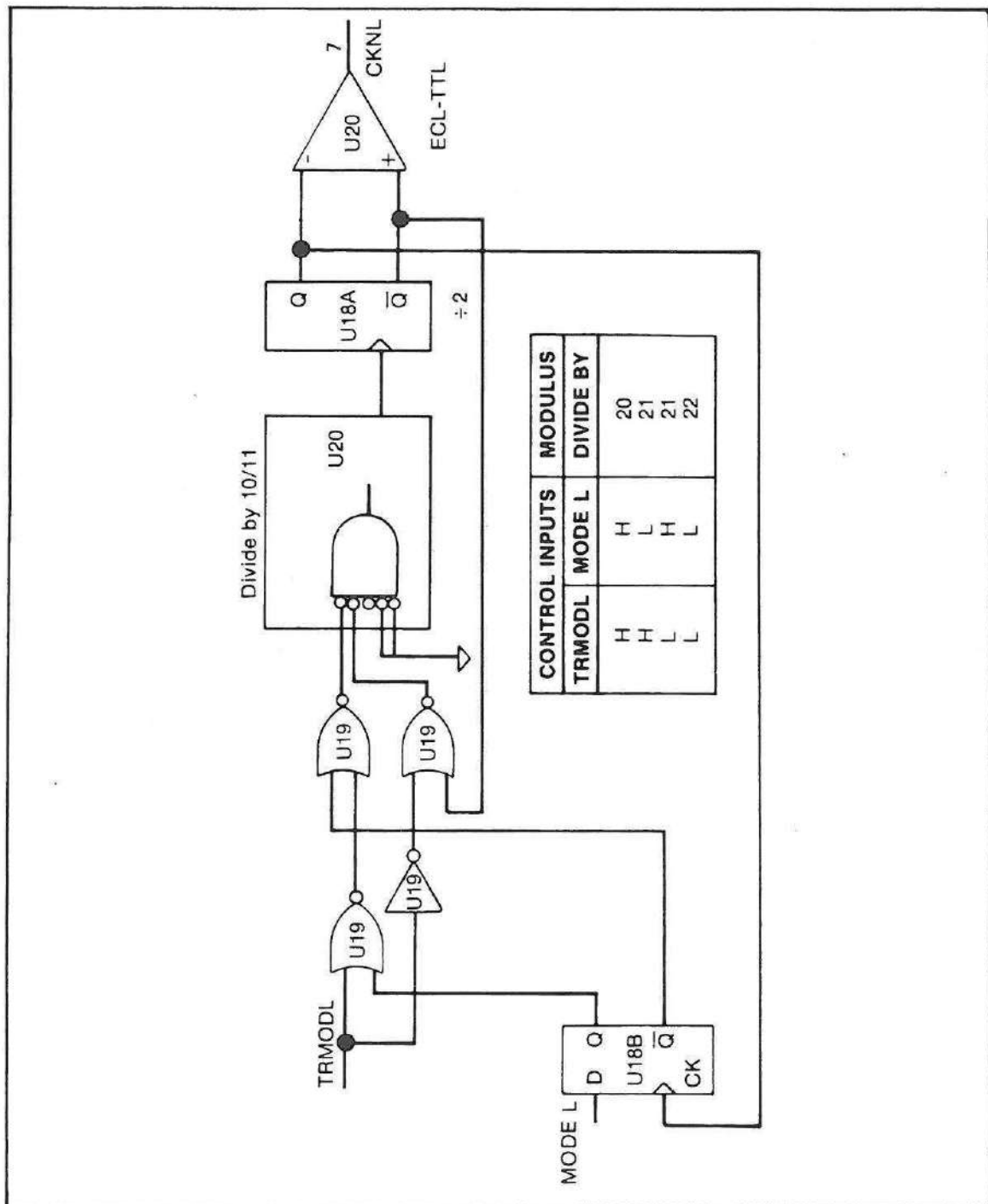


Figure 3-1. Triple-Modulus Prescaler Operation

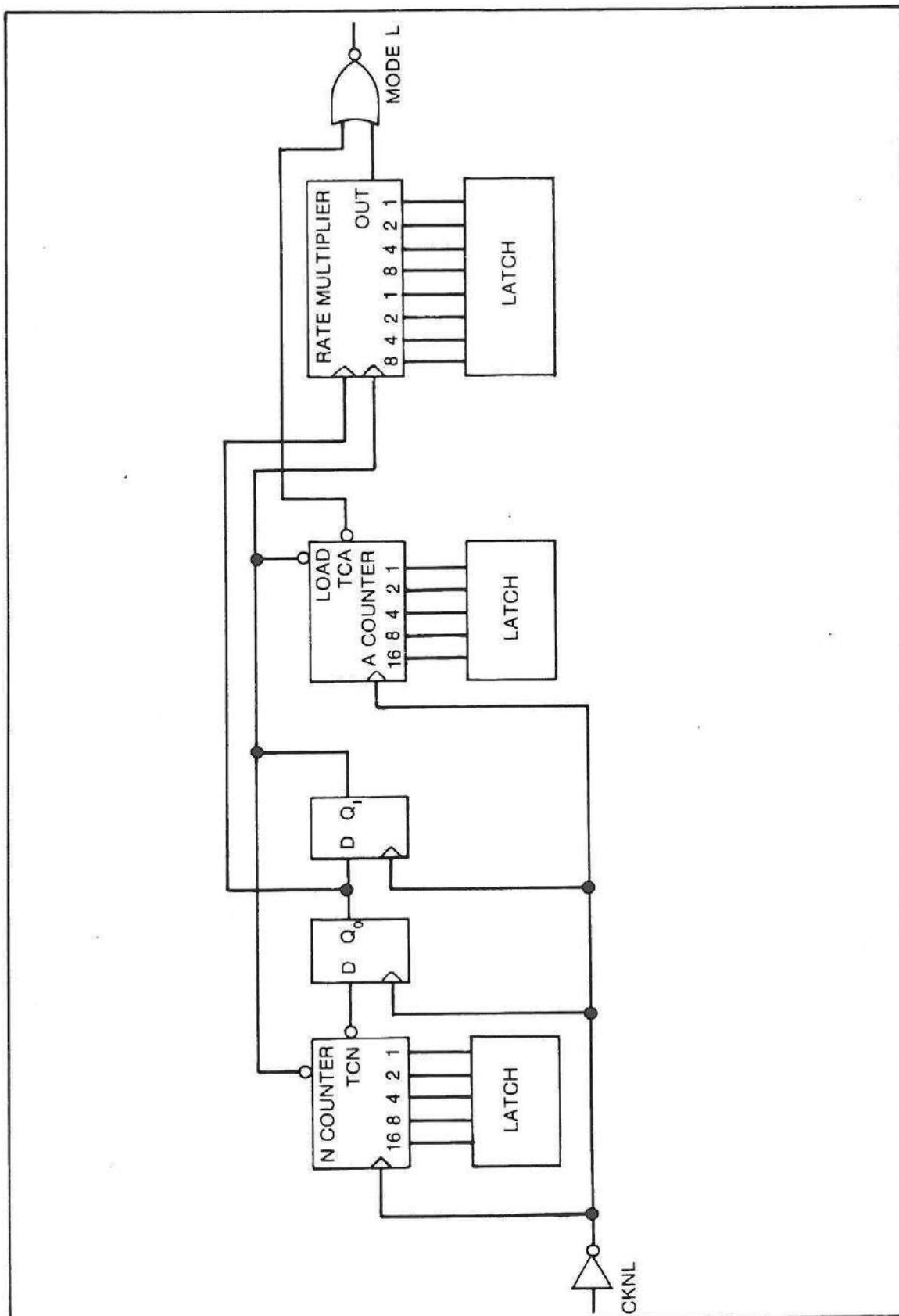


Figure 3-2. N-Divider Operation

On the 31st count, the counters are reinitialized. Figure 3-3 shows the timing for the A-counter programmed to 26 and the N-counter programmed to 18. Only the CKNL and MODE L signals shown in Figure 3-2 are accessible at U17 pin 6 and 22, respectively. Figure 3-3 shows the N-Divider timing diagram.

The N-Divider gate array includes a two-decade rate multiplier that produces the fractional part of the division. The N-Divider gate array produces a pulse train with a programmed number of pulses for a 100-cycle frame of the 1-MHz N-divider output.

The programmed number ranges between zero and 98 in steps of two, corresponding to 20-kHz steps at the mid-band output frequency. The flip-flops in the rate multiplier get set up on count 29, and on count 30 a pulse may or may not be present, depending on the programming of the rate multiplier. This is the shaded pulse in the timing diagram (Figure 3-3).

Irregularly spaced rate-multiplier pulses cause the mode line to go low, and the prescaler divides by $P+1$ at a rate equal to the rate multiplier programming. At a division of 255, the N and A counters are normally programmed to 15, which means the divider is always dividing by 21. Consequently, there is no place to slip in the rate-multiplier pulses.

A 20/21 dual-modulus prescaler will not allow division from 245 to 525 without holes. For example, 252 is 0 frames of 20 and 12 frames of 21. Consequently, there is no place to slip in the rate-multiplier pulses. It is not possible to divide by 253.

By using a triple-modulus prescaler, these problems are solved. Continuing with the previous example, 252 is 12 frames of 21 and 0 frames of 22. The deleter allows the prescaler to divide by 22 at a rate equal to the rate-multiplier frequency. Number 253 is 11 frames of 21 and 1 frame of 22. A software algorithm determines whether to operate in the 20/21 mode (TRMODL = 1) or 21/22 mode (TRMODL = 0).

The frequency at the output of the N-divider gate array is $(F_o - F_s - F_d)/N$; where F_o is the VCO output frequency, F_s is the subsynthesizer frequency, and F_d is the fractional-division frequency.

3-56. Phase Detector

The 1-MHz reference signal from divide-by-10 U58, and the 1-MHz signal from the N-divider U17 are connected to a digital phase-frequency detector (U43, U44, U45). If the N-divider output is greater than the reference frequency, the level at TP38 is high. When the output of the level shifter Q16 is above ground, CR12 is turned off. This allows current from Q19 to flow through CR13 into the integrator, decreasing the voltage at the integrator output U48 pin 6, which then lowers the frequency of the VCO until the reference and the N-divider output are the same frequency.

Similarly, if the N-divider output frequency is below the reference, TP39 is low and the voltage at the output of level shifter Q17 is below ground. This results in turning off CR15 and allowing current from R108 to flow through CR14 out of the integrator. This raises the voltage at the output of the integrator, which raises the VCO frequency. If the phase between the reference and N-Divider output slips more than two cycles in either direction, the corresponding phase-detector output is high or low. This provides twice the integrator current during acquisition as a conventional phase-frequency detector.

R107 provides a small bias current to the integrator to bias the phase detector at approximately 2.5 radians; consequently, the down-pump is normally always on. If the up-pump comes on, indicating an over-modulation condition, the pulses are detected by the one-shot U47, which produces the UNLOCK status that is then sensed by the A2 Controller PCA.

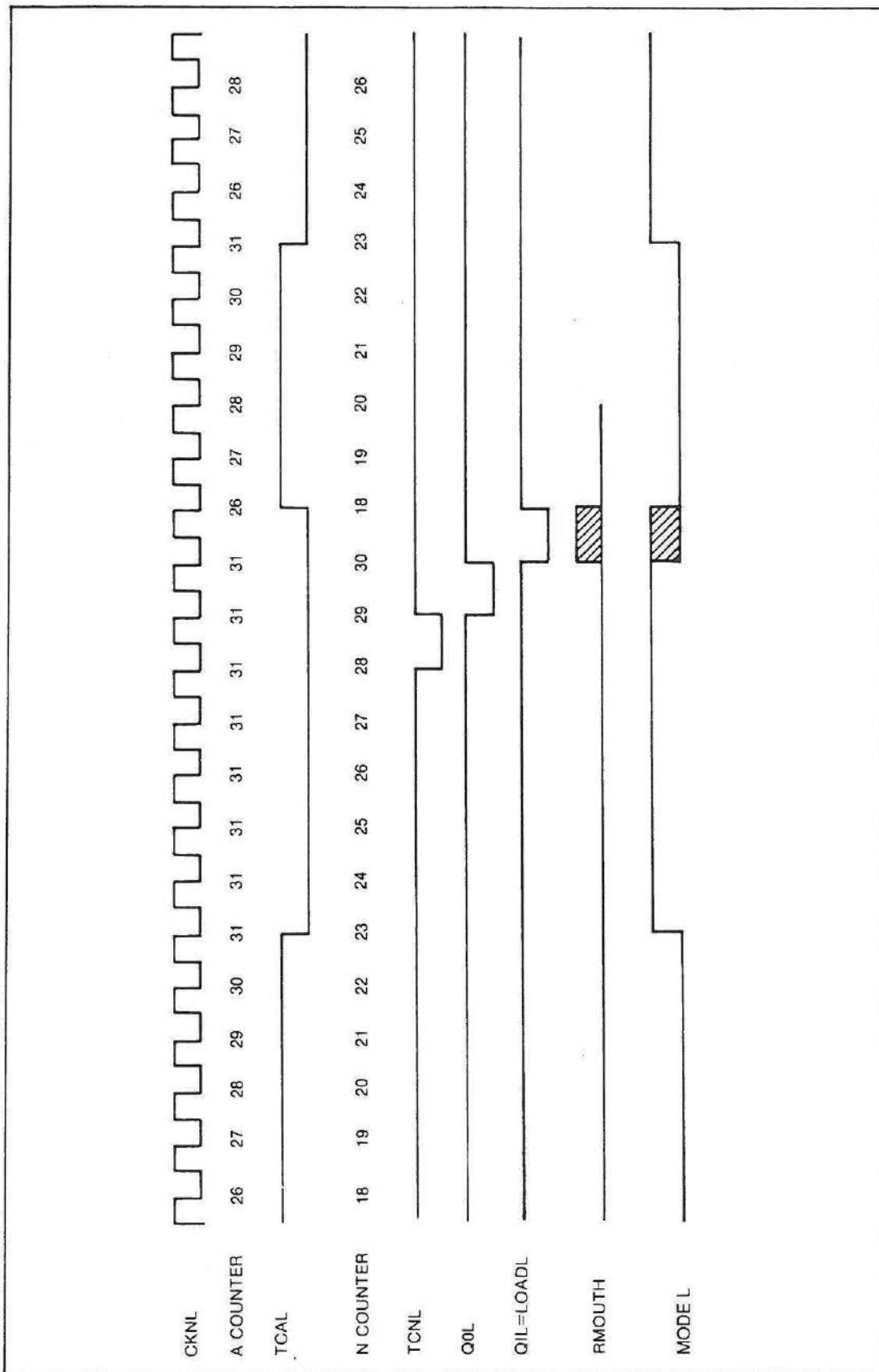


Figure 3-3. N-Divider Timing Diagram

For flat FM response, it is necessary for the PLL bandwidth to be constant at all VCO frequencies. Two factors cause the loop bandwidth to change: the VCO tuning coefficient (K_v) and the divider ratio (N).

During calibration of the VCO, the K_v is measured at many frequencies across the band, and compensation data is stored in the compensation memory. The instrument software uses this data along with N to control the PLL bandwidth in a compensating manner. The PLL bandwidth is controlled by changing the current to the down-pump via the KNDAC, U27, and the voltage-to-current converter, U46, Q18, and Q19.

3-57. Loop Amplifier

The loop amplifier-integrator consists of operational amplifier U48, C118, and R91. Capacitors C121 and C119 filter the 1-MHz reference. The output of the integrator is connected to a multi-pole LC filter (R92, C123, C99, C124, C126, C125, L49, L50, and R93) that attenuates the delete rate (20 and 40 kHz), and reference 1-MHz spurs.

Diodes CR9 and CR10 stabilize the loop during switching. The filter is buffered by the Darlington emitter-follower Q20, which is biased at 10 mA by Q21. Additional lead/lag compensation is provided by R99, R101, and C131. Proper termination for the filter is provided by R93 and Q22. The voltage for the loop amplifier is regulated to approximately +30V by Q15.

Amplifier U49 is a precision clamp to keep the VCO frequency above a minimum value for oscillation and below a maximum above which the N-divider would not divide correctly. The photoisolator U50 detects when the clamp is active, indicating an out-of-lock condition. This signal is ORed with the signal from one-shot U47 and sent to the microprocessor as the UNLOCK status.

3-58. FM PROCESSING

To provide FM accuracy, the FM modulation signal (FMV) from the A6 Output Control PCA is first processed by the KV DAC (U28 and U29) to compensate for the VCO tuning coefficient. The KV DAC setting is proportional to $1/K_v$, where K_v is the tuning coefficient. This correction is stored in the compensation memory on the A2 Controller PCA.

Range switching is provided by resistors R77, R78, R79, and FETs Q10, Q11, and Q12. Comparator U42 converts TTL levels to 0V (on), and -15V (off) required by the FETs. U41C buffers the range switch, and in conjunction with R82, provides an overall FM adjustment. At this point, the audio signal splits into two paths. The path that connects to the integrator, U41A, is for modulation frequencies inside the loop bandwidth.

The path that sums with the VCO control voltage at P102 is for frequencies outside the loop bandwidth. U41D is an active high-pass filter that compensates for the non-ideal integrator and the ac coupling to the VCO tuning port.

The output of U41D is summed with the VCO control voltage via R88 and C117. FET Q13 allows the FM to be turned off. The audio signal is also processed by integrator U41A, R85, R86, and C115. The audio signal is ac coupled into the phase-detector integrator via R89, R90, C116, and FET Q14. (Resistor R90 adjusts the low frequency FM gain). This integrator makes the phase modulation produced at the phase detector appear as FM.

3-59. 800/40 MHz PLL

When the Generator is operated in the HET band, the 800-MHz oscillator is locked to the 10-MHz Reference and provides a local oscillator for the heterodyne circuit on the A6 Output Control PCA. It also provides a 40-MHz signal to the subsynthesizer clock generator.

The 800-MHz VCO is connected to the divide-by-four U61, followed by a divide-by-five U62 and U63, providing 40 MHz to the subsynthesizer clock generator through selector U64. When the Generator is not in the HET band, the 800-MHz oscillator and the first divide-by-four are disabled by turning off Q28 (HET).

The 40-MHz oscillator consisting of U64, L66, and CR24, is selected by U64. The 40-MHz balanced ECL signal from U64 drives the two-phase clock generator. A self-biased gate U65 converts ECL to TTL. U66 divides the 40-MHz signal by four to produce a 10-MHz signal that is compared against the 10-MHz reference in the phase-detector U59 and U65.

Op-amp U60, resistor network Z9, and capacitors C181, C185, C186, and C201 integrate the phase detector pulses to produce a dc control voltage for the 800-MHz VCO and the 40-MHz VCO.

3-60. 800-MHz VCO

The 800-MHz VCO is a low noise, limited range, voltage-controlled oscillator for the 800-MHz PLL. The basic oscillator uses two active devices operating as negative resistance elements, coupled symmetrically to a resonator made up of two varactors and an adjustable capacitor. Each device is followed by an amplifier and isolation pad. This provides two coherent outputs of +5 dBm to the PLL and 0 dBm to the A6 Output Control PCA.

The oscillator transistors Q32 and Q35 are biased at 13 mA by R182 and R191. The voltage at the collectors of Q32 and Q35 is typically +2.5V. The two 6-dB amplifiers Q33 and Q37 are biased so that the voltage at their emitters is about +0.3V, and the voltage at their bases is about +1V with the collectors at +6.5V.

The PLL control voltage from U60 provides the tuning voltage for the dual varactors CR26 and CR27. The adjustable capacitor C206 is set to provide +16V on the varactor to optimize the VCO noise characteristic. The output attenuators consisting of R186, R187, R189, R197, R198, and R200 provide isolation for the outputs. The VCO signal is coupled to the A6 Output Control PCA by a through-the-plate coaxial connector P108 at the 0 dBm level. The other VCO signal is connected to the divider U61 to provide the feedback for the PLL.

3-61. SUBSYNTHESIZER

The subsynthesizer consists of the clock generator U34, U35, Q4, Q5, the gate-array U33, the divide by 500 U15 and U16, and the low-pass filter L11 and L17. Internal to the subsynthesizer gate-array U33 are a divide-by-two, a 3½-decade-rate multiplier, and associated latches.

The balanced 40-MHz ECL clock signal is converted to TTL in Q4 and Q5 and converted to a two-phase 20-MHz clock in U34, U35.

The input frequency to the rate multiplier is 20 MHz. The output frequency can be programmed from zero to 19.995 MHz in 5-kHz steps. This signal is ORed with the other phase of the 20-MHz clock to produce 20 MHz to 39.995 MHz at U33 pin 1. It is also divided by 2 for the output at U33 pin 23, by 10 in U15, and again by 50 in U16 to produce 20 to 39.995 kHz in 5-Hz steps. This TTL signal at TP11 is filtered by L11, L17, and C41, C42, C48, C50, and C51. Op-amp U10 forms an active quadrature generator and the output pins 14 and 8 are offset by 90 degrees. These two signals are the 20- to 40-kHz inputs for the Main PLL single-sideband mixer.

3-62. A5 VCO PCA

The A5 VCO PCA is the heart of the main PLL. It produces the signal that is further processed to become the Generator output. The A5 VCO PCA is located in a bottom side compartment of the module section.

THEORY OF OPERATION

The A5 VCO PCA tunes over a frequency range of 490 to 1050 MHz in four bands, programmed by binary control signals Q and H/L, as follows:

Band	Frequency Range (MHz)	Q	H/L
L1	512 - 639.99999	0	0
L2	640 - 729.99999	1	0
H1	730 - 889.99999	1	1
H2	890 - 1050	0	1

The control voltage varies from about +2V to +16V in each of the four bands, and varies approximately linearly with frequency in each band. The A5 VCO PCA contains two oscillator circuits of similar design, but with different element values and transmission line lengths, to cover 490 to 730 MHz (L bands oscillator) and 730 to 1050 MHz (H bands oscillator).

Each oscillator uses a transistor (Q1, Q2) configured as a negative resistance device. The voltage at the collector of Q1 is about 7.2V at 1050 MHz, and the voltage at the collector of Q2 is about 8.1V at 700 MHz. Each oscillator transistor is coupled loosely to a resonator that consists of a microstrip transmission line in series with a varactor diode (CR1, CR2) and a switchable value capacitor circuit (C3, C4, C7, C8, and associated components).

Each oscillator operates in two bands, selected by switching the capacitance value from varactor cathode to ground. For the H bands oscillator, C3 is selected for H2 band operation, while C3 in parallel with C4 is selected for H1 band. For the L bands oscillator, C7 is selected for L2 band operation, while C7 in parallel with C8 is selected for L1 band. The PLL control voltage from the A4 Synthesizer PCA at P202 provides the tuning voltage for varactor diodes CR1 and CR2.

The +13-dBm nominal signal from each oscillator is applied to a 12-dB attenuator that provides isolation (R8-R10, R14-R16), and then to a low-pass filter that attenuates harmonics to -15 dBc nominal (C16, C17, C24, C25). A PIN diode switch selects the signal from the ON oscillator, while the other oscillator is disabled by shorting the resonator to ground with a PIN diode switch (CR3, CR4, CR9, CR10). The signal is then applied to a signal-splitter/attenuator that provides further isolation (R18, R19, R20, R23, R24).

One signal-splitter output goes to 11-dB amplifier U1. This amplifier provides further isolation and also boosts the signal to about +3 dBm. The boosted signal is connected to the A6 Output Control PCA by a plug-in capacitor, C1. This capacitor allows either the A5 VCO PCA or the A6 Output Control PCA to be removed independently from the module section without the use of a soldering iron.

The other signal-splitter output goes to series-connected 11-dB amplifiers U2 and U3. Two amplifiers are required for adequate isolation from the A4 Synthesizer PCA. The +3-dBm output signal from U3 is connected to the A4 Synthesizer PCA by a through-the-plate coaxial connector P204.

Amplifiers U1, U2, and U3 are biased at about +1.5V at their inputs and +4.5V at their outputs.

The binary band control signals Q and H/L are connected from the A6 Output Control PCA to the A5 VCO PCA with a cable assembly W1 that plugs onto connector pins on the A6 Output Control PCA. The control signals are level shifted by dual operational amplifier U4, which acts as a comparator.

3-63. Output Assembly (A6 and A7)

The A6 Output Control PCA and A7 Output PCA are intimately associated. The following paragraphs describe the circuitry of the two PCAs as a single assembly (Output Assembly). The Output Assembly accepts RF signals from the A4 Synthesizer PCA and the A5 VCO PCA and command signals from the A2 Controller PCA. The Output Assembly provides a 0.1- to 2100-MHz RF signal to the A8 Attenuator/RPP Assembly.

The Output Assembly reduces harmonic distortion components in the RF signal, controls RF signal amplitude, and introduces AM and Pulse. Also, the Output Assembly generates the low (heterodyne) frequency band from 0.1 to 245 MHz through mixing, and the X2 frequency band from 1050 to 2100 MHz by doubling the high band. The Output Assembly also generates a modulation signal to provide internal AM, FM, ϕ M, and Pulse, and provides a digital interconnect path between the A2 Controller and the A4 Synthesizer PCAs.

3-64. RF PATH

The RF path begins with the two RF signals from the VCO and the Synthesizer assemblies. The SPDT (single-pole double-throw) bandswitch circuit selects between the 512- to 1050-MHz signal at P6 and the 245- to 512-MHz signal at P7. The selected signal is applied to buffer amplifier U38 and U39.

The 245- to 512-MHz signal directly generates the 245- to 512-MHz mid-band output signal. The 512- to 1050-MHz signal generates the high-band output signal directly and the 0.1- to 245-MHz low-band output signal by mixing with an 800-MHz LO signal. The 1050- to 2100-MHz X2 frequency band is generated from the high band with a frequency doubler.

The buffer amplifier consists of an input attenuator followed by monolithic RF amplifiers U38 and U39 and has 7-dB gain. Following the buffer amplifier is a printed low-pass filter and a X2-band select switch consisting of PIN diodes CR7 through CR10. This switch either routes the signal to a low-pass filter circuit for frequencies from 245 to 1050 MHz, or to a doubler circuit for 1050 to 2100 MHz.

For operation in the 245- to 1050-MHz frequency band, the RF signal is routed through CR8 to a cascade of low-pass filters. These filter circuits consist of combinations of discrete components and printed traces that suppress harmonics in the RF output signal. The first filter section is switched into the RF path via PIN diodes CR12 through CR16 by asserting MIDH when the Generator is operated in the mid band (245 to 512 MHz). PIN diodes CR17 through CR19 select capacitors C16, C18, and C20 whenever HAOCTH is asserted to change the section cutoff frequency from 512 to 350 MHz. The third section provides harmonic filtering for the two higher bands, 512 to 730 MHz, and 730 to 1050 MHz. PIN diodes CR20 through CR22 select capacitors C24, C26, and C28 to change the cutoff frequency from 1050 to 730 MHz whenever HAOCTH is not asserted.

For operation in the X2 band from 1050 to 2100 MHz, the RF signal is directed through CR7 to the frequency doubler and associated filters on the A6 Output Control PCA. The control signal X2L is asserted at -15V. A signal of approximately +9 dBm (± 2 dB) at the input of U1 becomes +14 dBm (± 1 dB) at the input to balance T1. U1 and U2 operate as a limiter to control the amplitude and limit signal variations to this point. T1 generates a balanced drive for rectifier CR26 with a resulting second harmonic of +4 dBm (± 2 dB) at the output of C134. In addition, a strong fundamental component and third harmonic are also present.

The remaining circuitry splits the signal into four frequency bands: 1050 to 1250 MHz, 1250 to 1450 MHz, 1450 to 1750 MHz, and 1750 to 2100 MHz. The circuitry then acts as a bandpass filter for the signal, leaving only the desired second harmonic. Filter 1 is the topmost filter on

the 9-layer stripline quad filter pca. When CR27 is on, CR29 is on, and CR28 is off, a signal is delivered through this filter. Logic signal X2FL1L is at -15V to produce this result. Meanwhile, X2FL2L, X2FL3L, X2FL4L are at +15V to turn off the other three parallel paths. The other three paths operate in a similar manner. Operation can be observed by switching frequency and observing voltages at the coil end of appropriate resistors. (See Table 3-1.)

Table 3-1. Bandpass Logic States

BAND/MHz	R47	R48	R49	R50	R51	R52	R53	R54
1050 to 1250	-0.8V	-0.8V	+14V	+0.8V	+0.8V	+0.8V	+0.8V	+0.8V
1250 to 1450	+14V	+0.8V	-1.6V	-0.8V	+0.8V	+0.8V	+0.8V	+0.8V
1450 to 1750	+14V	+0.8V	+14V	+0.8V	-1.6V	-0.8V	+0.8V	+0.8V
1750 to 2100	+14V	+0.8V	+14V	+0.8V	+0.8V	+0.8V	-1.6V	-0.8V

The output signal from the switched filters is amplified by U8 (8 dB gain) to a level of 3 dBm (± 2.5 dB) at the input to the RF modulator. CR43 and CR44 serve as a switch to turn off this signal when 0.245 to 1.05 GHz is being sent to the modulator. Likewise, CR45 and CR46 exclude the lower frequencies when 1.05 to 2.1 GHz is required.

The amplitude modulator on the A7 Output PCA consists of PIN diodes CR1 through CR7 and associated components, and receives the RF signal from the A6 Output Control PCA through W1. The modulator is a voltage-controlled variable attenuator that provides AM and output level control. Modulator control voltage is determined by the leveling-loop circuitry. The leveling loop is described later in this section.

U1, U2, U3, Q2, and associated components follow the modulator in the signal path and form a four-stage, 25-dB gain, 245- to 2100-MHz amplifier. This amplifier drives a 3-dB power splitter that consists of resistors R28 through R31 and associated printed transmission lines.

One power-splitter output drives the leveling-loop detector diode CR8. The other output goes to a 2-dB gain buffer amplifier that includes Q4 and associated components. The HET band switch follows the buffer amp and consists of PIN diodes CR10 through CR15 and biasing components. In the 245-to 2100-MHz position, the signal passes through diodes CR11 through CR14 to the pulse modulator, U5 through U7. The output amplifier follows the pulse modulator and consists of FET Q9 and associated components. This low distortion amplifier has 9-dB gain and output capability of 19 dBm.

For low-band operation (0.1 to 245 MHz), the signal from the power splitter is routed through CR10 and W2 to the HET band circuitry on the A6 Output Control PCA. The RF signal first passes through an adjustable attenuator, R61 through R66, and then to the RF port of U3 (a double-balanced mixer). The signal frequency at the mixer RF port varies from 800.1 to 1045 MHz. The 800-MHz local oscillator (LO) signal for the mixer comes from the A4 Synthesizer PCA through P8 and is amplified by Q37. This fixed-tuned amplifier has 13 dB of gain and provides a 10-dBm signal at the mixer LO port.

The mixer 0.1- to 245-MHz output signal is passed through a diplexing low-pass filter (C194 through C205, R59, R60) that suppresses unwanted mixer spurious products while maintaining a 50-ohm load at the mixer IF port. The filtered IF signal is amplified by a three-stage IF amplifier Q34, Q35, Q36 and associated components.

The IF amplifier gain increases with frequency and is nominally 35 dB at 0.1 MHz and 37 dB at 245 MHz. This gain characteristic compensates for the increasing loss with frequency of the

mixer and the diplexing low-pass filter. The output of the IF amplifier passes through W3 to a 245-MHz low-pass filter on the A7 Output PCA (C53, C54, C55 and printed inductors L14 and L15). Following this filter, the signal passes through PIN diode CR15, the pulse modulator, and the output amplifier. The +15V power supply for the LO and IF amplifiers is switched off by Q20 when the instrument is operating in the 245- to 2100-MHz bands to avoid introducing spurious products in the instrument output.

3-65. LEVELING LOOP

The leveling loop accepts the unlevelled 245- to 2100-MHz signal from the switchable low-pass filters and doubler-filter circuits on the A6 Output Control PCA. The leveling loop generates a leveled signal at the power splitter output on the A7 Output PCA that feeds the buffer amplifier. The leveled signal is proportional to the leveling loop control voltage that is generated by the level-control circuit. The signal amplitude at the other output of the power splitter is detected by a Schottky detector diode, CR8.

The Schottky detector diode CR8 generates a temperature-dependent dc voltage. This is a non-linear function of the applied RF voltage, thus temperature compensation and linearization are necessary. The detector diode signal is low-pass filtered by L10 and C35, and is offset by the voltage across temperature-compensating diode CR9. Q25, Q26, and associated components on the A6 Output Control PCA form a current source circuit that provides bias current for CR9 and CR8.

The offset detector-diode voltage at U29-A pin 5 on the A7 Output Control PCA is linearized by amplifier U29-A and its associated feedback components. Potentiometer R113 provides detector linearity adjustment. Thus, the voltage at U29-A pin 7 is proportional to the RF voltage at detector-diode CR8.

This voltage is divided and applied to the loop-integrator amplifier at U29-B pin 2. This amplifier drives the modulator through a modulator-linearizer circuit and maintains the voltage level (via the ALC loop) at U29-B pin 2 equal to that on pin 3. The voltage on pin 3 is a function of the leveling loop control voltage applied to R121 (available on TP7). R121, R122, CR64, and CR57 form an additional detector-linearizing network that is active for low RF levels. Amplitude modulation is achieved by summing an appropriately scaled modulation signal with the dc leveling loop control voltage applied to R121.

The amplitude modulator on the A7 Output PCA consists of PIN diodes CR1 through CR7 and associated components. Attenuation through the modulator is a function of bias current through the PIN diodes. This current is provided by the modulator-linearizer circuit on the A6 Output Control PCA. U31 and associated components provide modulator series diode current, while U32 and associated components provide shunt diode current.

Modulator attenuation is approximately proportional to the modulator control voltage on TP8. Proportionality is required to maintain constant leveling loop bandwidth as modulator attenuation varies. Minimum attenuation is obtained with a modulator control voltage of 10V, while maximum attenuation is obtained with 0V.

Comparator U15-D and associated components form an unlevelled-indicator circuit. The comparator senses the modulator control voltage at U29-B pin 1. This voltage is normally less than +11V, and the comparator output is high. If the modulator control voltage exceeds +11V, the modulator attenuation is at a minimum, and the leveling loop becomes inoperative (unleveled). This condition could be due to a fault or some abnormal operation such as over modulation. In this case, the comparator output (UNLVLL) goes low. The A2 Controller PCA senses this low and causes the front panel UNCAL indicator to flash and displays an unlevelled status if interrogated.

3-66. LEVEL CONTROL

The instrument output level is set by the level-control circuit. Inputs to this audio signal processing circuit are the internal and external modulation signals, a dc reference voltage, and the digital control commands. The circuit output is the leveling-loop control voltage that provides vernier level control of the Generator output. Digitally encoded level, modulation depth, and temperature-compensation information are provided by the A2 Controller PCA.

Selection of the internal or external dc- or ac-coupled modulating signal, or no modulation, is made by analog switches U27-A, U27-B, U27-C, and Op-amp U28-B. The selected buffered modulation signal at U28-B pin 7 is applied to pin 4 of U19, a dual 8-bit DAC. U19, with U20-A, acts as a digitally programmed variable attenuator and controls AM depth.

Binary AM depth control information from the A2 Controller PCA is applied to DAC U19. The output at U20-A pin 7 is the modulation signal scaled to the programmed AM depth. This signal is summed by Op-amp U20-B with a dc reference current provided by CR50. The output at U20-B pin 8 is called the 1+AM signal. This signal provides the desired AM depth when scaled by the level DAC and applied to the leveling loop. AM depth adjustment is provided by potentiometer R97 and AM DAC offset by R46.

The instrument RF output amplitude is temperature compensated in a frequency-dependent manner as follows. The 1+AM signal is applied to pin 18 of dual 8-bit DAC U19, the DAC B reference input. The DAC output, at U20-C pin 1, is the 1+AM signal attenuated by an RF frequency-dependent factor provided by the A2 Controller PCA using constants stored in the Generator firmware. This voltage is applied to a resistor/thermistor network that includes R77, R78, R80, and RT79.

The network output is the 1+AM signal attenuated by an RF frequency and temperature-dependent factor, and is applied to summing Op-amp U20-D. The 1+AM signal is also applied to this summing amplifier. Thus, the voltage at U20-D pin 14 is the temperature-compensated and scaled 1+AM signal.

This signal is applied to the reference input of level DAC U21. This 14-bit DAC, with Op-amp U35-A, controls the Output Assembly RF output amplitude. The DAC output voltage, U35-A pin 7, is the temperature-compensated 1+AM signal multiplied by a factor proportional to the 14-bit level control number provided by the A2 Controller PCA. This voltage is the leveling loop control voltage. The Generator RF output level adjustment is provided by potentiometer R82, and DAC offset voltage adjustment is provided by potentiometer R86.

3-67. MODULATION OSCILLATOR

The modulation oscillator generates a leveled sine wave of 400 Hz or 1 kHz and is the modulation source for the internal AM, FM, ϕ M, and pulse functions. The oscillator is a level-controlled Wien-Bridge type and consists of Op-amps U22-A, U22-B, and associated components. Frequency is determined by the series RC time constant of the components between pins 5 and 7 of U22-B and by the parallel RC time constant of the components from U22 pin 5 to ground. The modulation-frequency control line, MF400L, originating at latch U13, selects either 400-Hz or 1-kHz operation, and is selected by switching resistors with JFETs Q22 and Q23.

The amplitude of oscillation is controlled by an ALC loop that varies the resistance on U22-B pin 6 to ground. This resistance, comprised of R107 and the drain resistance of Q24, is nominally 2 k Ω . The oscillator signal amplitude is sensed by rectifier CR52. The average current through CR52 is made equal to the reference current in R100 by integrator-amplifier U22-A. Level adjustment is set by potentiometer R99. Temperature compensation is provided by R101, R102, and CR51.

3-68. FM and ϕ M DEVIATION CONTROL

The FM and ϕ M modulation signal source and deviation control circuits are on the Output Assembly. Analog switches U26-A, U26-C, and Op-amp U28-A select the internal or external FM signal or no modulation. ϕ M is obtained by differentiating the modulating signal and selecting either external or internal modulation with analog switches U26-B and U26-D. The selected and buffered modulating signal at U28-A pin 1 is applied to FM DAC U25. This DAC provides fine control of the FM deviation. (The coarse control FM circuitry is part of the A4 Synthesizer PCA.) The output of the DAC, at U35-B pin 1, is the modulation signal multiplied by a factor proportional to the 12-bit FM deviation control provided by the A2 Controller PCA.

For output frequencies below 245 MHz, and from 512 to 1050 MHz, the FM DAC setting is halved to account for the effective frequency doubling that occurs on these bands. For frequencies above 1050 MHz, the FM DAC setting is quartered to account for the effective frequency quadrupling in this band.

3-69. PULSE MODULATION CONTROL

External modulation is provided by the user controlling the voltage at the external modulation input connector. This input is one input to comparator U33. The reference input, composed of R147 and R148, is held at 1V. The connector input is held high by R144, R145, and R146 at 1.2V. This input can be lowered with TTL, a resistor less than 150 ohms, a pulse generator, or even a sine-wave generator. Lowering this input brings the output of the comparator (U33A) high. If EXTPUH is high (pulse modulation on), then U34A output is low, which brings U34C high. U4A converts this to a differential drive with the base of Q7 and Q8 high, and the base of Q5 and Q6 low. Level shifting resistors R49 through R52 translate this into 0V at pin A, and -6V at pin B for the "off" switch condition.

For internal pulse, the comparator U33B is driven by the internal oscillator of the instrument. This signal is combined with external pulse to produce the final drive logic.

3-70. A8 Attenuator/RPP Assembly

The A8 Attenuator/RPP Assembly consists of an A8A1 Relay Driver/RPP PCA and an A8A2 Attenuator/RPP PCA. The A8 Attenuator/RPP Assembly is enclosed in a metal housing mounted on the top side of the module section.

The A8 Attenuator/RPP Assembly, controlled by the microprocessor, provides coarse control of the Generator output level. The high-level signal from the Output Assembly (A6 and A7) is applied to the A8 Attenuator/RPP Assembly which provides 0 to 138 dB of attenuation (in 6-dB steps) to this signal before going to the Generator RF OUTPUT connector.

Compensation data for the attenuator in each Generator is stored in the compensation memory located on the A2 Controller PCA. The instrument program uses this data to correct for the combined deviations of the attenuator sections in use. For more details on level correction, refer to the paragraphs on Amplitude Control.

The A8 Attenuator/RPP Assembly provides an attenuation range from 0 to 138 dB in 6-dB steps, and consists of seven independently cascaded 50-ohm attenuation sections, a 6-dB, a 12-dB, and five 24-dB sections. Each section consists of a DPDT relay and a pi attenuator pad.

One relay position (when power is applied to the relay), provides a straight path for the RF signal. The other position (no power applied to the relay), inserts the attenuator pad into the RF signal path. All seven relays are inside individual shielded compartments in the attenuator housing.

The control of the attenuator relays is latched via U27, the open-collector drivers U30 and U31 on the A2 Controller PCA, and transistor drivers on the A8A1 Relay Driver/RPP PCA. For calibration and troubleshooting purposes, special functions 83 through 86 allow the direct selection of four of the five 24-dB attenuators. The other 24-dB attenuator is selected by programming the appropriate level (-12 dBm).

Coupling capacitor C8 protects against dc or low-frequency power. The diode limiter, consisting of CR2 and CR3, provides protection against medium RF power levels and short-term (fast acting) protection against high RF power levels. Long-term (latched) protection is provided by relay K8 whenever the reverse RF power exceeds a preset level.

RF power detected by CR1 is compared with the preset voltage in one section of comparator U1. When the detected voltage exceeds the set value, the output of U1 pin 14 goes negative, turning off Q8 and Q9. This deactivates K8 to the protect position. In the protect position, the Generator output is disconnected from the output connector.

CR11 and R34 form a latching network so that K8 remains in the protect position until the RF output is reset by an RF ON entry. The output of the comparator is buffered and sent as RPTRPL to the A2 Controller PCA for processing. The A2 Controller PCA annunciates the RPP trip condition by flashing the UNCAL and RF OFF indicators.

3-71. A9 Power Supply PCA

The bridge rectifiers in the power supply are used in either a bridge or full-wave center-tapped configuration with capacitor input filters. Table 3-2 lists the rectifier configurations as well as the component designations for the various supplies.

The two +15V, the -15V, and the +5V supplies use conventional three-terminal IC regulators with internal current-limiting and temperature protection. All three +15V regulators have reverse-voltage protection diodes CR3, CR4, and CR8.

The +37V regulator voltage is adjustable via R3. A +6.2V supply is developed from the +37V supply through resistor R4 and zener diode CR7. The +6.2V supply is then applied to the center tap of the 6V ac filament supply. This provides grid bias for the front panel displays. All regulators (except +37V) have their common reference terminals brought out to an external ground point (P2) on the module section to reduce power supply ripple.

Triac U6 is a voltage surge protector to protect against line voltage surges as well as overvoltage in case of a wrong setting of the selector switch.

Switch S1 is the REF INT/EXT reference selection switch and is not functionally part of the power supply.

Table 3-2. Power Supply Rectifier Configurations

SUPPLY	RECT.	CONFIG.	CAP	REGULATOR	REMARKS
+37	CR6	Bridge	C11	A9 U4	Adjustable
+15	CR2	CT/FW	C2	" U5	Fixed
+15	CR2	CT/FW	C2	" U1	Fixed
-15	CR2	CT/FW	C5	" U2	Fixed
+5	CR5	CT/FW	C8	A3 U3	Fixed
+18	CR1	Bridge	C1		Fixed
FIL	6V ac with center-tap biased at 6.2V dc.				Unregulated relay supply